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Rev	Date	Changes
X1	27/11/2017	First Release
X2	14/12/2017	Updated Review Comments.
A	20/12/2017	A085 Release
AX1	28/02/2018	R3548 Added, BOM Update
B	01/03/2018	A085 Release

XFRWY-LS1012A-PA

REV - B

All information is subject to change without notice.
No warranty, expressed or applied, is made as to the accuracy of the information contained herein. This schematic is provided for reference purposes only.
Contact your NXP representative to obtain the latest information on this product.

VAR1

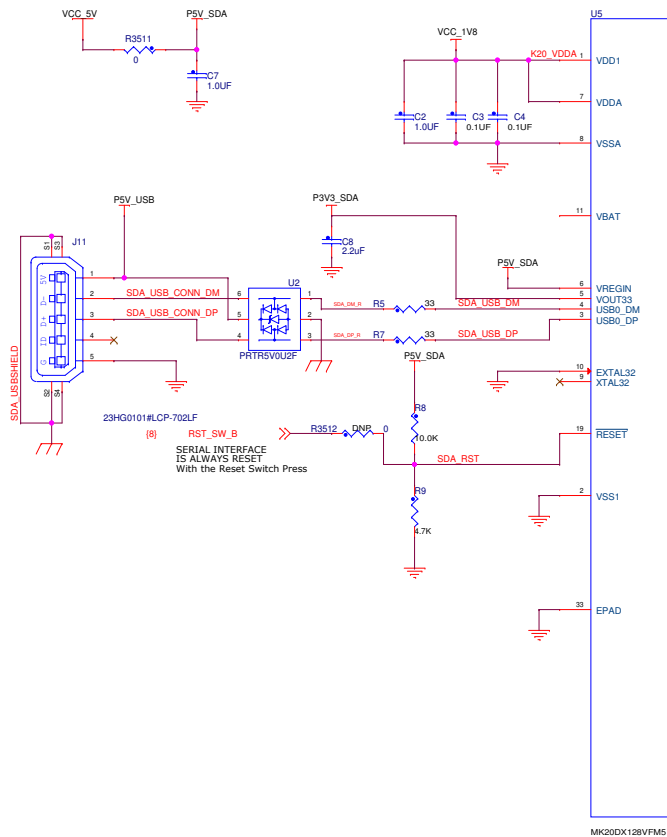


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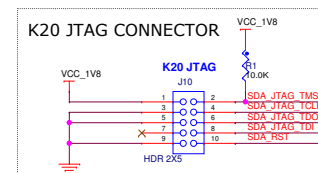
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Page Title: 01_TABLE OF CONTENTS

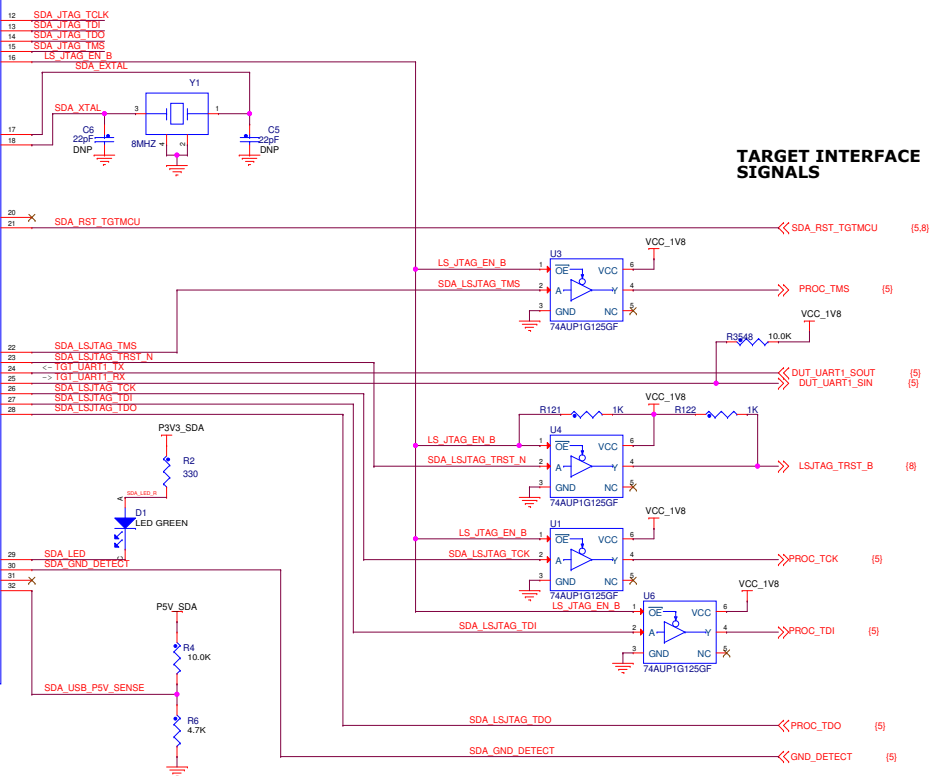
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CMSIS-DAP INTERFACE



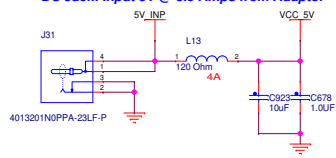
TARGET INTERFACE SIGNALS



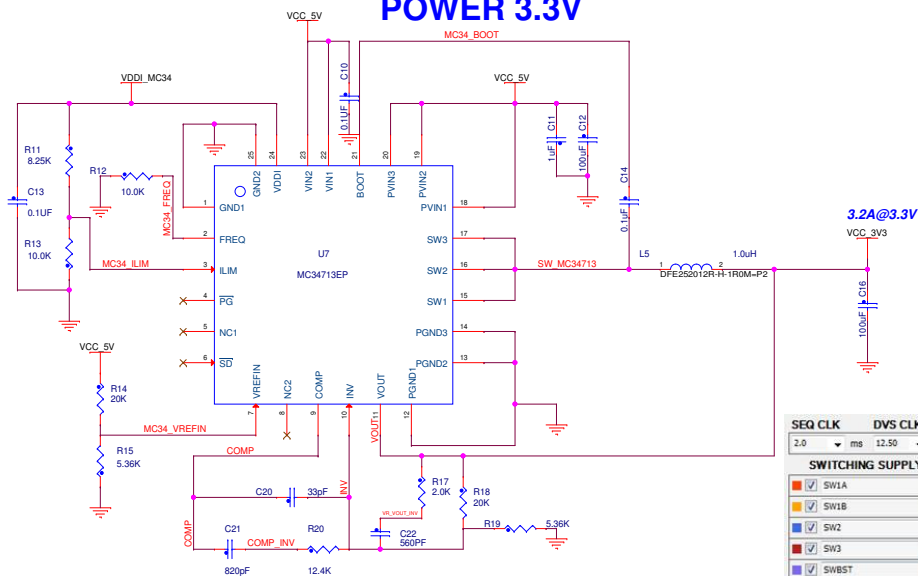
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POWER IN

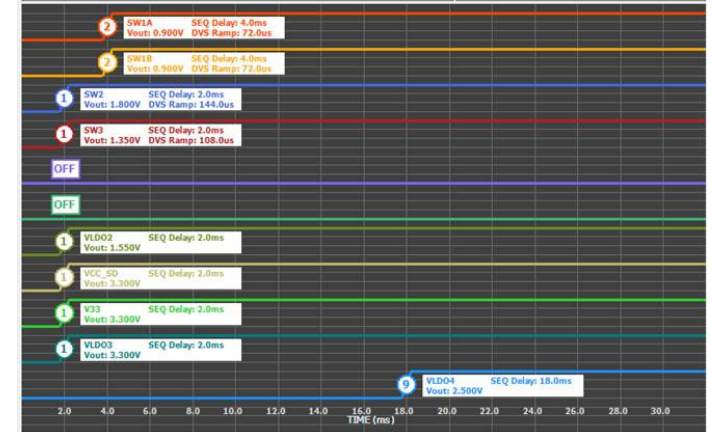
DC Jack: Input 5V @ 3.5 Amps from Adaptor



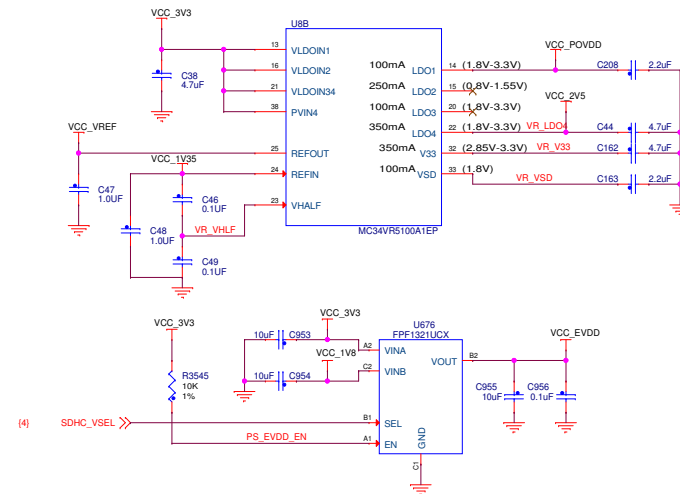
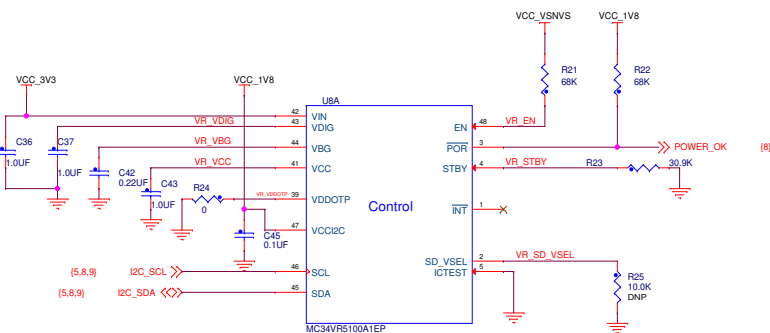
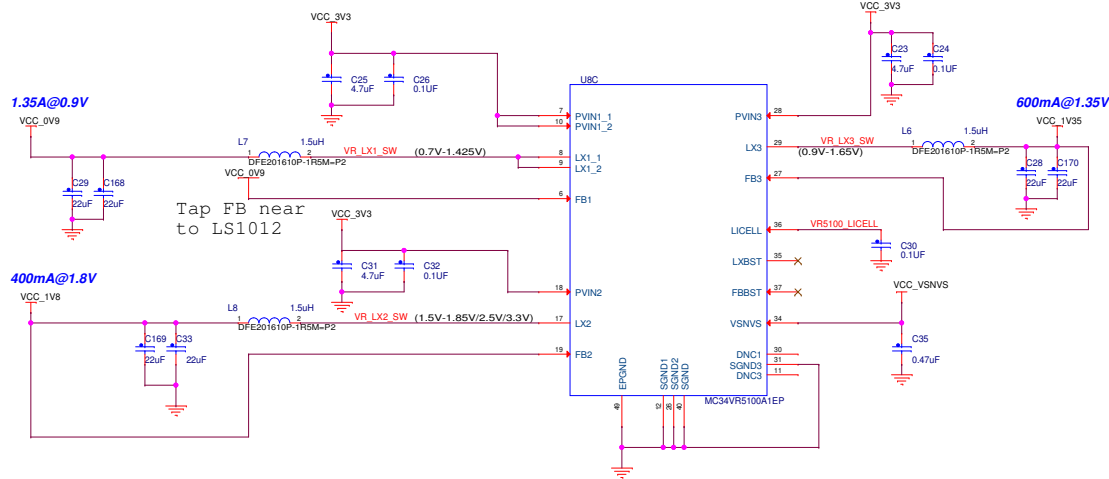
POWER 3.3V



SEQ CLK		DVS CLK		PWORN CFG		PGOOD		I2C		LINEAR SUPPLY		VOUT		SEQ	
2.0	ms	12.50	mV/us	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	0x08	<input type="checkbox"/>	<input checked="" type="checkbox"/>	VDD01	1.800	V	<input type="checkbox"/>	<input type="checkbox"/>
SWITCHING SUPPLY															
<input checked="" type="checkbox"/>	SW1A	0.900	V	2	2.0	MHz	A+B SINGLE PHASE								
<input checked="" type="checkbox"/>	SW1B	0.900	V	2	2.0	MHz	A+B SINGLE PHASE								
<input checked="" type="checkbox"/>	SW2	1.800	V	1	2.0	MHz	Range High: <input type="checkbox"/>								
<input checked="" type="checkbox"/>	SW3	1.350	V	1	2.0	MHz									
<input checked="" type="checkbox"/>	SWBST	5.000	V	OFF											
CONFIG															
<input checked="" type="checkbox"/>	VCC_5D	3.300	V	1											
<input checked="" type="checkbox"/>	VCC3	3.300	V	1											
<input checked="" type="checkbox"/>	VDD03	3.300	V	1											
<input checked="" type="checkbox"/>	VDD04	2.500	V	9											
	VSNV5	3.000	V												

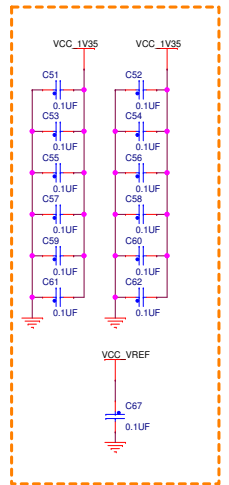
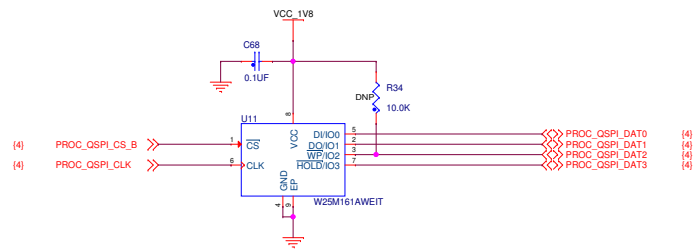
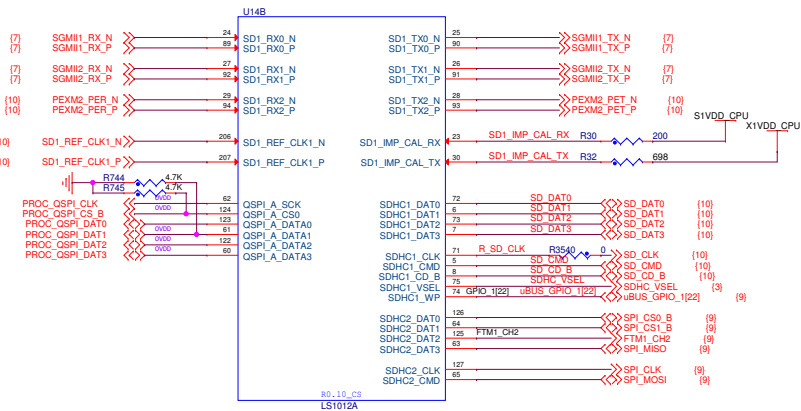


VR5100

**VAR1**

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03 POWER				
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QSPI FLASH (16MBIT NOR + 1GBIT NAND)



[illegible]

NOTE:
**Place R & C as filters near to VCC pin and use thick trace.

VCC_1V8

R749 0

VCC_1V8 OSC25M

C203 0.1uF

Y13

1 VCC

2 GND

3 OUT

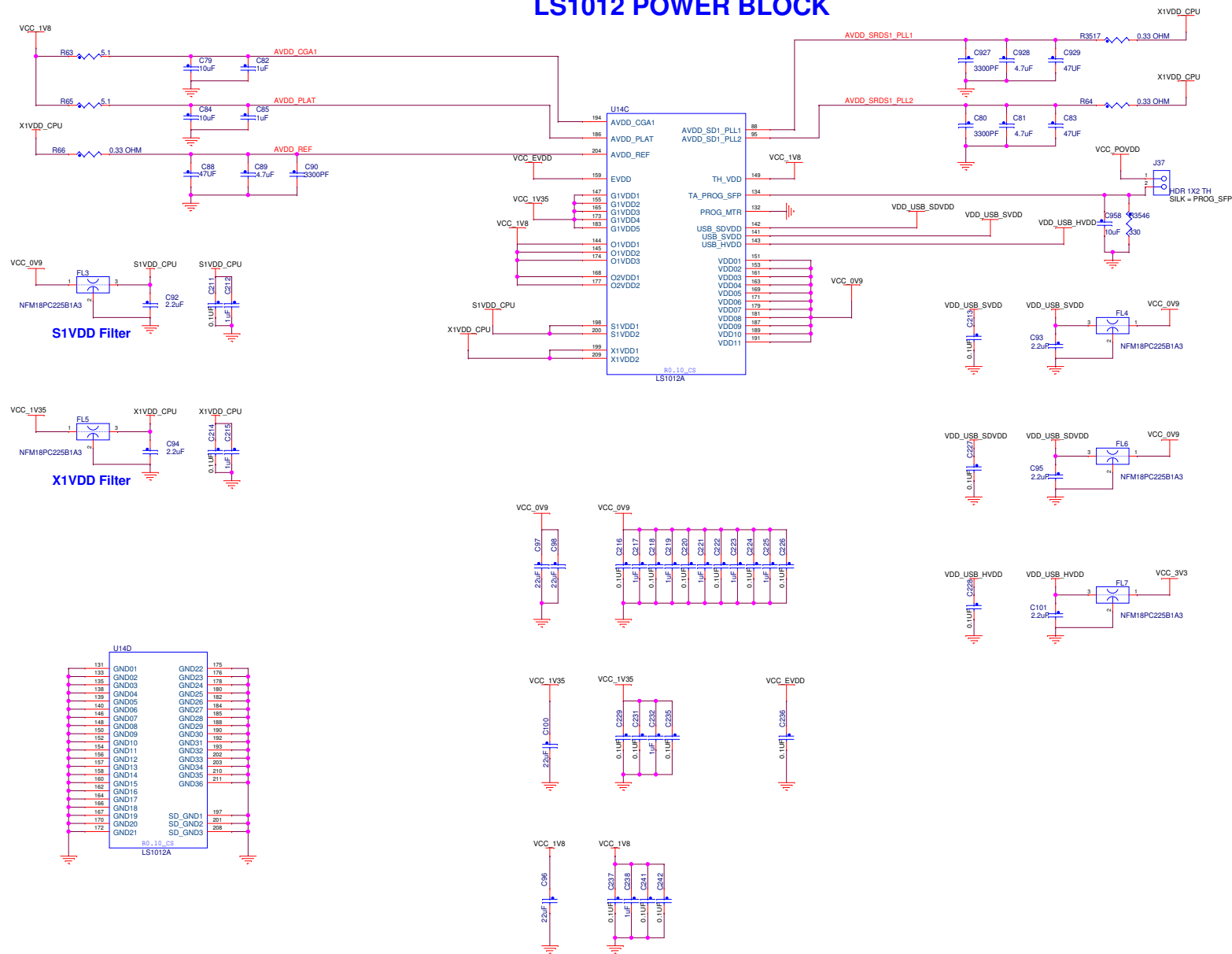
4 VCC

R742

DUT_EXTAL

30MHz

LS1012 POWER BLOCK

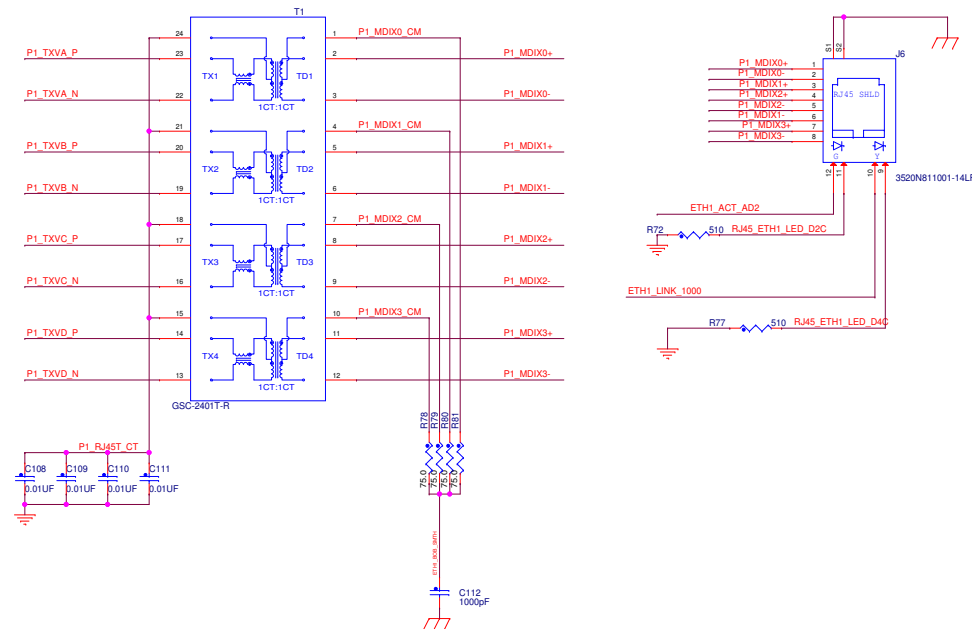
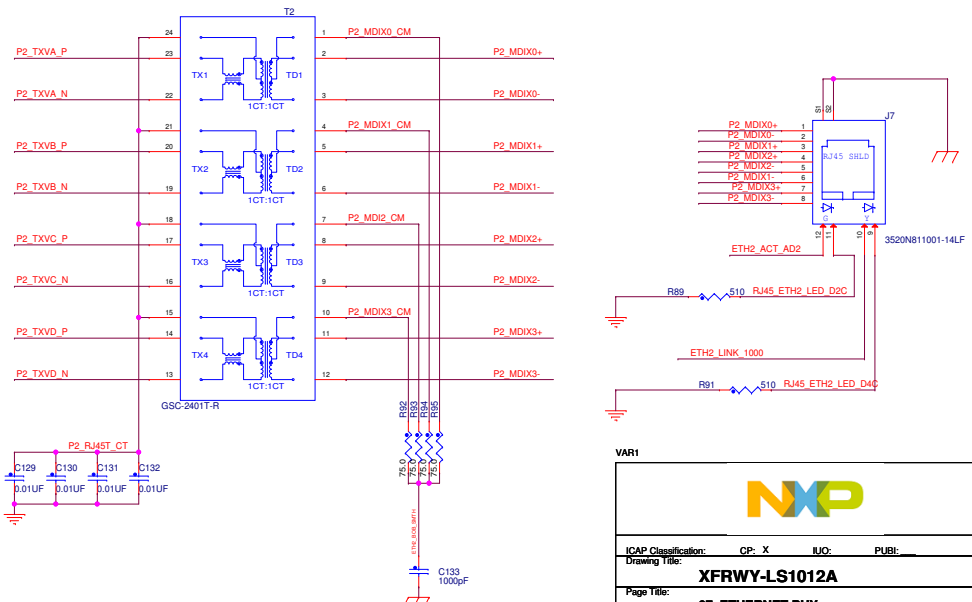



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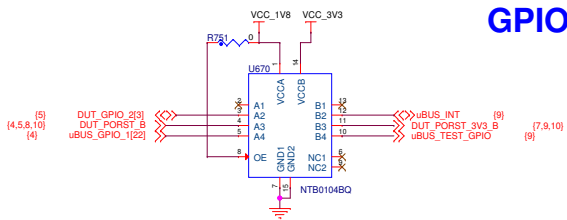


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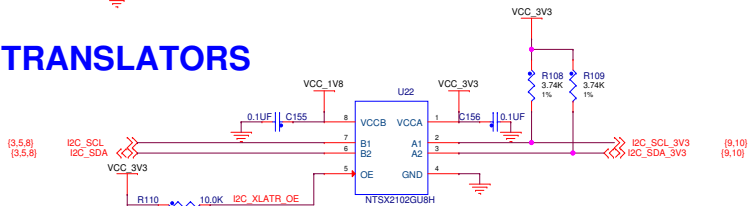
PHY1

[illegible]

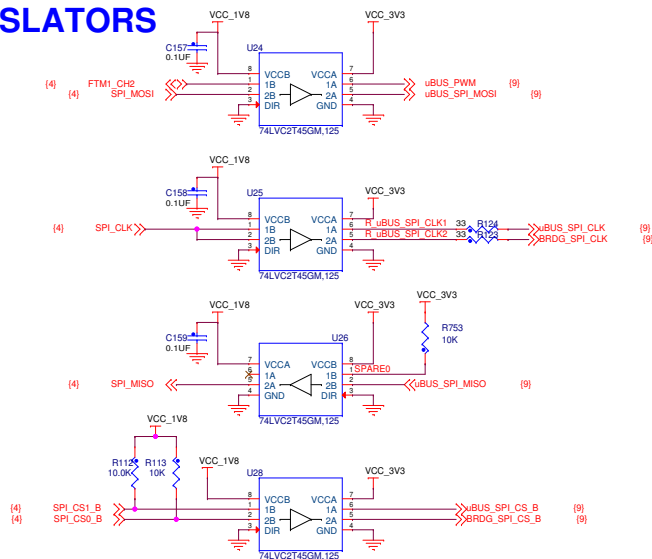
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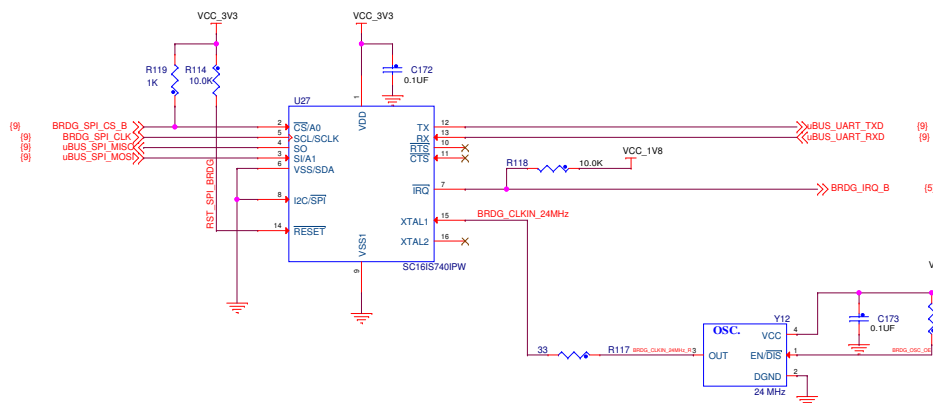
I2C TRANSLATORS



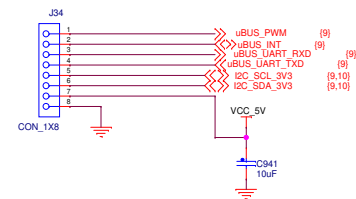
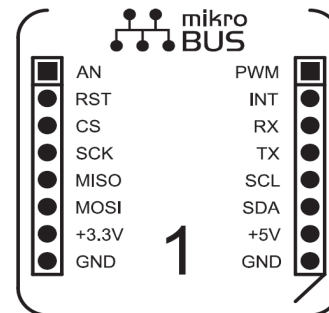
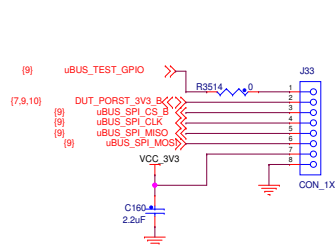
SPI TRANSLATORS



SPI TO UART BRIDGE



MIKRO-CCLICK CONNECTORS



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09_MIKROBUS CLICK & SPI BRIDGE			
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M.2 Type-E NGFF mini PCIe Slot

[illegible]

NOTE:
SD card does not support Write Protect(WP) Feature.

