

1. THIS DRAWING SPECIFIES THE REQUIREMENTS FOR A PRINTED WIRING BOARD IN ACCORDANCE WITH SPECIFICATION IPC-A-600 CLASS 2 (LATEST REVISION).
2. THE PWB MUST BE LEAD FREE ASSEMBLY PROCESS COMPATIBLE AND MUST BE ABLE TO HANDLE A MINIMUM OF 5 CYCLES AT 260 DEGREES CELSIUS FOR 10 SECONDS.
3. BASE MATERIAL : LAMINATE AND PREPREG SHEET MEET MIL-4018-26, B3 or 98
T_g - MUST BE GREATER THAN OR EQUAL TO 150 DEGREES CELSIUS.
T_d - MUST BE GREATER THAN OR EQUAL TO 330 DEGREES CELSIUS.
4. COPPER FOIL WEIGHT - SEE STACKUP DETAIL 'A'
5. CHARACTERISTIC IMPEDANCE - NONE
6. MINIMUM CONDUCTIVE WIDTH/SPACING TO BE .010"/.005"
7. PLATING FINISH - BOTH SIDES ENIG (ELECTROLESS NICKEL IMMERSION GOLD):
0.5000- .232 MICRON (2-8 MICRONS) OF GOLD OVER
2.540-6.350 MICRON (100-250 MICRONS) OF NICKEL.

9. SOLDERMASK - BLUE COLOR, BOTH SIDES.
MODIFICATION OF SOLDERMASK IS NOT ALLOWED WITHOUT WRITTEN PERMISSION FROM FREESCALE.

10. SILKSCREEN - WHITE EPOXY INK, BOTH SIDES. NO SILK ON PADS.

11. ELECTRICAL TEST - 100% IPCD356.

12. PRINTED WIRING BOARD IS TO BE INDIVIDUALLY BAGGED

13. DRC'S MUST BE RUN ON THE GERBER BEFORE BUILDING BOARDS, UNLESS PRIOR APPROVAL IS GIVEN IN WRITING BY FREESCALE.

14. TEARDROPS MAYBE ADDED AT THE FAB HOUSE TO ALL SIGNAL LAYERS.

15. 2 SOLDER SAMPLES TO BE PROVIDED.

16. BASIC GRID INCREMENT AT 1:1 IS .0001.

17. SUPPLIER MARKINGS - ON SOLDER SIDE ONLY, WHERE SHOWN.
- MUST BE UL RECOGNIZED AND MUST HAVE

18. THE PWB WILL BE MARKED AS LEAD FREE BY USE OF AN INK STAMP (96

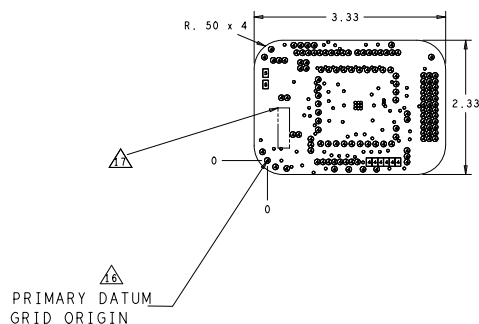
19. THE PWB WILL BE MARKED AS LEAD FREE PROCESS COMPATIBLE BY USE OF AN INK STAMP (260°C)

20. ALL PLATED AND NON-PLATED THROUGH HOLES ARE TO BE DRILLED AT PRIMARY DRILL STEP.
ALL HOLE LOCATION TOLERANCES ARE TO BE $\pm .002$ IN REFERENCE TO THE PRIMARY DATUM.

21. FINISHED PCB MUST BE PANELIZED FOR ASSEMBLY ACCORDING TO CONTRACT MANUFACTURERS REQUIREMENTS. THE ADDITION OF RAILS AND .125" NON-PLATED TOOLING HOLES ARE AT THE DISCRETION OF CONTRACT MANUFACTURER. PANELIZATION MUST BE APPROVED BY CONTRACT MANUFACTURER.

22. NO INTENTIONAL SHORTS ON DESIGN.

23. COPPER SHAVING IS ALLOWED IN FRONT OF PADS OF CONNECTORS J5 AND J7 IN ORDER TO KEEP A 0.010" CLEARANCE FROM THE EDGE OF THE BOARDS.

[illegible]

DRILL CHART: TOP to BOTTOM				
ALL UNITS ARE IN MILS				
FIGURE	SIZE	TOLERANCE	PLATED	QTY
○	10.0	+0.0/-10.0	PLATED	89
⊙	40.0	+3.0/-3.0	PLATED	130
⊠	41.0	+3.0/-3.0	PLATED	6
⊞	51.0	+3.0/-3.0	PLATED	2

FINISHED Cu WEIGHT

TOTAL THICKNESS 0.059"

LAYER 1 COMPONENT SIDE 1/2 oz.

LAYER 2 GROUND PLANE 1 oz.

LAYER 3 POWER PLANE 1 oz.

LAYER 4 SOLDER SIDE 1/2 oz.

DETAIL A
LAYER STACKUP
SEMI-CON

PART NO. 170-28829 THIS DOCUMENT CONTAINS INFORMATION WHICH IS UNCLASSIFIED AND WILL NOT BE USED FOR ENGINEERING DESIGN OR MANUFACTURE OF HARDWARE IN WHOLE OR IN PART WITHOUT THE CONSENT OF FREESCALE.		FREESCALE SEMICONDUCTOR 6501 WILLIAM CANNON DRIVE WEST AUSTIN, TEXAS 78735 US	
PURT (PUBLIC INFORMATION) <input checked="" type="checkbox"/> FWD (FREESCALE INTERNAL USE ONLY) FPC (FREESCALE CONFIDENTIAL PROPERTY)		TITLED: PRINTED WIRING BOARD FRM-CD1030EVB	
UNLESS OTHERWISE SPECIFIED, DIMENSIONS ARE IN INCHES TOLERANCES ARE: DIMENSIONS IN INCHES .XXX .XXX .2-30T <input checked="" type="checkbox"/> ALL DIMENSIONS APPROXIMATE UNLESS ALL DIMENSIONS EXCEPTED LENGTH, WIDTH, & THICKNESS DIMENSIONS ARE NOT TO SCALE. THESE DIMENSIONS ORTHOGRAPHIC PROJECTION IS USED.		APPROVALS DATE DRAWN J. ROMO 07-20-15 CHECKED J. SHEPARD 07-20-15 DESIGNER J. ROMO 07-20-15 SIZE CAD FILE NAME ENG. NAME REV D LAY-28829 FAB-28829 A SCALE 1/1 DO NOT SCALE DRAWING SHEET 1 OF 1	