



Table of Contents	
2	NOTES
3	Block Diagram
4	MAIN SCHEMATIC

Revisions			
Rev	Description	Date	Approved
X1	Initial Release	04/31/2011	Julio Muñoz
A	Replace CAPS 12pF with 11pF Xtal 32 MHz Replace name X-1231X-MRB with 1231X-MRB	08/13/2011	Jose Quiroz
A2	Update U1 symbol	03/6/2012	Jose Quiroz

		Wireless Connectivity Operation 6501 William Cannon Drive West Austin, TX 78735-8088	
This document contains information proprietary to Freescale Semiconductor and shall not be used for engineering design, procurement or manufacture in whole or in part without the express written permission of Freescale Semiconductor.			
Designer: K.Marjula		ICAP Classification: FCP: X FIUO: PUB:	
Drawing Title: 1231X-MRB (Navajo)			
Drawn by: K.Marjula		Page Title: TITLE PAGE	
Approved: J. MUNOZ		Size C	Document Number SCH-27106 PDF: SPF-27106
Date: Tuesday, March 06, 2012		Sheet 1	of 4
		Rev A2	

- 54321
- D
- C
- B
- A
- Unless Otherwise Specified:
 - All resistors are in ohms, 5%, 1/8 Watt
 - All capacitors are in uF, 20%, 50V
 - All voltages are DC
 - All polarized capacitors are aluminum electrolytic
 - Interrupted lines coded with the same letter or letter combinations are electrically connected.
 - Device type number is for reference only. The number varies with the manufacturer.
 - Special signal usage:
 - _B Denotes - Active-Low Signal
 - <> or [] Denotes - Vectored Signals
 - Interpret diagram in accordance with American National Standards Institute specifications, current revision, with the exception of logic block symbology.



ICAP Classification:			FCP: X	FIUO:	PUBI:
Drawing Title:					
1231X-MRB (Navajo)					
Page Title:					
NOTES					
Size	Document Number				Rev
C	SCH-27108 PDF: SPF-27108				A2
Date:	Tuesday, March 06, 2012		Sheet	2	of 4

54321

