

NXP Semiconductors

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2, Esplanade Anton Philips, Campus Effiscience, Colombelles

BP 20 000, 14906 Caen Cedex 9, France

DATE: January 3rd 2017

FCC DECLARATION OF CONFORMITY (DoC)

Trade Name(s): NXP Semiconductors B.V.

Model Number(s): DR1222 JTAG Expansion Board

Our device above has been tested and found to be compliant with the FCC Part 15 Class B limits as a computer peripheral.

This device complies with Part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) This device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.

Details of test lab and documentation:

Test Lab Name:

LCIE

33 avenue du Général Leclerc 92260 Fontenay aux Roses

France

FCC Test Site Accreditation Number (as seen on FCC website): FR0010 Test report number and issue date: rapport-140329-682424-0000026891 issued on March 16th 2016.

Signature:

Name: Ménard christophe

Position: Customer Support and Application Engineer

Adress:

2 Esplanade Anton Philips, Campus Effiscience,

14460 Colombelles

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USA representative & address (if address above not located in USA):

Name: Niel P. Smith

Address:

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