Freescale Semiconductor
Reference Manual Addendum

MCF52259RMAD
Rev. 1, 12/2011

MCF52259 Reference Manual Errata

by: Microcontroller Solutions Group

This errata document describes corrections to the *MCF52259 Reference Manual*, order number MCF52259RM. For convenience, the addenda items are grouped by revision. Please check our website at http://www.freescale.com for the latest updates.

The current available version of the *MCF52259 Reference Manual* is Revision 4.

Table of Contents

1 Errata for Revision 4

Table 1. MCF52259 Reference Manual Rev. 4 Errata

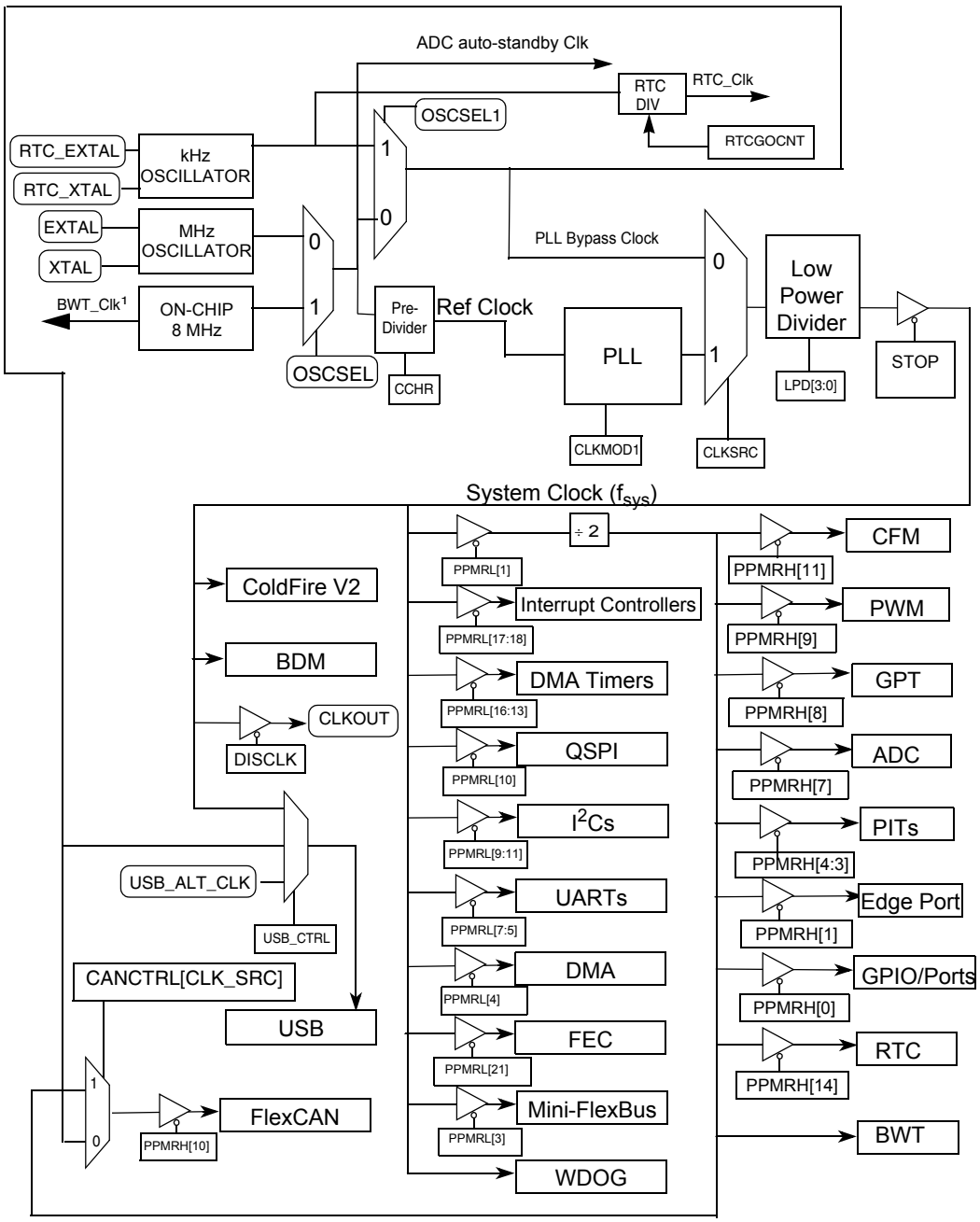
Location	Description
Chapter “Clock Module”/Section “Block Diagram”/Figure “Clock Module Block Diagram”	Updated “Clock Module Block Diagram”.below. The figure in Rev. 4 shows that FlexCAN clock is sourced by the system clock (f_{sys}) or by EXTAL.Also, it incorrectly showed that the USB module can be sourced by EXTAL. This figure needs to be improved as given below. 

Table 1. MCF52259 Reference Manual Rev. 4 Errata (continued)

Location	Description
Chapter “Universal Serial Bus,OTG Capable Controller ”/Section “USB Control Register(USB_CTRL)” /Table“USB_CTRL field descriptions”	In Table “USB_CTRL field descriptions”, in field “1-0 CLK_SRC”, the clock source is incorrectly described as follows: 00 USB_ALT_CLK pin (External clock that can feed in from PTG0) 01 External OSC on EXTAL pin 10 Reserved 11 System clock source (MCGPLLSCLK) It should be as follows: 00 USB_ALT_CLK pin (External clock that can feed in from PTG0) 01 PLL Bypass Clock 10 Reserved 11 System clock source (f_{sys})
Chapter “FlexCAN”/Section “FlexCAN Control Register (CANCTRL)”/Table “CANCTRL field descriptions”/Equation 32-1	In Equation 32-1, the S clock frequency equation is incorrectly shown as $\text{S clock frequency} = \frac{f_{sys} \text{ or EXTAL}}{\text{PRESDIV} + 1}$ It should be as follows: $\text{S clock frequency} = \frac{f_{sys/2} \text{ or PLL bypass clock}}{\text{PRESDIV} + 1}$
Chapter “FlexCAN”/Section “FlexCAN Control Register (CANCTRL)”/Table “CANCTRL field descriptions”	In Table “CANCTRL field descriptions”, bit 13 “CLK_SRC”, the clock source is incorrectly described as 0 Clock source is EXTAL 1 Clock source is the internal bus clock, f_{sys} It should be as follows: 0 Clock source is PLL bypass clock 1 Clock source is the internal bus clock, $f_{sys/2}$
Chapter “FlexCAN”/Section “FlexCAN Control Register (CANCTRL)”/Figure “CAN engine clocking scheme”	In Figure “CAN engine clocking scheme “Oscillator Clock (EXTAL)” needs to be changed to PLL bypass clock In Equation 32-6, a. fsys needs to be changed to $f_{sys/2}$ b. “EXTAL” needs to be changed to PLL bypass clock

2 Revision History

Table 2 provides a revision history for this document.

Table 2. Revision History Table

Rev. Number	Substantive Changes	Date of Release
1.0	Initial release. Incorporated changes in the following chapters: • Clock Module • Universal Serial Bus, OTG Capable Controller • FlexCAN	12/2011

How to Reach Us:

Home Page:
www.freescale.com

Web Support:
http://www.freescale.com/support

USA/Europe or Locations Not Listed:
Freescale Semiconductor, Inc.
Technical Information Center, EL516
2100 East Elliot Road
Tempe, Arizona 85284
+1-800-521-6274 or +1-480-768-2130
www.freescale.com/support

Europe, Middle East, and Africa:
Freescale Halbleiter Deutschland GmbH
Technical Information Center
Schatzbogen 7
81829 Muenchen, Germany
+44 1296 380 456 (English)
+46 8 52200080 (English)
+49 89 92103 559 (German)
+33 1 69 35 48 48 (French)
www.freescale.com/support

Japan:
Freescale Semiconductor Japan Ltd.
Headquarters
ARCO Tower 15F
1-8-1, Shimo-Meguro, Meguro-ku,
Tokyo 153-0064
Japan
0120 191014 or +81 3 5437 9125
support.japan@freescale.com

Asia/Pacific:
Freescale Semiconductor China Ltd.
Exchange Building 23F
No. 118 Jianguo Road
Chaoyang District
Beijing 100022
China
+86 10 5879 8000
support.asia@freescale.com

For Literature Requests Only:
Freescale Semiconductor Literature Distribution Center
1-800-441-2447 or 303-675-2140
Fax: 303-675-2150
LDCForFreescaleSemiconductor@hibbertgroup.com

freescale™
semiconductor

MCF52259RMAD
Rev. 1
12/2011