



**Freescale Semiconductor, Inc.**



Errata to MC68302 User's Manual

**Section #/Page #**

**Changes**

2.8, Page 2-15 The last sentence should read, “Revision C parts have revision number \$0003.”

4.3, Page 4-5 In the RST—Software Reset Command. Replace the 5th sentence with the following:

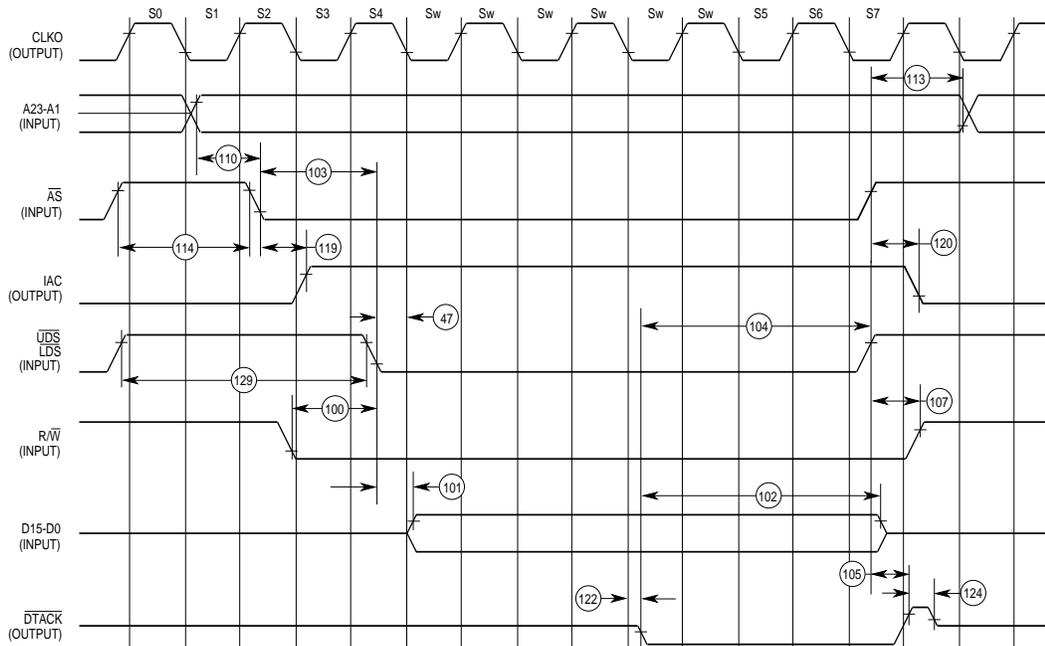
The CP reset resets the SCCs to the state following a hardware reset, but it does not affect the serial interface (the port A and B registers and the configuration of the SIMODE and SIMASK registers).

5.6, Page 5-7 In Figure 5-5, change the reference to A23–A1 to D15–D0.

6.7, Page 6-5 Replace the table with the following (note the correction to Num 5a, 25 MHz, Max):

Num.	Characteristic	Symbol	16.67 MHz		20 MHz		25 MHz		Unit
			Min	Max	Min	Max	Min	Max	
	Frequency of Operation	f	8	16.67	8	20	8	25	MHz
1	Clock Period (EXTAL) (See note 3)	$t_{cyc}$	60	125	50	125	40	125	ns
2, 3	Clock Pulse Width (EXTAL)	$t_{CL}, t_{CH}$	25	62.5	21	62.5	16	62.5	ns
4, 5	Clock Rise and Fall Times (EXTAL)	$t_{Cr}, t_{Cf}$	—	5	—	4	—	4	ns
5a	EXTAL to CLKO Delay (See Notes 1 and 2)	$t_{CD}$	2	11	2	9	2	9	ns

6.10, Page 6-18 Replace Figure 6-9 with the following:



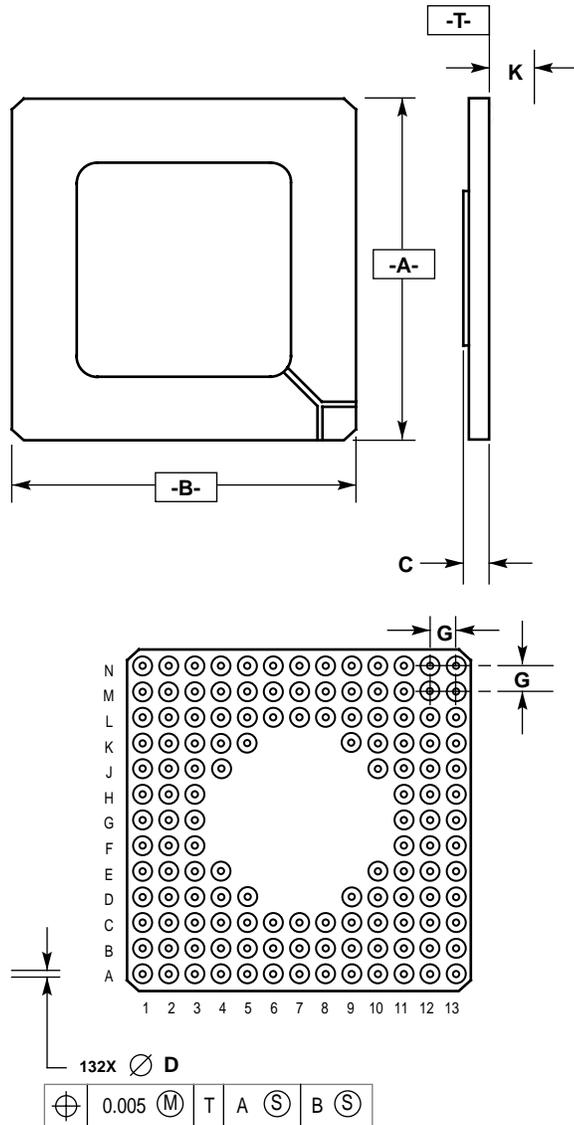
**Figure 6-9. External Master Internal Asynchronous Write Cycle Timing Diagram**

Section #/Page #

Changes

7.2.1, Page 7-4

Replace the PGA package case drawings with the following:



- NOTES:
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
  2. CONTROLLING DIMENSION: INCH.

DIM	INCHES	
	MIN	MAX
A	1.340	1.380
B	1.340	1.380
C	0.100	0.150
D	0.017	0.022
G	0.100 BSC	
K	0.170	0.195

7.32.3, Page 7-6

In the dimensions table, Dimension G is incorrect. Change ‘0.2 BSC’ to ‘0.02 BSC’.

D.9.2, Page D-70

The last paragraph, first sentence should read, “The general-purpose inputs (GPI) were simply pulled high for this example.”

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