

DSL Modem

Overview

Digital subscriber line—or DSL—technologies significantly increase the bandwidth over the "last mile" in the public network while providing basic plain old telephone services (POTS) and supplying POTS power, over the same wire. DSL technology eliminates the need for the public switched telephone network (PSTN) to handle high-speed data calls. With DSL, data calls are routed directly to the ISP or corporate network.

A DSL circuit connects a DSL modem on each end of a twisted-pair telephone line, which creates three information channels:

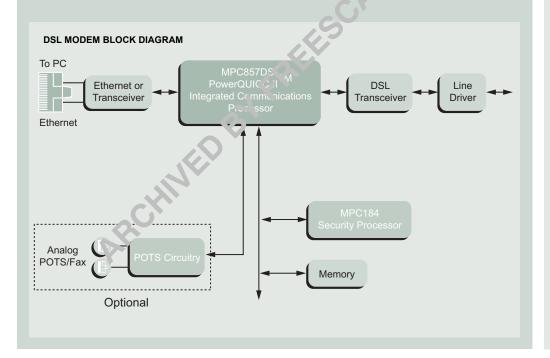
> High-speed downstream channel from the central office to the user (8 Mbps or higher)

- > Medium-speed duplex channel (up to 1 Mbps)
- > POTS channel

The POTS channel is usually separated from the digital modem by filters which guarantee uninterrupted POTS service regardless of the other services that are utilized through the line. Each channel car be submultiplexed to form multiple low rate channels.

Key Benefits

- > Provides bro laband service over normal histed-pair telephone lines without inter paing POTS services
- > E.im.nates the need for the FSTN to handle high-speed data calls







Freescale Ordering	Information	
Part Number		
MPC857DSL	 > Embedded MPC8xx core > Single-issue, 32-bit version of the core (compatible with the PowerPC architecture definition) with 32, 32-bit general-purpose registers (GPRs) > Enhanced ATM functionality over that of the MPC860SAR > Multiple APC priority levels available to support a range of traffic pace requirements > Port-to-port switching capability without the need for RAM-based microcode > Simultaneous MII (100Base-T) and UTOPIA (half-duplex) capability > Parameter RAM for both SPI and I2C can be relocated without RAM-based microcode > Supports full-duplex UTOPIA master (ATM side) operation using a "split" bus > Up to 32-bit data bus (dynamic bus sizing for 8, 16, and 32 bits) > UTOPIA port supports up to 4 PHY addresses > Communications processor module (CPM) > On-chip 16x16 multiply accumulate controller (MAC) > One serial communication controller (SCC) for Ethernet (SCC1) > One serial management channel (SMC) for UART (SMC1) > One serial peripheral interface (SPI) > One inter-integrated circuit (I²C) port > Parallel interface port (PIP) > One PCMCIA socket (Port B) > 3.3 V core, 3.3 V I/O 	www.freescale.com/smartnetworks
MPC184	> One public key execution unit (PKEU) that supports RSA and ["fe it allman, programmable field size up to 2048-bits, elliptic curve cryptograph", F2m and F(p) modes, and programmable field size up to 511-bits > One data encryption standard execution units (DEUs) > One advanced encryption standard unit (AESU) > One ARC four execution unit (AFEU) > One message digest execution units (MDEU), > One random number generator (RNG) > 8xx compliant external bus interface withaster/slave logic > PCI 2.2 compliant external bus in enfact, with master/slave logic > Four crypto-channels, each supporting multi-command descriptor chains > 8KB of internal scratchpalical in ending multi-command descriptor chains > 8KB of internal scratchpalical in ending multi-command descriptor chains > Software and developinant support available	www.freescale.com/smartnetworks



Design Challenges

As with most CPE applications, DSL modem designs face significant cost pressures which place severe constraints on the optimization of cost/ performance trade-offs. The common use of ATM as the transport technology over the DSL link burdens the design with complex data stream and control requirements. In addition, the designer faces the challenges of simplifying the manufacturing process and maintaining a high level of flexibility to accommodate system compatibility issues and facilitate smooth upgrades and enhancements.

Freescale Semiconductor Solution Freescale Semiconductor's MPC85x PowerQUICC™ family moves the entire price/performance curve for DSL

modems to a new level with the new MPC857DSL, which integrates ATM, Ethernet, and USB functionality with a high-performance PowerPC ISA core in a very cost-effective package. The proven family architecture includes a versatile memory controller and a separate on-chip communications processor module that off loads peripheral tasks from the core, such as the traffic handling of ATM via the UTOPIA port, Ethernet, and USB. In addition, the established code base and broad third-party support from Freescale Semiconductor's Smart Network Alliance Program members further enable costefficient solutions and accelerated timeto-market for DSL equipment suppliers.

Of course, the designer can select the MPC857DSL or any other MPC85x family member, each with an attractive set of functional capabilities to meet specific performance goals without sacrificing flexibility and cost targets. Network security protocols involve processing sophisticated, computationalintensive algorithms that can reduce system performance by monopolizing the host processor. The MPC184 security processor is easily integrated into systems an facy using Freescale Semiconductor a processors, and because it ctilizes existing system men. י, '.ne result is significant savings cibeth board space and system cost.

Developmen	at Tools Note			
			Je scription	
Hardware	MPC857TIBIS	Freescale Semiconductor	MPC857T IBIS Model Please download the README file (MPC8XXIBIS).	
Hardware	MPC8XXIBIS	Freescale Semiconductor	README File for MPC8XX IBIS Models Please read before using the IBIS models for the MPC8xx products.	
Hardware	PPCEVAL-SP3-7400	Freescale Semicond [,] ctor	Altimus X3 MPC7400 PMC Module for the Sandpoint X3 Motherboard	
Hardware	PPCEVAL-SP3-750	Freescale Sercondi .ctor	Altimus X3 MPC750 PMC Module for the Sandpoint X3 Motherboard (discontinued)	
Hardware	SANDPOINTX3	Freescale Certiconductor	Sandpoint X3 Evaluation System - Motherboard	
Software	DINK32	Fressa e Semiconductor	ROM-Based Debug Monitor, R13.0	
Note: For detaile	d information, go to www.freesc	ale. om and click on Tools. Search for	r tools by product or technology, family or function, and product number.	
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