



Package information

Package summary 1

D (double) Terminal position code

SO8 Package type descriptive code

Package type industry code SO8

Package style descriptive code SO (small outline)

Package body material type P (plastic) IEC package outline code 076E03 JEDEC package outline code MS-012

Mounting method type S (surface mount)

18-02-2003 Issue date Manufacturer package code 98ASA01364D

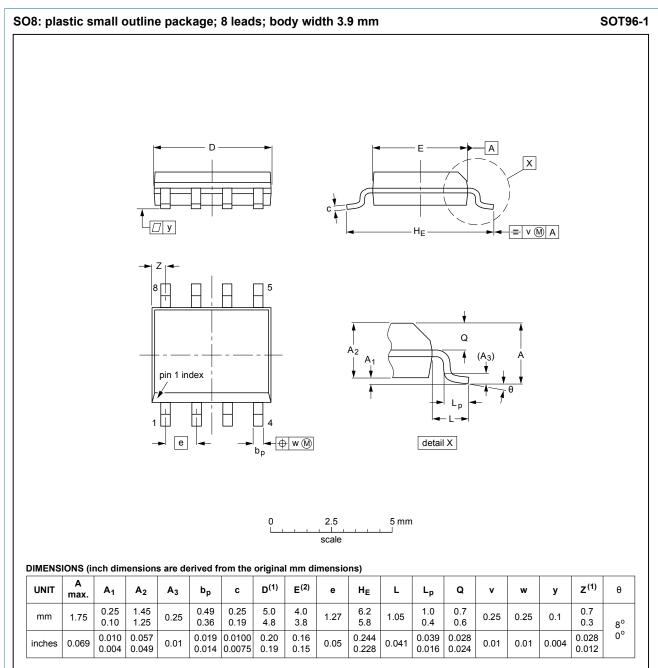
Table 1. Package summary

Parameter	Min	Nom	Max	Unit
package length	4.8	4.9	5	mm
package width	3.8	3.9	4	mm
seated height	-	1.75	-	mm
package height	1.25	1.35	1.45	mm
nominal pitch	-	1.27	-	mm
actual quantity of termination	-	8	-	



plastic, small outline package; 8 leads; 1.27 mm pitch; 4.9 mm x 3.9 mm x 1.75 mm body

2 Package outline



Notes

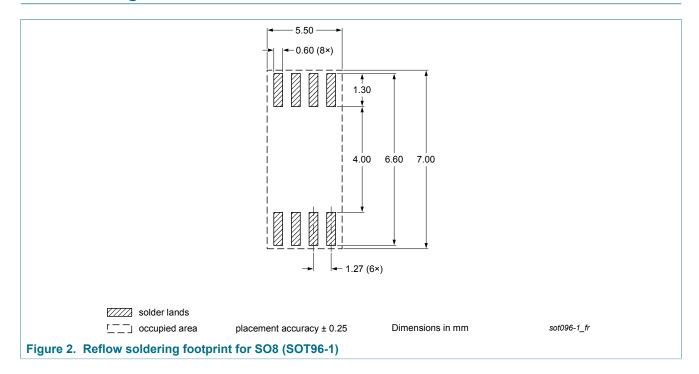
- 1. Plastic or metal protrusions of 0.15 mm (0.006 inch) maximum per side are not included.
- 2. Plastic or metal protrusions of 0.25 mm (0.01 inch) maximum per side are not included.

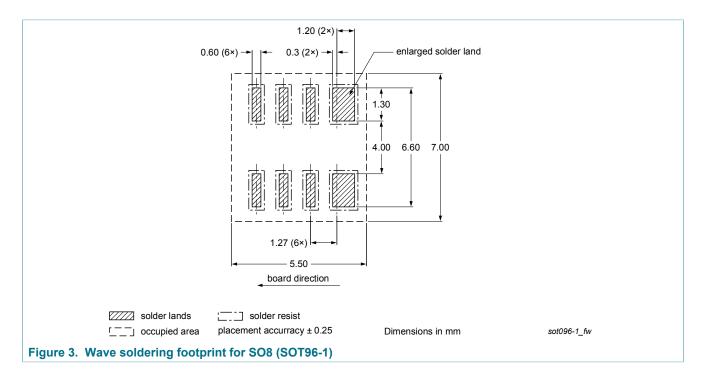
OUTLINE REFERENCES				EUROPEAN	ISSUE DATE	
VERSION	IEC	JEDEC	JEITA		PROJECTION	ISSUE DATE
SOT96-1	076E03	MS-012				99-12-27 03-02-18

Figure 1. Package outline SO8 (SOT96-1)

plastic, small outline package; 8 leads; 1.27 mm pitch; 4.9 mm x 3.9 mm x 1.75 mm body

3 Soldering





plastic, small outline package; 8 leads; 1.27 mm pitch; 4.9 mm x 3.9 mm x 1.75 mm body

4 Legal information

Disclaimers

Limited warranty and liability — Information in this document is believed to be accurate and reliable. However, NXP Semiconductors does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. NXP Semiconductors takes no responsibility for the content in this document if provided by an information source outside of NXP Semiconductors.

In no event shall NXP Semiconductors be liable for any indirect, incidental, punitive, special or consequential damages (including -without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, NXP Semiconductors' aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the Terms and conditions of commercial sale of NXP Semiconductors.

Right to make changes — NXP Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

plastic, small outline package; 8 leads; 1.27 mm pitch; 4.9 mm x 3.9 mm x 1.75 mm body

Contents

1	Package summary	1
2	Package outline	2
3	Soldering	
4	Legal information	