OL-PCF8576DU_DA

Wire bond die; 59 bonding pads

10 February 2016

Package information

1. Package summary

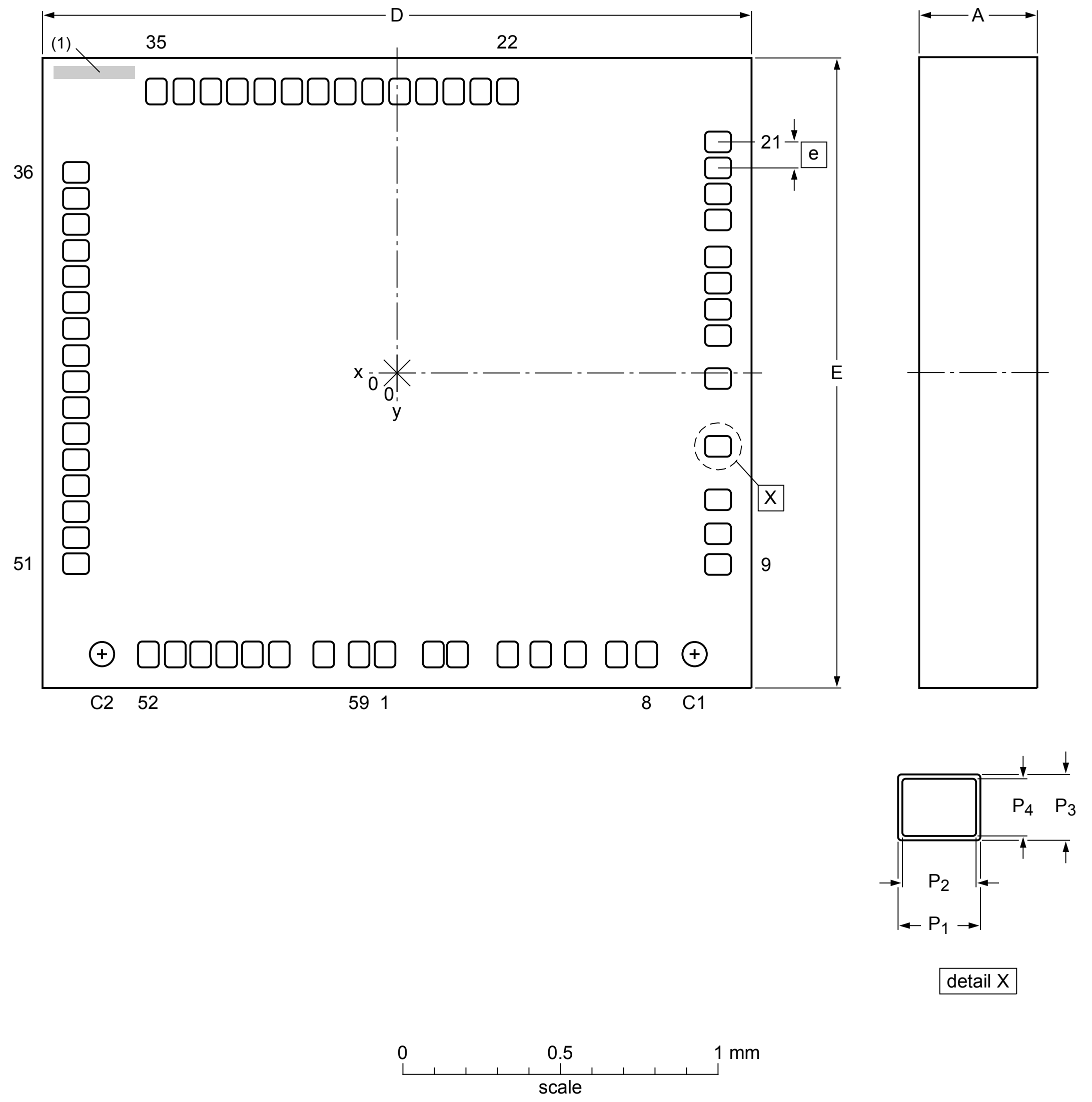
Terminal position code	U (upper)
Package type descriptive code	UC
Package style descriptive code	UC (uncased chip)
Package style suffix code	NA (not applicable)
Package body material type	X (other)
IEC package outline code	---
JEDEC package outline code	---
JEITA package outline code	---
Mounting method type	S (surface mount)
Issue date	26-10-2011

Table 1. Package summary

Symbol	Parameter		Min	Typ	Nom	Max	Unit
D	package length		-	-	2.2	-	mm
E	package width		-	-	2	-	mm
A	seated height		-	-	0.38	-	mm
e	nominal pitch		-	-	0.072	-	mm
n_2	actual quantity of termination		-	-	59	-	

2. Package outline

Wire bond die; 59 bonding pads **PCF8576DU/DA**



Notes

1. Marking code: PC8576D-2

pcf8576du_da_do

Outline version	References				European projection	Issue date
	IEC	JEDEC	JEITA			
PCF8576DU/DA						~~08-12-10~~ 10-12-20

Fig. 1. Package outline OL-PCF8576DU_DA

3. Legal information

Disclaimers

Limited warranty and liability — Information in this document is believed to be accurate and reliable. However, NXP Semiconductors does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. NXP Semiconductors takes no responsibility for the content in this document if provided by an information source outside of NXP Semiconductors.

In no event shall NXP Semiconductors be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, NXP Semiconductors' aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the *Terms and conditions of commercial sale* of NXP Semiconductors.

Right to make changes — NXP Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

4. Contents

For more information, please visit: http://www.nxp.com
For sales office addresses, please send an email to: salesaddresses@nxp.com
Date of release: 10 February 2016