



Device Errata

XC68SC302

Revision A.1 Mask 1F86S.
February 18, 1998

1. The ID bit in the SCCS1-3 registers is always zero and does not get set when and idle status is detected on the line.

This document contains information on a product under development. Motorola reserves the right to change or discontinue this product without notice.



MOTOROLA