Document Number: ISF95KSWER
Rev. 0, 5/2014

ISF Errata

Release 1.1 for FXLC95000

This errata sheet describes the problems known at the release date of this document.

All ISF related updates are available at freescale.com/isf

Open Issues in ISF Release 1.1 for FXLC95000				
Freescale Issue Identification Number	**Headline**	**First Found on Release**	**Issue Submission Date**	**Resolved by Release**
ENGR00296187	Error messages generated by ISF are suppressed by the Sensor Manager. WORKAROUND: None.	ISF1P195K_1000	22 January 2014	TBD

ENGR00308188	The sensor adapter for the FXOS8700 provides incorrect enumeration for the range.
	
	WORKAROUND:
	Edit C:\Program Files\Freescale\ISF_R1p1\ISF\Include\Sensors\fsl_fxos8700_i2cspi_6D_AccMag_config_types.h to correct the problem.
	Replace this text:
	
	`typedef enum fxos8700_Range_tag {`
	`  FXOS8700_RANGE_8G = 0,  /*<!Acceleration range is 8G.*/`
	`  FXOS8700_RANGE_4G = 1,  /*<!Acceleration range is 4G.*/`
	`  FXOS8700_RANGE_2G = 2,  /*<!Acceleration range is 2G.*/`
	`  FXOS8700_RANGE_MAX = FXOS8700_RANGE_2G,`
	`} fxos8700_Range_t;`
	
	with the following text:
	
	`typedef enum fxos8700_Range_tag {`
	`  FXOS8700_RANGE_2G = 0,  /*<!Acceleration range is 2G.*/`
	`  FXOS8700_RANGE_4G = 1,  /*<!Acceleration range is 4G.*/`
	`  FXOS8700_RANGE_8G = 2,  /*<!Acceleration range is 8G.*/`
	`  FXOS8700_RANGE_MAX = FXOS8700_RANGE_8G,`
	`} fxos8700_Range_t;`
	
	Alternatively, use FXOS8700_RANGE_8G when 2G is desired and FXOS8700_RANGE_2G when 8G is desired. However, this will require another correction when this is fixed in a future ISF release. As a result, the change in the include file is the recommended solution.
		ISF1P195K_1000	11 April 2014	ISF1P195K_1001

Issues Fixed Between ISF1P01 and ISF1P1_1000				
Freescale Issue Identification Number	**Description**	**First Found on Release**	**Fixed in Release**	**Ticket Closed Date (Submission Date)**
(ENGR00266664) ENGR00270566	Update to the Software Reference Manual to notify users about a FXLC95000 hardware feature. The AFE manual trigger bit inhibits frame interrupt causing the Power Manager to spin waiting for the Start of Frame IRQ.	ISF1P0195K	ISF1P195K_1000	10 October 2013 (11 June 2013)
ENGR00275462	Sensor Adapters were required to be in the STATE_STARTED state before unregistering callbacks from the Bus Manager. This was an unnecessary condition for unregistering callbacks and has been removed in all of the sensor adapters.	ISF1P0195K	ISF1P95K_1000	25 September 2013 (15 August 2013)
ENGR00277198		ISF1P0195K	ISF1P95K_1000	12 September 2013 (28 August 2013)
ENGR00296203	It is possible for an embedded application to begin sensor configuration before the ISF initialization task completes. An error message is generated but is not passed to the embedded application by the Sensor Manager.	ISF1P0195K	ISF1P95K_1000	14 February 2014 (22 January 2014)
	MQX 3.7 returns allocated memory to the memory pool when the allocating task is destroyed.			
	The FXOS8700 requires a 1 millisecond delay after reset which is not reflected in the FXOS8700 sensor adapter.			

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