AN11700

PN7120 product quick start guide

Rev. 1.9 — 1 February 2021 336019

Application note COMPANY PUBLIC

Document information

Information	Content
Keywords	PN7120, NFC, quick start-up
Abstract	This document describes the PN7120 documentation to be used to start working with PN7120.



PN7120 product quick start guide

1 Revision history

Revision history

Rev	Date	Description
1.9	20210201	 Removed Windows IoT support The format of this application note has been redesigned to comply with the new identity guidelines of NXP Semiconductors.
1.8	20190708	Updated Linux demo part with link to instructions
1.7	20181113	Updated with MCUXpresso reference
1.6	20180725	Updated weblinks
1.5	20170222	Updated demo images weblinks
1.4	20160819	Added Arduino demo kit description and fixed broken links
1.3	20160520	Corrected syntax error in demo kit images list
1.2	20151013	 Updated with Windows for IoT SW support Section 6 updated
1.1	20150624	Corrected wrong link to "NXP-NCI NullOS library example" document
1.0	20150611	First released version

PN7120 product quick start guide

2 Introduction

This document describes how to start working with PN7120 to add NFC functionality to a device. It points where to find the dedicated information for hardware, antenna and software integration as well as information related to the PN7120 demo kit.

3 General description of PN7120

PN7120 is a full NFC controller solution with integrated firmware and NCI interface designed for contactless communication at 13.56 MHz.

PN7120 is the ideal solution for rapidly integrating NFC technology in any application. Especially those running O/S environment like Linux or Android, reducing Bill of Material (BOM) size and cost.

You can get PN7120 technical details in the product data sheet: http://www.nxp.com/documents/data-sheet/PN7120.pdf.

4 PN7120 Integration steps

4.1 Hardware integration

Guidelines for hardware integration of the PN7120 are given in the "Hardware Design Guide" document: http://www.nxp.com/documents/application note/AN11565.pdf.

The "Low-Power Mode Configuration" document describes how to optimize power consumption by use of PN7120 low-power polling mode: http://www.nxp.com/documents/application_note/AN11562.pdf.

The PN7120 demo kit can be used as reference design (see Section 5.1.1).

4.2 Antenna integration

Recommendation for the antenna design choice and guidelines for antenna matching are given in the "Antenna Design and Matching Guide" document: http://www.nxp.com/documents/application note/AN11564.pdf.

The PN7120 demo kit can be used as reference design (see Section 5.1.1).

4.3 Software integration

The "User Manual" describes the PN7120 host interface commands: http://www.nxp.com/documents/user_manual/UM10819.pdf.

Furthermore, depending on the targeted platform the following software solutions are supported.

4.3.1 Linux

The "Linux Software Stack Integration Guidelines" document describes the Linux libnfc-nci software stack supporting PN7120 under a Linux system: http://www.nxp.com/documents/application_note/AN11697.pdf.

PN7120 product quick start guide

4.3.2 Android

The "NXP-NCI Android Porting Guidelines" document provides guidelines about how to integrate support of PN7120 under an Android system: http://www.nxp.com/documents/application_note/AN11690.pdf.

4.3.3 Other OS or Null OS

For other systems, source code examples and related documentation are given for both NXP's LPC and NXP's Kinetis MCUs:

- NXP-NCI MCUXpresso example project: https://www.nxp.com/doc/SW4325
- AN11990 NXP-NCI MCUXpresso example: http://www.nxp.com/documents/application note/AN11990.pdf

5 PN7120 demo kits

Two PN7120 NFC Controller SBC kits exist. First one been referenced as OM5577/PN7120S allowing plug'n'play support of Raspberry Pi and BeagleBone platforms. While the second one, referenced as OM5577/PN7120ARD, allows easy support to Arduino compatible platforms.

All information can be found on the related webpage: https://www.nxp.com/products/rfid-nfc/nfc-hf/nfc-readers/development-kits-for-pn7120-plugn-play-nfc-controller:OM5577

5.1 OM5577/PN7120S

5.1.1 Hardware

The "PN7120 NFC Controller SBC Kit User Manual" provides a description of the demo kit from hardware perspectives: http://www.nxp.com/documents/user_manual/UM10878.pdf.

The related schematics, BOM and Gerber files can be found here: https://www.nxp.com/downloads/en/board-support-packages/HW3346.zip.

5.1.2 Software

The "PN7120 NFC Controller SBC Kit Quick Start Guide" document describes how to get started with the demo kit with Raspberry Pi on Linux or with BeagleBone on Linux/Android: http://www.nxp.com/documents/application_note/AN11646.pdf.

Guidelines to set up the Linux demonstration are provided here https://community.nxp.com/t5/NXP-Designs-Knowledge-Base/Easy-set-up-of-NFC-on-Raspberry-Pi/ta-p/1099034. For others demonstration-related software images can be downloaded here:

- SW OM5577 BeagleBone Android KitKat demo image: https://www.nxp.com/lgfiles/updates/NFC/OM5577-PN7120S BBB AndroidKitKat demo v1.0.zip
- SW OM5577 BeagleBone Linux demo image: https://www.nxp.com/lgfiles/updates/
 NFC/OM5577-PN7120S_BBB_Linux_demo_v1.1.zip

PN7120 product quick start guide

5.2 OM5577/PN7120ARD

5.2.1 Hardware

The "PN7120 NFC Controller Arduino SBC Kit User Manual" provides a description of the demo kit from hardware perspectives: http://www.nxp.com/documents/user_manual/UM11008.pdf.

The related schematics, BOM and Gerber files can be found here: https://www.nxp.com/downloads/en/board-support-packages/HW3739.zip

5.2.2 Software

The "PN7120 Arduino SBC Kit Quick Start Guide" document describes how to get started with the demo kit on UdooNeo platform running Linux and Android but also on LPCXpresso and Kinetis (relates to NullOS and RTOS software integration described at Section 4.3.3): http://www.nxp.com/documents/application_note/AN11844.pdf.

The related software images can be downloaded here:

- UDOO Neo Linux demo image: https://www.nxp.com/lgfiles/updates/NFC/OM5577-PN7120S UdooNeo Linux demo v1.1.zip
- UDOO Neo Android Lollipop demo image: https://www.nxp.com/lgfiles/updates/NFC/OM5577-PN7120S_UdooNeo_AndroidLollipop_demo_V1.0.zip
- UDOO Neo Android Marshmallow demo image: https://www.nxp.com/lgfiles/updates/
 NFC/OM5577-PN7120S_UdooNeo_AndroidMarshmallow_demo_v1.1.zip

PN7120 product quick start guide

Legal information

6.1 Definitions

Draft — A draft status on a document indicates that the content is still under internal review and subject to formal approval, which may result in modifications or additions. NXP Semiconductors does not give any representations or warranties as to the accuracy or completeness of information included in a draft version of a document and shall have no liability for the consequences of use of such information.

6.2 Disclaimers

Limited warranty and liability - Information in this document is believed to be accurate and reliable. However, NXP Semiconductors does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. NXP Semiconductors takes no responsibility for the content in this document if provided by an information source outside of NXP Semiconductors. In no event shall NXP Semiconductors be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory. Notwithstanding any damages that customer might incur for any reason whatsoever, NXP Semiconductors' aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the Terms and conditions of commercial sale of NXP Semiconductors

Right to make changes — NXP Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

Suitability for use — NXP Semiconductors products are not designed, authorized or warranted to be suitable for use in life support, life-critical or safety-critical systems or equipment, nor in applications where failure or malfunction of an NXP Semiconductors product can reasonably be expected to result in personal injury, death or severe property or environmental damage. NXP Semiconductors and its suppliers accept no liability for inclusion and/or use of NXP Semiconductors products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

Applications — Applications that are described herein for any of these products are for illustrative purposes only. NXP Semiconductors makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification. Customers are responsible for the design and operation of their applications and products using NXP Semiconductors products, and NXP Semiconductors accepts no liability for any assistance with applications or customer product design. It is customer's sole responsibility to determine whether the NXP Semiconductors product is suitable and fit for the customer's applications and products planned, as well as for the planned application and use of customer's third party customer(s). Customers should provide appropriate design and operating safeguards to minimize the risks associated with their applications and products. NXP Semiconductors does not accept any liability related to any default, damage, costs or problem which is based on any weakness or default in the customer's applications or products, or the application or use by customer's third party customer(s). Customer is responsible for doing all necessary testing for the customer's applications and products using NXP Semiconductors products in order to avoid a default of the applications and the products or of the application or use by customer's third party customer(s). NXP does not accept any liability in this respect.

Export control — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from competent authorities.

Evaluation products — This product is provided on an "as is" and "with all faults" basis for evaluation purposes only. NXP Semiconductors, its affiliates and their suppliers expressly disclaim all warranties, whether express, implied or statutory, including but not limited to the implied warranties of non-infringement, merchantability and fitness for a particular purpose. The entire risk as to the quality, or arising out of the use or performance, of this product remains with customer. In no event shall NXP Semiconductors, its affiliates or their suppliers be liable to customer for any special, indirect, consequential, punitive or incidental damages (including without limitation damages for loss of business, business interruption, loss of use, loss of data or information, and the like) arising out the use of or inability to use the product, whether or not based on tort (including negligence), strict liability, breach of contract, breach of warranty or any other theory, even if advised of the possibility of such damages. Notwithstanding any damages that customer might incur for any reason whatsoever (including without limitation, all damages referenced above and all direct or general damages), the entire liability of NXP Semiconductors, its affiliates and their suppliers and customer's exclusive remedy for all of the foregoing shall be limited to actual damages incurred by customer based on reasonable reliance up to the greater of the amount actually paid by customer for the product or five dollars (US\$5.00). The foregoing limitations, exclusions and disclaimers shall apply to the maximum extent permitted by applicable law, even if any remedy fails of its essential purpose.

Translations — A non-English (translated) version of a document is for reference only. The English version shall prevail in case of any discrepancy between the translated and English versions.

Security — Customer understands that all NXP products may be subject to unidentified or documented vulnerabilities. Customer is responsible for the design and operation of its applications and products throughout their lifecycles to reduce the effect of these vulnerabilities on customer's applications and products. Customer's responsibility also extends to other open and/or proprietary technologies supported by NXP products for use in customer's applications. NXP accepts no liability for any vulnerability. Customer should regularly check security updates from NXP and follow up appropriately. Customer shall select products with security features that best meet rules, regulations, and standards of the intended application and make the ultimate design decisions regarding its products and is solely responsible for compliance with all legal, regulatory, and security related requirements concerning its products, regardless of any information or support that may be provided by NXP, NXP has a Product Security Incident Response Team (PSIRT) (reachable at PSIRT@nxp.com) that manages the investigation, reporting, and solution release to security vulnerabilities of NXP products.

6.3 Licenses

Purchase of NXP ICs with NFC technology

Purchase of an NXP Semiconductors IC that complies with one of the Near Field Communication (NFC) standards ISO/IEC 18092 and ISO/ IEC 21481 does not convey an implied license under any patent right infringed by implementation of any of those standards. Purchase of NXP Semiconductors IC does not include a license to any NXP patent (or other IP right) covering combinations of those products with other products, whether hardware or software.

6.4 Trademarks

Notice: All referenced brands, product names, service names and trademarks are the property of their respective owners.

MIFARE — is a trademark of NXP B.V. Kinetis — is a trademark of NXP B.V.

© NXP B.V. 2021. All rights reserved

PN7120 product quick start guide

 $\ensuremath{\mathsf{NXP}}$ — wordmark and logo are trademarks of NXP B.V.

PN7120 product quick start guide

Contents

1	Revision history	2
2	Introduction	
3	General description of PN7120	3
4	PN7120 Integration steps	3
4.1	Hardware integration	3
4.2	Antenna integration	
4.3	Software integration	
4.3.1	Linux	3
4.3.2	Android	4
4.3.3	Other OS or Null OS	4
5	PN7120 demo kits	4
5.1	OM5577/PN7120S	4
5.1.1	Hardware	4
5.1.2	Software	
5.2	OM5577/PN7120ARD	
5.2.1	Hardware	5
5.2.2	Software	5
6	Legal information	6

Please be aware that important notices concerning this document and the product(s) described herein, have been included in section 'Legal information'.