



i.MX 8 Family – Arm® Cortex®-A53, Cortex-A72, Virtualization, Vision, 3D Graphics, 4K Video

i.MX8

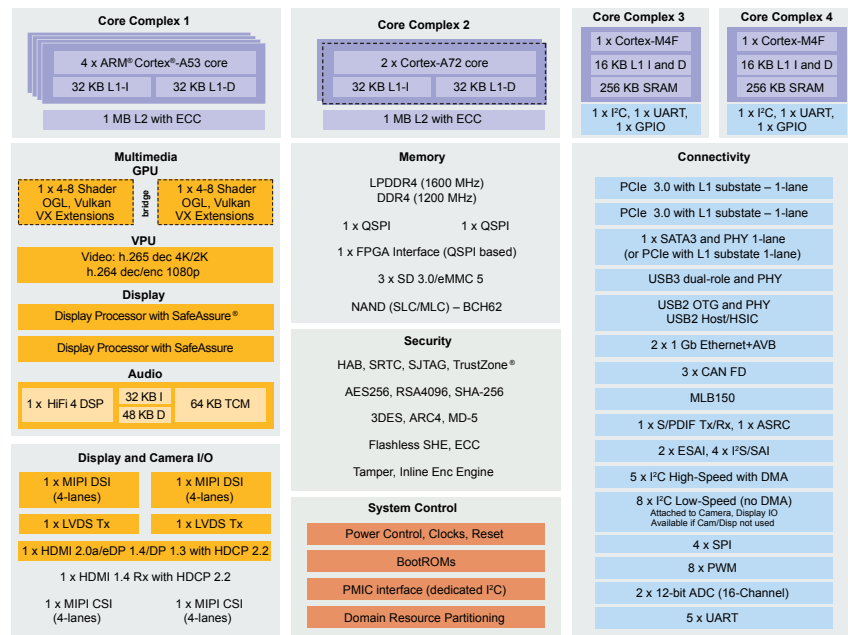
Last Updated: Dec 16, 2024

- Fast multi-OS platform deployment via advanced full-chip hardware virtualization and domain protection
- Deploy rich, fully-independent graphics content across 4x HD screens or 1x 4K screen
- Incorporate Vision and Speech Recognition interactivity via a powerful vision pipeline and audio processing subsystem
- Rapidly deploy multiple products by utilizing pin and power compatible packages and software friendly copy-exact IP blocks
- Android™, Linux®, FreeRTOS, QNX™, Green Hills®, Dornerworks XEN™
- Automotive AEC-Q100 Grade 3 (-40° to 125° C Tj), Industrial (-40° to 105° C Tj)
- Fully supported on NXP's 10 and 15-year Longevity Program

i.MX 8 applications processors are part of NXP's EdgeVerse™ [edge computing](#) platform.

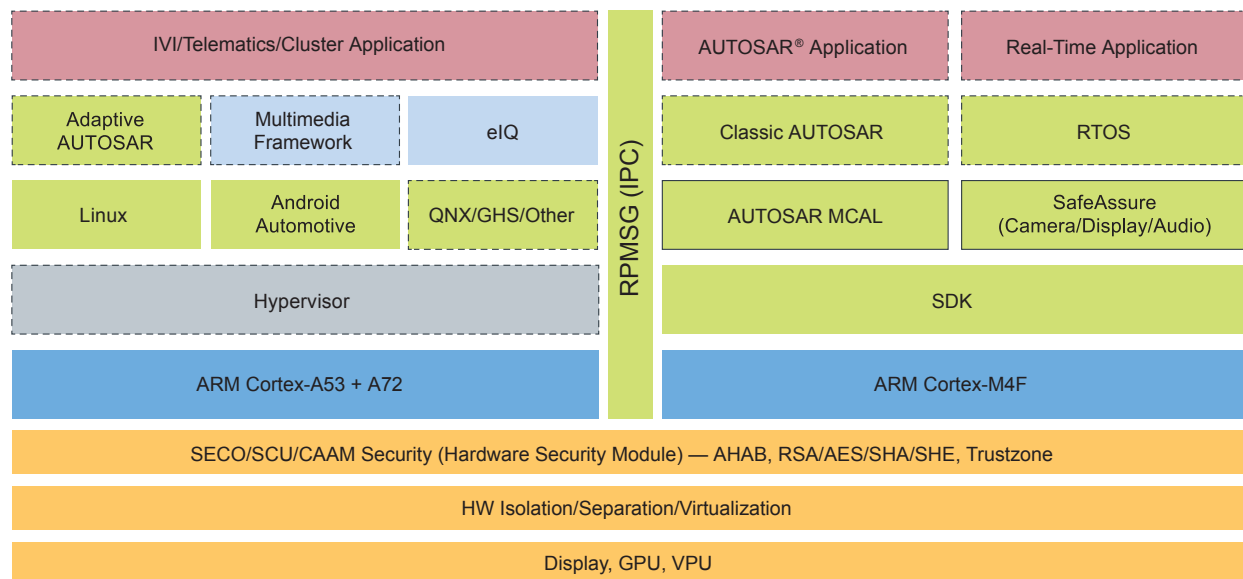
For additional information and sample availability, contact [NXP Support](#).

i.MX 8 Processors Block Diagram



Available on certain product families Note: Accessing muxable controller's full capabilities is dependent upon board component choices.

i.MX 8QM Software Ecosystem Block Diagram



Production Quality Third Party * Reference

View additional information for [i.MX 8 Family – Arm® Cortex®-A53, Cortex-A72, Virtualization, Vision, 3D Graphics, 4K Video](#).

Note: The information on this document is subject to change without notice.

www.nxp.com

NXP and the NXP logo are trademarks of NXP B.V. All other product or service names are the property of their respective owners. The related technology may be protected by any or all of patents, copyrights, designs and trade secrets. All rights reserved. © 2025 NXP B.V.