



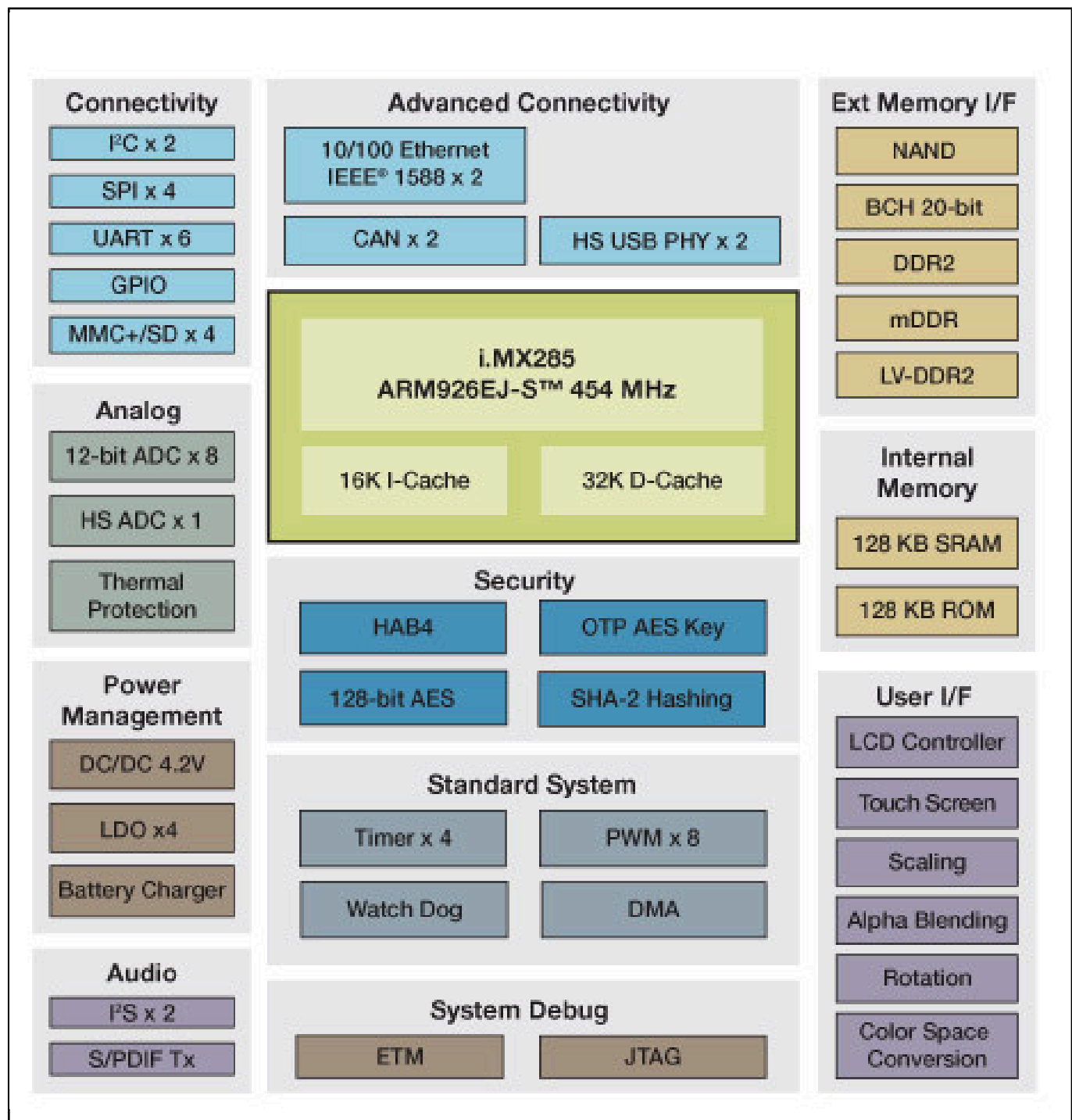
Multimedia Applications Processors – Integrated Power Management, Ethernet, Resistive Touch Screen, Arm9™ Core

i.MX285

Last Updated: Feb 19, 2025

The core of the i.MX285 is Our fast, proven, power-efficient implementation of the Arm926EJ-S™ core, with speeds of up to 454 MHz. With attention to overall system cost, i.MX28 integrates physical USB interfaces (PHY), a 10/100 Ethernet, power management, and a resistive touch screen display controller. This processor with full AEC –Q100 automotive qualification is ideal for automotive infotainment systems featuring display and touch screen control systems. As a result, the i.MX285 enables many of the features only available in high-end systems, but at a price point suitable for all vehicles.

i.MX285 Applications Processor Block Diagram Block Diagram



View additional information for [Multimedia Applications Processors - Integrated Power Management, Ethernet, Resistive Touch Screen, Arm9™ Core](#).

Note: The information on this document is subject to change without notice.

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