



Multimedia Applications Processors – Entry Level Automotive Applications, Low Power, Cost Effective, Arm9™ Core

i.MX251

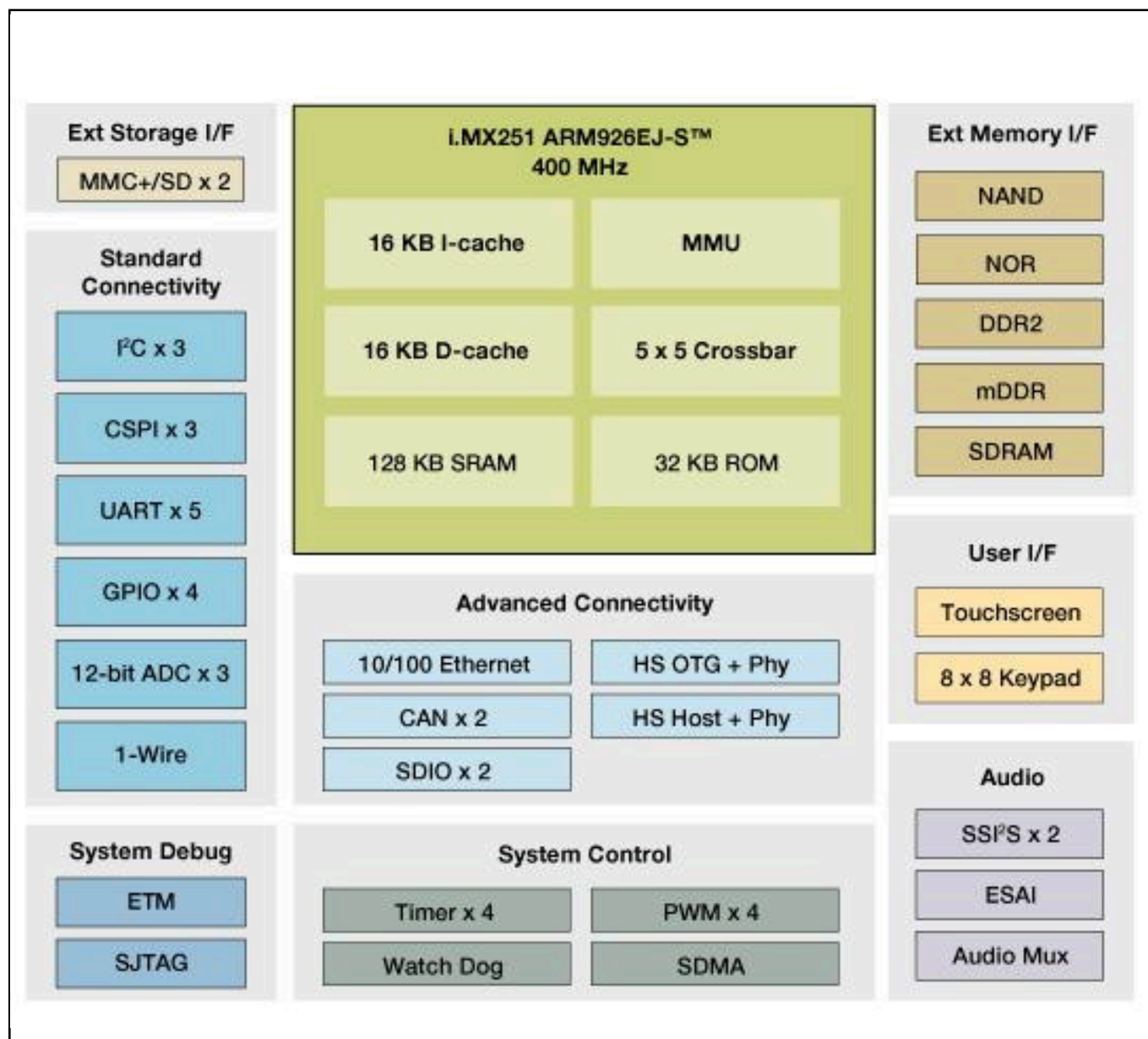
Last Updated: Dec 15, 2024

The Arm9™-based i.MX251 applications processor is for cost sensitive automotive applications.

The Arm926EJ-S™ CPU can run at speeds up to 400MHz giving enough headroom for entry level automotive applications while minimizing power consumption. Integration of two USB PHYs, a 12-bit ADC, low cost DDR2, MMC/SD/SDIO, 10/100 Ethernet with RMII, and on-chip SRAM reduces the Bill of Material (BOM) costs for price-sensitive applications.

The i.MX251 has the ability to connect with many different peripherals like Wi-Fi through SDIO or USB and Bluetooth via UART or SSI/I2S. With Enhanced Serial Audio Interface (ESAI) and CAN connectivity the i.MX251 is a cost effective solution targeted for audio connectivity.

i.MX251 Multimedia Applications Processor Block Diagram Block Diagram



View additional information for [Multimedia Applications Processors - Entry Level Automotive Applications, Low Power, Cost Effective, Arm9™ Core](#).

Note: The information on this document is subject to change without notice.

www.nxp.com

NXP and the NXP logo are trademarks of NXP B.V. All other product or service names are the property of their respective owners. The related technology may be protected by any or all of patents, copyrights, designs and trade secrets. All rights reserved. © 2025 NXP B.V.