



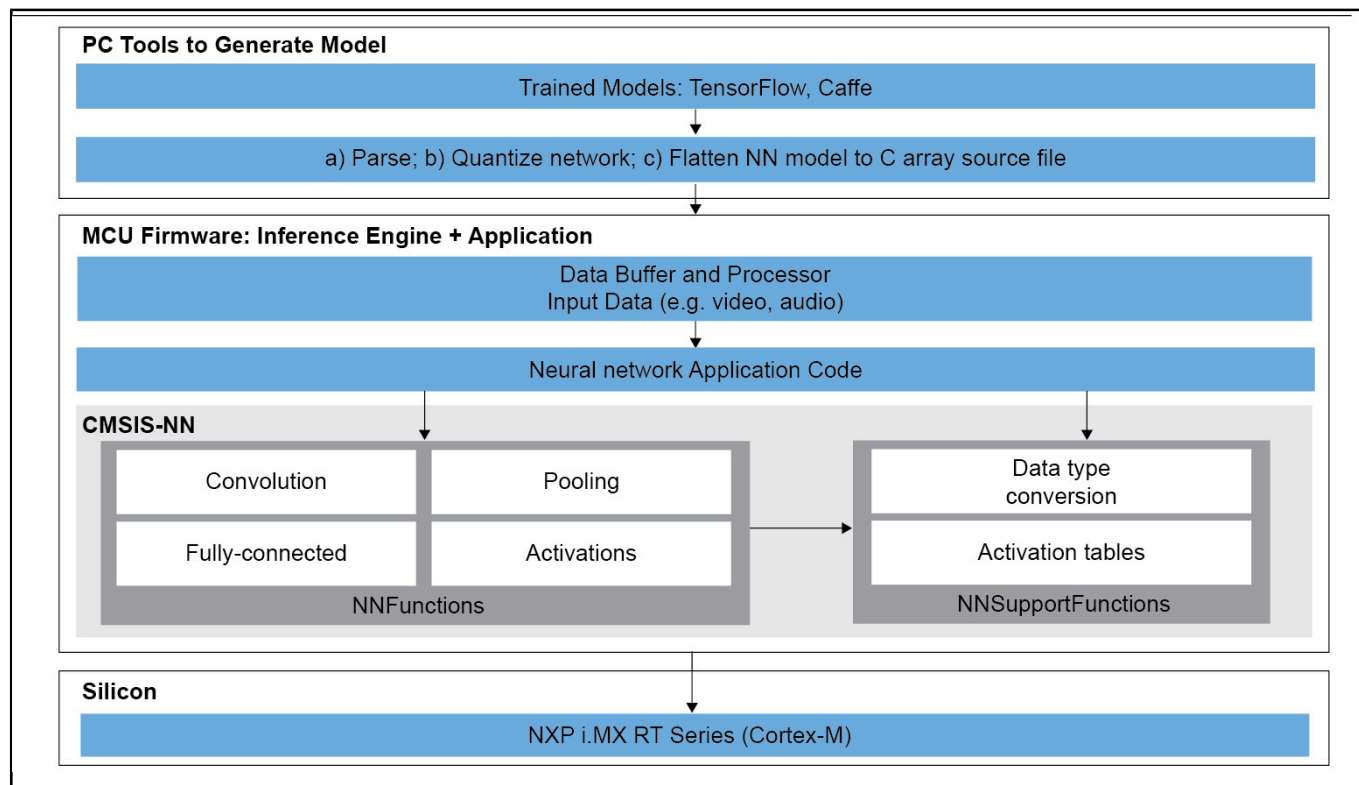
# eIQ<sup>®</sup> for Arm<sup>®</sup> CMSIS-NN

## eIQArmCMSISNN

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Arm CMSIS-NN is a collection of efficient neural network kernels used to maximize the performance and minimize the memory footprint of neural networks on Arm<sup>®</sup> Cortex<sup>®</sup>-M processor cores. For easy deployment, NXP eIQ integrates CMSIS-NN directly into MCUXpresso SDK, along with all the other Arm CMSIS components. The use of CMSIS-NN requires more manual intervention than TensorFlow Lite, but yields faster performance and small memory footprint, at the expense of a limited set of neural network operators. Therefore, some models are not supported. Arm also provides a conversion script to parse the network topology and automatically determine the appropriate functions to be used.

## eIQ for Arm® CMSIS-NN Block Diagram



View additional information for [eIQ® for Arm® CMSIS-NN](#).

**Note:** The information on this document is subject to change without notice.

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