



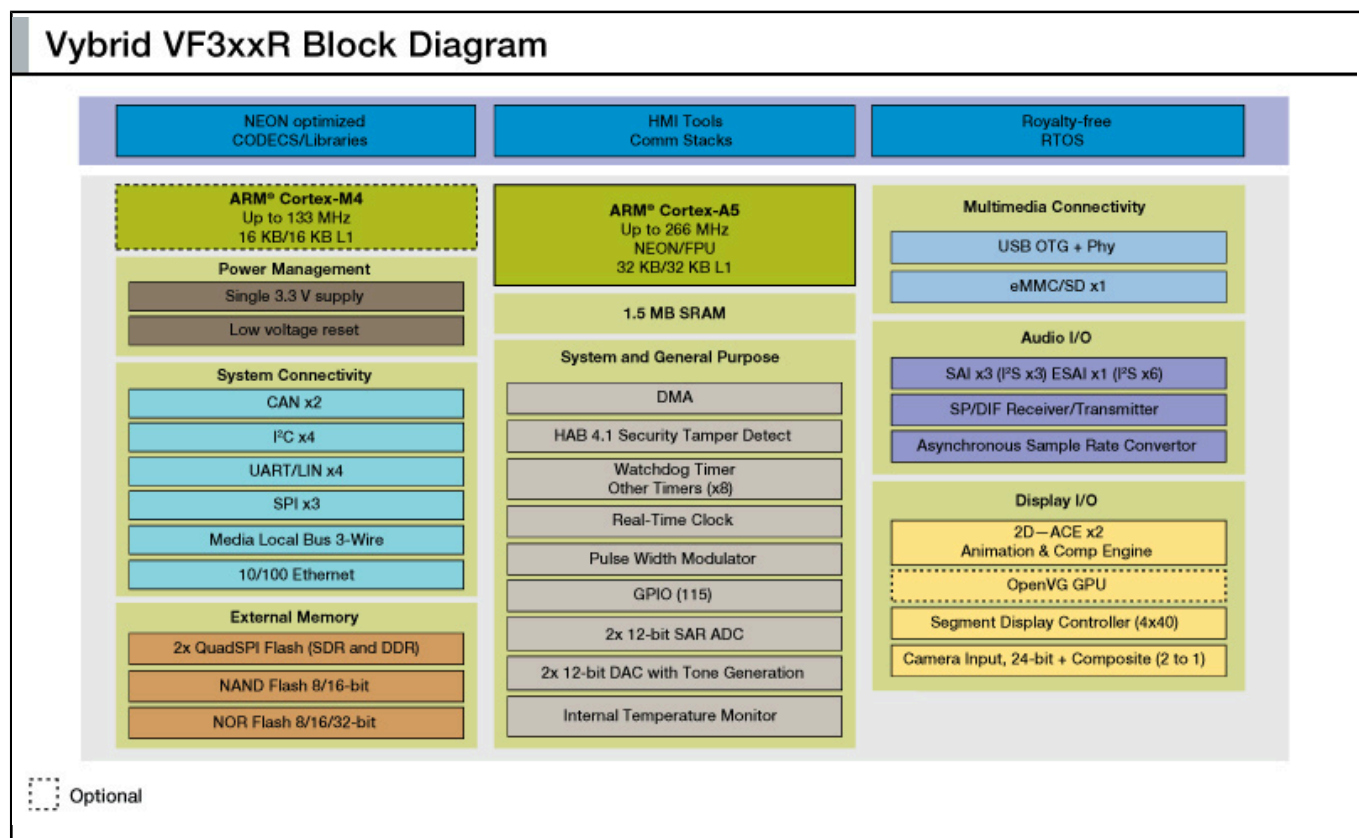
32-Bit Devices for Advanced Connected Radio, Entry-Level Infotainment and Digital Instrument Cluster Applications

VF3xxR

Last Updated: Dec 17, 2024

The VFxxx automotive VF3xxR is purpose-built and cost-optimized for instrument cluster applications. A dual-core (Arm® Cortex®-A5 + Cortex-M4) architecture handles both MCU and MPU tasks on a single chip. Generous 1.5 MB on-chip SRAM and multiple package options provide scalability from low-cost basic connected radios without external DRAM up to entry-level infotainment systems with dual displays and GPU-accelerated rich, compelling user interfaces.

Vfxxx R Series VF3xxR Block Diagram Block Diagram



View additional information for [32-Bit Devices for Advanced Connected Radio, Entry-Level Infotainment and Digital Instrument Cluster Applications](#).

Note: The information on this document is subject to change without notice.

www.nxp.com

NXP and the NXP logo are trademarks of NXP B.V. All other product or service names are the property of their respective owners. The related technology may be protected by any or all of patents, copyrights, designs and trade secrets. All rights reserved. © 2025 NXP B.V.