

# S32K3X4EVB-T172 Evaluation Board for Automotive General Purpose

## S32K3X4EVB-T172

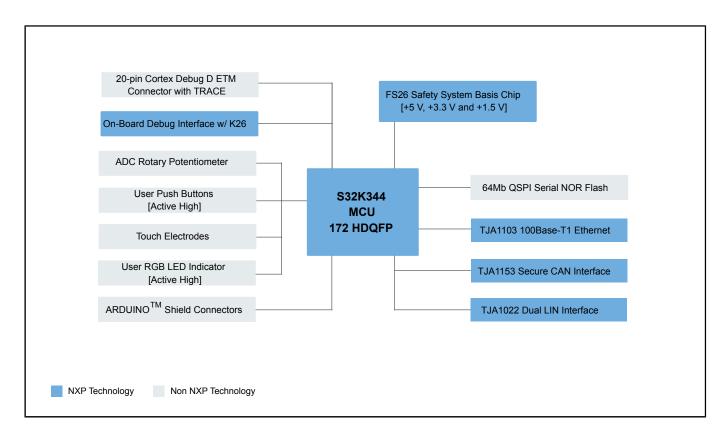
Last Updated: Dec 17, 2024

The S32K3X4EVB-T172 is an evaluation and development board for general-purpose industrial and automotive applications.

Based on the 32-bit Arm<sup>®</sup> Cortex<sup>®</sup>-M7 S32K3 MCU in a 172 HDQFP package, the S32K3X4EVB-T172 offers dual cores configured in lockstep mode, ASIL D safety hardware, HSE security engine, OTA support, advanced connectivity and low power.

The S32K3X4EVB-T172 offers a standard-based form factor compatible with the Arduino<sup>®</sup> UNO pin layout, providing a broad range of expansion board options for quick application prototyping and demonstration.

#### S32K3X4EVB-T172 Evaluation Board Block Diagram



### S32K3 Familiy Overview Block Diagram

Image: Control of the last state	K311 K312	K314	Common Features	K322	K324	K341	K342	K344	K328	K338	K348	K358	
148 Flam248 Flam448 Flam448 Flam448 Flam148 Flam448 Flam418 Fl	1 x Arm <sup>6</sup> Cortex <sup>6</sup> -M7 1x Cortex-M7 @120 MHz @240 MHz			2 x Cortex-M7 @240 MHz		1 Lockstep Cortex-M7 @ 240 MHz			2 x Cortex-M7 @ 240 MHz	3 x Cortex-M7 @ 240 MHz	1 LS Cortex-M7 @ 240 MHz	1 LS Cortex-M7 1 Cortex-M7 @ 240 MHz	
Image: constraint of the state of the st	1 MB Flash 2 MB Flash 4 MB Flash			2 MB Flash	4 MB Flash	1 MB Flash	2 MB Flash	4 MB Flash		8 MB	8 MB Flash		
Image: constraint of the constr	128 K SRAM 192 K SRAM	512 K SRAM		256 k SRAM	512 k SRAM	256 k SRAM	256 k SRAM	512 k SRAM	1152 KB SRAM	1152 KB SRAM	1152 KB SRAM	1152 KB SRAM	
16:ch, eDMA32-ch, eDMA3	up to 84 I/Os up to 143 I/Os	up to 218 I/Os		up to 143 I/Os up to 218 I/Os up to 143 I/Os up to 143 I/Os up to 218 I/Os up to 218 I/Os									
$ \begin{tabular}{ c c c c c c } \hline \begin{tabular}{ c c c c c c } \hline \begin{tabular}{ c c c c c c c } \hline \begin{tabular}{ c c c c c c c c } \hline \begin{tabular}{ c c c c c c c c c c c c c c c c c c c$	16-ch. eDMA	32-ch. eDMA	Modes and Peripherals	32-ch, eDMA 32-ch, eDMA									
Image: Second problem (TSN)CRC, Watchdogs)CRC, Watchdogs)C	3 x CAN (3 x FD) 6 x CAI	N (6 x FD)		4 x CAN (4 x FD)	6 x CAN (6 x FD)						8 x CAN (8 x FD)	8 x CAN (8 x FD)	
2 x FC2 x FC4 x SPL6 x SPL6 x SPL4 x SPL4 x SPL6 x SPL6 x SPL6 x SPL6 x SPL2 x 2 x b 1 12 b 1 x D 1 1 12 b 1 x	100 Mbit/s Ethernet (TSN)		ASIL B/D Safety: (ECC Memories, MPU, CRC, Watchdogs)	100 Mbit/s Ethernet (TSN)					1 Gbit/s Ethernet (TSN)				
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $	2 × FC 2 × FC			2 x FC	2 x FC	2 x FC	2 × FC	2 x I²C	2 x FC				
$ \frac{2 \times 24 \text{ch}}{12 \text{bit ADC}} = \frac{3 \times 24 \text{ch}}{12 \text{ch}} = 3 \times 24 $	4 x SPI*	6 x SPI*	Comparator, Logic Control Unit, Body Cross Triggering	4 x SPI*	6 x SPI*	4 x SPI*	4 x SPI*	6 x SP <b>I*</b>	6 x SPI*				
Image: second	2 x 24-ch. 12-bit ADC	3 x 24-ch. 12-bit ADC	Unit, Trigger Mux	2 x 24-ch. 12-bit ADC	3 x 24-ch. 12-bit ADC		2 x 24-ch. 12-bit ADC	3 x 24-ch. 12-bit ADC	3 x 24-ch. 12-bit ADC				
LQFP-48         HDQFP-172         Austration Drives (SURG)         HDQFP-172         HDQFP-172           HDQFP-10         HDQFP-100         HDQFP-100         HDQFP-100         HDQFP-100           MAPBGA-257         Social of F Washingtow (Subcome F Washingtow) (Subcome F Washingt	2 × SAI (PS)		JTAG	2 x SAI (FS) 2 x SAI (FS)									
LUPP-48         HDUPP-1/2         Real-Time Divers (AUTOSAR® and Non-AUTOSAR® and No			S32 Design Studio IDE										
HDGFP-100 MAPBGA-257 MAPBGA-25 MA			(AUTOSAR® and							HDQFI	P-172		
	HDQFP-100					HDUFP-100			MAPBQA-289				

View additional information for S32K3X4EVB-T172 Evaluation Board for Automotive General Purpose.

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