



S32 Design Studio for Power Architecture®

S32DS-PA

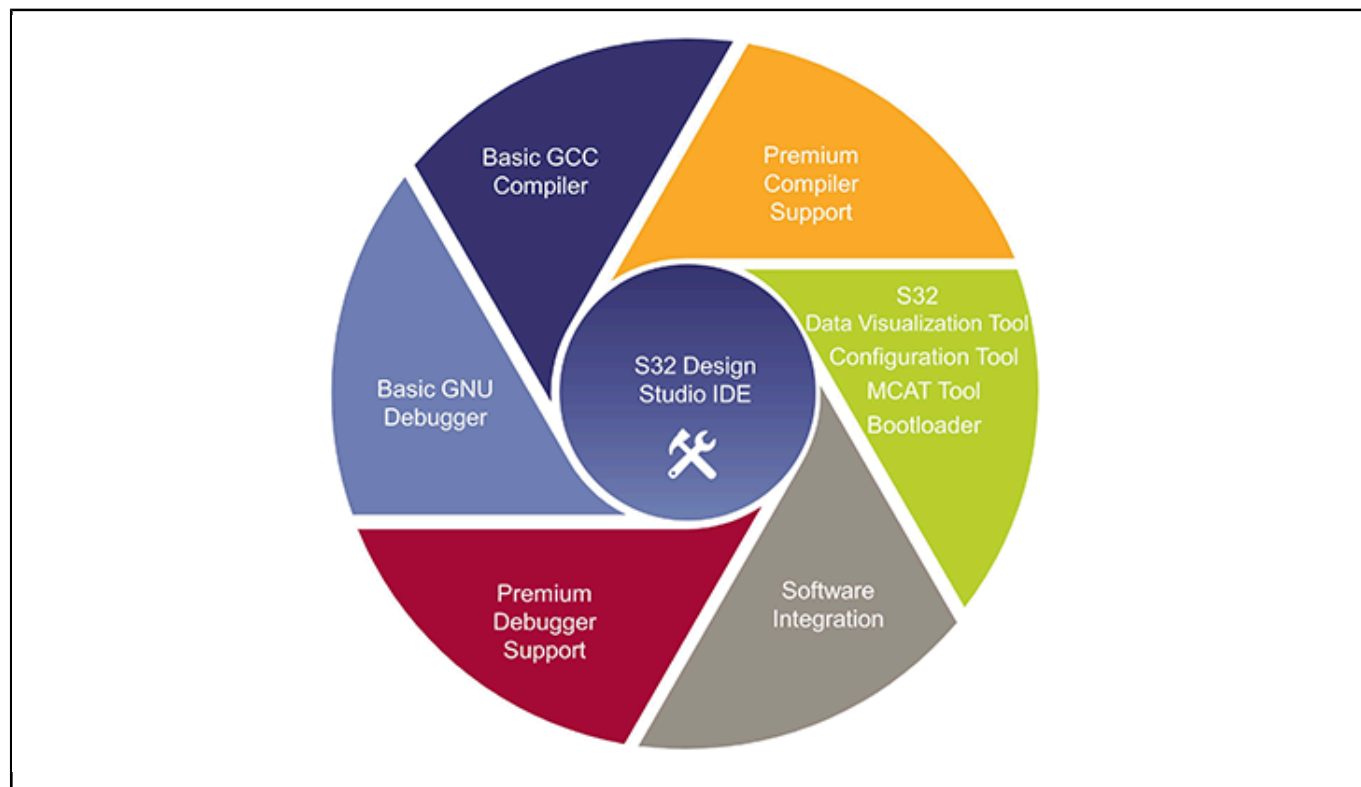
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The S32 Design Studio for Power Architecture® is a complimentary Integrated Development Environment (IDE) for automotive and ultra-reliable Power Architecture-based microcontrollers that enables editing, compiling, and debugging of designs.

The S32DS for Power Architecture offers designers a straightforward development tool with no code-size limitations, based on open-source software including Eclipse IDE, GNU Compiler Collection (GCC) and GNU Debugger (GDB).

NXP software, along with the S32 Design Studio IDE, provides a comprehensive enablement environment that reduces development time.

S32 Design Studio for Power Architecture Block Diagram



View additional information for [S32 Design Studio for Power Architecture®](#).

Note: The information on this document is subject to change without notice.

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