

S32 Debug Probe

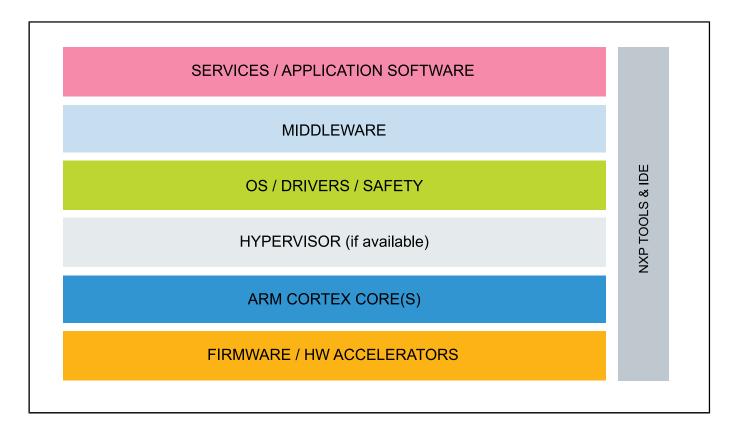
S32-DP

Last Updated: Apr 9, 2025

S32 Debug Probe enables NXP target system debugging via a standard debug port while connected to a developer's workstation via Ethernet or USB. S32 Debug Probe may also be referred to by industry-standard terms such as a probe, JTAG probe, JTAG emulator or target probe.

Using S32 Debug Probe's Ethernet connection, developers can debug systems remotely or effectively share a single system within a team. Alternatively Or, S32 Debug Probe connects directly to the developer's workstation with a single USB cable. S32 Debug Probe is designed to work in conjunction with S32 Design Studio and NXP Automotive Microcontrollers and Processors. It is a critical debug tool in all phases of the project.

Automotive General Block Diagram Block Diagram



View additional information for S32 Debug Probe.

Note: The information on this document is subject to change without notice.

www.nxp.com

NXP and the NXP logo are trademarks of NXP B.V. All other product or service names are the property of their respective owners. The related technology may be protected by any or all of patents, copyrights, designs and trade secrets. All rights reserved. © 2025 NXP B.V.