



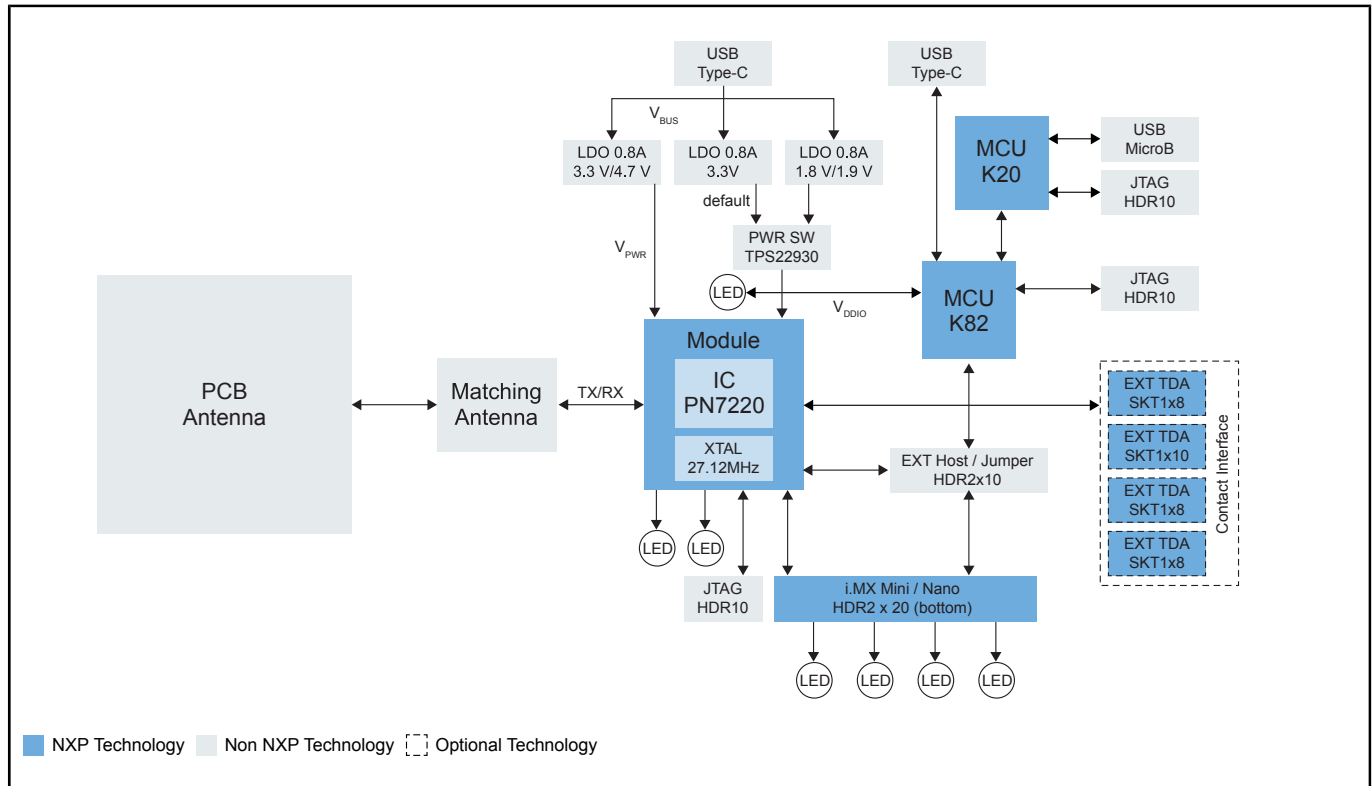
Development Board for PN7220 NFC Controller with Two Host Configuration

PNEV7220BP2

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PNEV7220BP2 is a flexible development board which can be connected to an i.MX 8 MCU running Android and a dedicated security MCU. Both CPUs are connected using their own host interface based on I²C (Android host) and SPI (security MCU host). The board is implementing the PN7221 which is backward-compatible with the PN7220. PNEV7220BP2 enables quick evaluation of NFC Forum, EMVCo 3.2 and Apple ECP applications. A TDA8035 evaluation board can be connected for contact card support.

Development Board for PN7220 Block Diagram Block Diagram



View additional information for [Development Board for PN7220 NFC Controller with Two Host Configuration](#).

Note: The information on this document is subject to change without notice.

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