9-Channel Power Management Integrated Circuit (PMIC) for High-Performance Processing Applications

PF8101-PF8201

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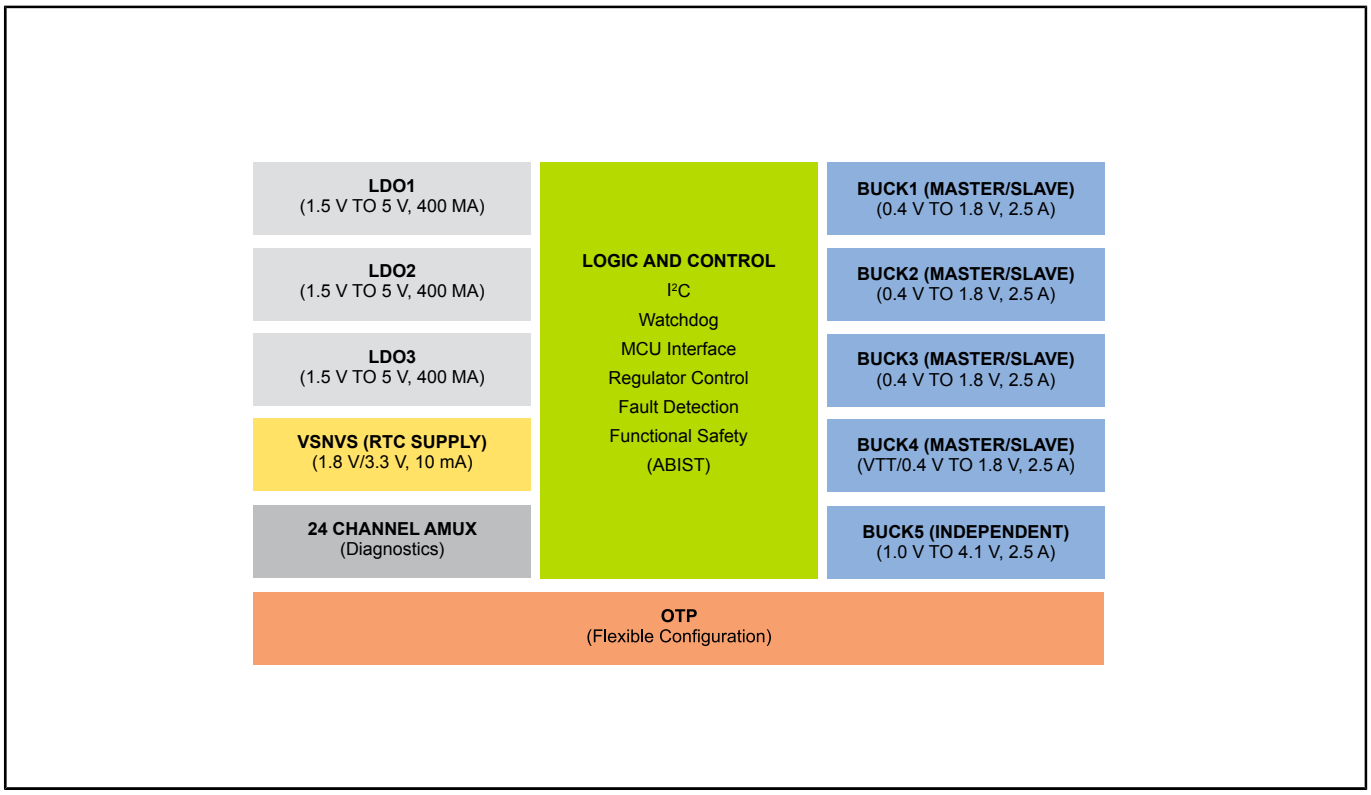
The PF8101/PF8201 is a power management integrated circuit (PMIC) designed for high-performance i.MX 8 based applications. It features five high-efficiency buck converters and three linear regulators for powering the processor, memory and miscellaneous peripherals.

Built-in one-time programmable memory stores key startup configurations, drastically reducing external components typically used to set output voltage and sequence of external regulators. Regulator parameters are adjustable through high-speed I²C after startup offering flexibility for different system states.

PF8101 and PF8201 Multi-Channel Block Diagram



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