Multi-Channel PMIC for Automotive Applications

PF5030

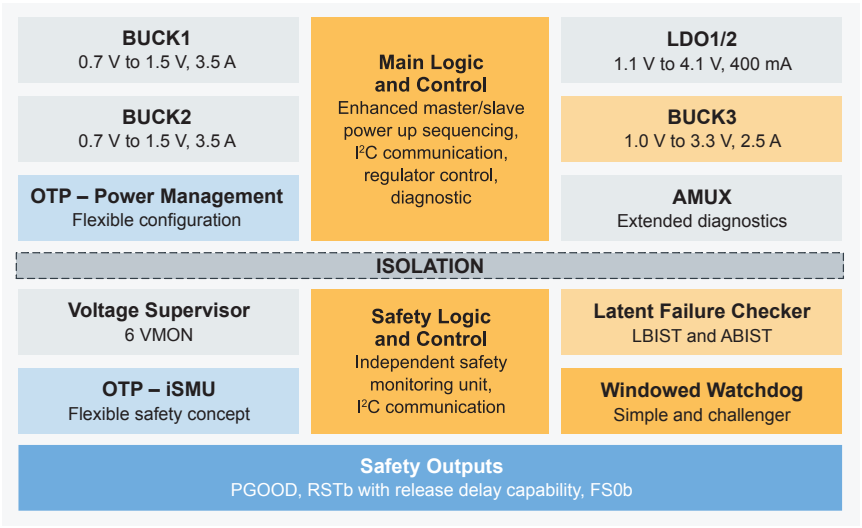
Last Updated: Apr 1, 2025

PF5030 silicon and enablement (documentation, software and boards) are available for selected customers (NDA required). For additional information and sample availability contact support or your local sales representative.

The PF5030 is a power management integrated circuit (PMIC) designed for S32Z2/E2 processors, ideally attached to NXP front system supply families (FS86, FS6x, other). Built-in OTP memory stores key startup configurations, high-speed I²C allows parameters adjustment after startup, offering flexibility for different system states.

The PF5030 is part of the BYLink system power platform enabling new smart approach to design safe system power management thanks to innovative synchronization feature. The PF5030 family offers scalability in power and safety, pin to pin and software compatible. It is developed in compliance with the ISO 26262 standard and qualified according to AEC-Q100 requirements.

PF5030 Block Diagram



View additional information for Multi-Channel PMIC for Automotive Applications.

Note: The information on this document is subject to change without notice.