



10-Channel Configurable PMIC for i.MX6 and i.MX7 Application Processors

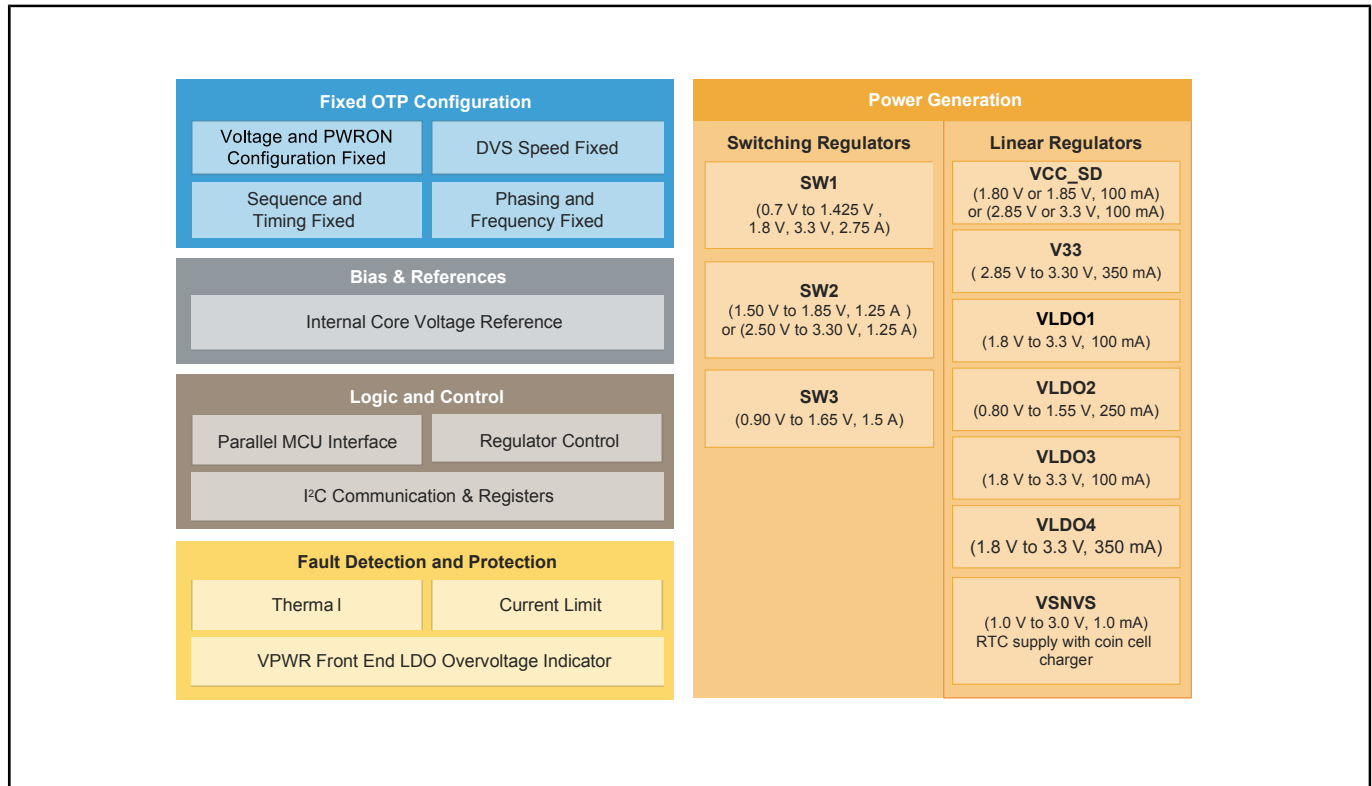
PF3001

Last Updated: May 25, 2023

The PF3001 power management integrated circuit (PMIC) features a configurable architecture that supports numerous outputs with various current ratings as well as programmable voltage and predefined sequencing. This enables the PF3001 to power the core processor, external memory and peripherals to provide a single-chip system power solution in multiple applications, reducing design complexity and lowering overall bill of materials. The high-performance architecture offers a cost-optimized solution for "always ON" applications while [PF3000](#) enables Low Power implementation in consumer and industrial environments.

The PF3001 is ideally suited for [i.MX6UL processors](#), and can also meet the power consumption requirements for another low end [i.MX 6 series](#), including the [i.MX 6Solo](#), [i.MX 6SoloLite](#), [i.MX 6SoloX](#), [i.MX7D](#) and [i.MX7S](#). The pin to pin compatibility with the PF3000 makes the i.MX and PF3000/1 solution scalable and allows to reuse the current Board Support Package (BSP) used in multiple reference designs. This provides customers with a platform-level solution from a single supplier to enable faster time to market and reduces engineering effort.

PF3001 Block Diagram



View additional information for [10-Channel Configurable PMIC for i.MX6 and i.MX7 Application Processors](#).

Note: The information on this document is subject to change without notice.

www.nxp.com

NXP and the NXP logo are trademarks of NXP B.V. All other product or service names are the property of their respective owners. The related technology may be protected by any or all of patents, copyrights, designs and trade secrets. All rights reserved. © 2024 NXP B.V.