



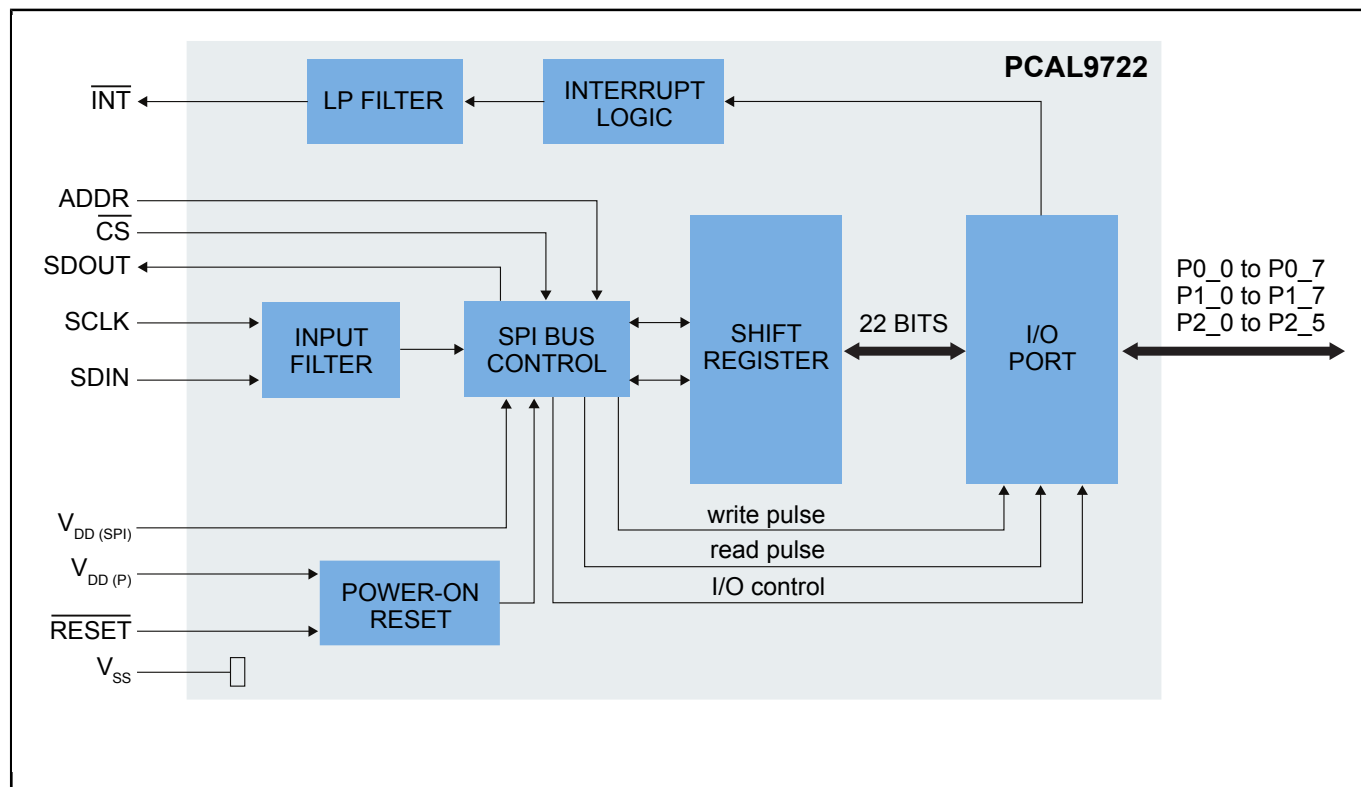
22-Bit SPI I/O Expander with Agile I/O Features

PCAL9722

Last Updated: Mar 10, 2025

The PCAL9722 is an ultra-low voltage 22-bit I/O expander and translator with SPI interface and is ideal for a wide range of applications that require expansion of inputs and outputs to their existing micro and reduces design cycle time. Integrated Agile I/O features further enable flexible and configurable designs with push-pull or open-drain outputs, interrupt status registers, four different output drive strengths, enable and resistor selector for pull-up and pull-down resistors and interrupt mask options.

PCAL9722 Block Diagram



View additional information for [22-Bit SPI I/O Expander with Agile I/O Features](#).

Note: The information on this document is subject to change without notice.

www.nxp.com

NXP and the NXP logo are trademarks of NXP B.V. All other product or service names are the property of their respective owners. The related technology may be protected by any or all of patents, copyrights, designs and trade secrets. All rights reserved. © 2025 NXP B.V.