8-Bit Microcontrollers with Accelerated Two Clock 80C51 Core 4 KB/8 KB 3 V Low-Power Flash with 8-Bit A/D Converter

P89LPC924FDH

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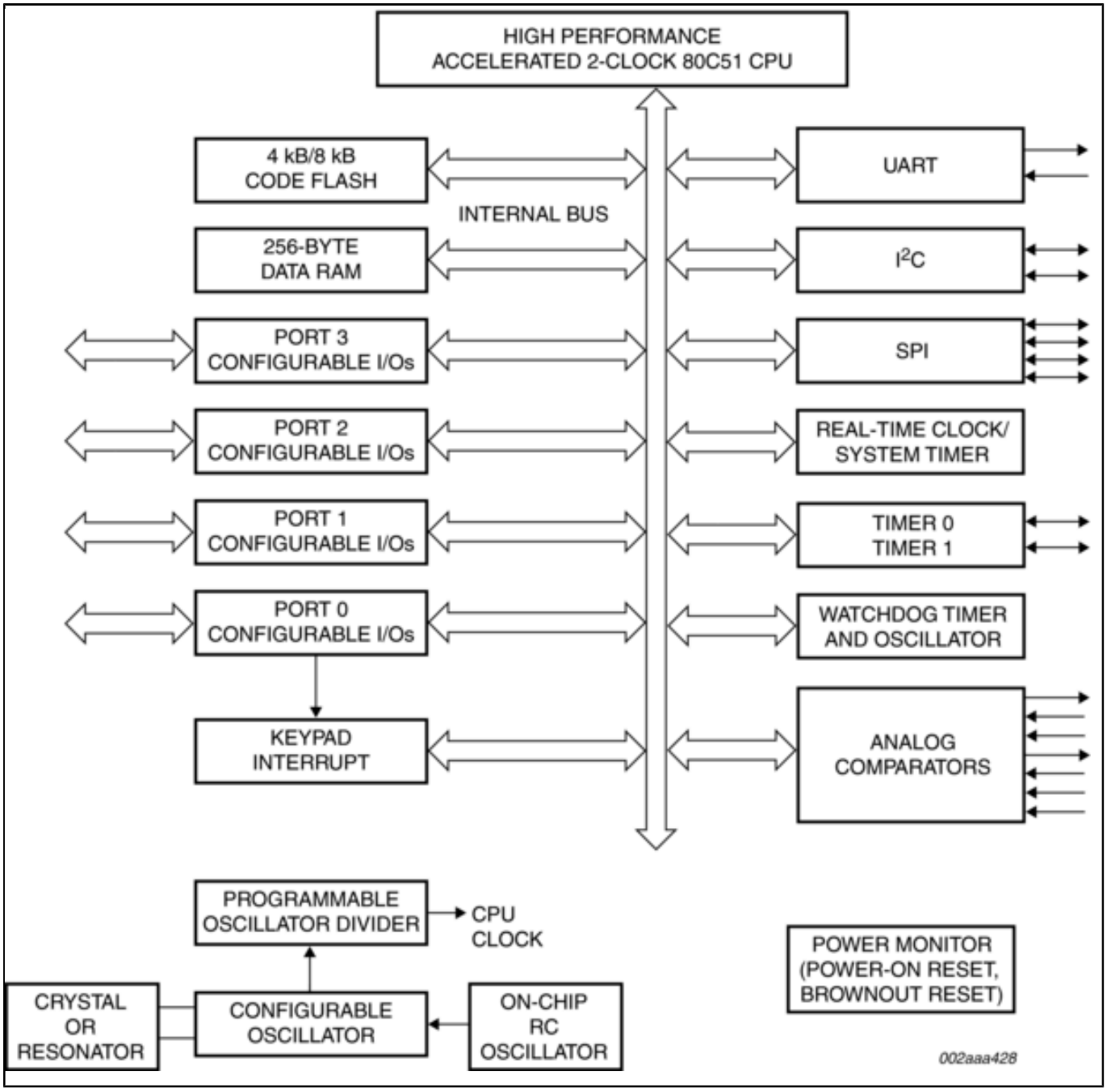
The P89LPC924/925 are single-chip microcontrollers designed for applications demanding high-integration, low cost solutions over a wide range of performance requirements. The P89LPC924/925 is based on a high performance processor architecture that executes instructions in two to four clocks, six times the rate of standard 80C51 devices. Many system-level functions have been incorporated into the P89LPC924/925 in order to reduce component count, board space, and system cost.

Block diagram: P89LPC930FDH, P89LPC931FDH Block Diagram



View additional information for 8-Bit Microcontrollers with Accelerated Two Clock 80C51 Core 4 KB/8 KB 3 V Low-Power Flash with 8-Bit A/D Converter.

Note: The information on this document is subject to change without notice.

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