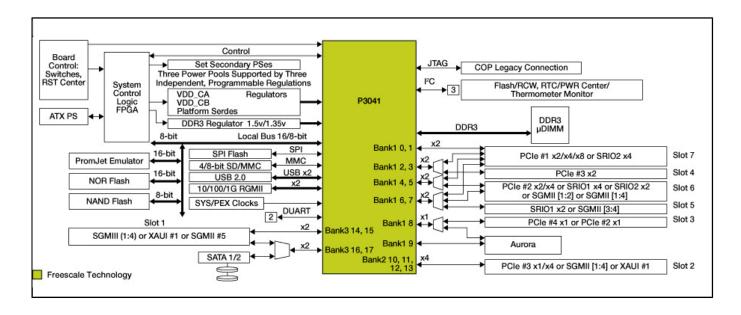


P3041 QorlQ® Development System P3041DS

Last Updated: Apr 30, 2025

The P3041DS is a flexible development system supporting the NXP® quad-core P3041 device. With its 1.5 GHz P3041 and rich input/output (I/O) mix, the board is intended for development of P3041 in networking and Ethernet-centric applications, such as control plane and mixed control plane/data plane in switches, routers, base station network interface cards, aerospace and defense and factory automation.

P3041DS BD Block Diagram



View additional information for P3041 QorlQ® Development System.

Note: The information on this document is subject to change without notice.

www.nxp.com

NXP and the NXP logo are trademarks of NXP B.V. All other product or service names are the property of their respective owners. The related technology may be protected by any or all of patents, copyrights, designs and trade secrets. All rights reserved. © 2025 NXP B.V.