

## Dual Low-Ohmic Single-Pole Single-Throw Analog Switch

## **NX3L2T66**

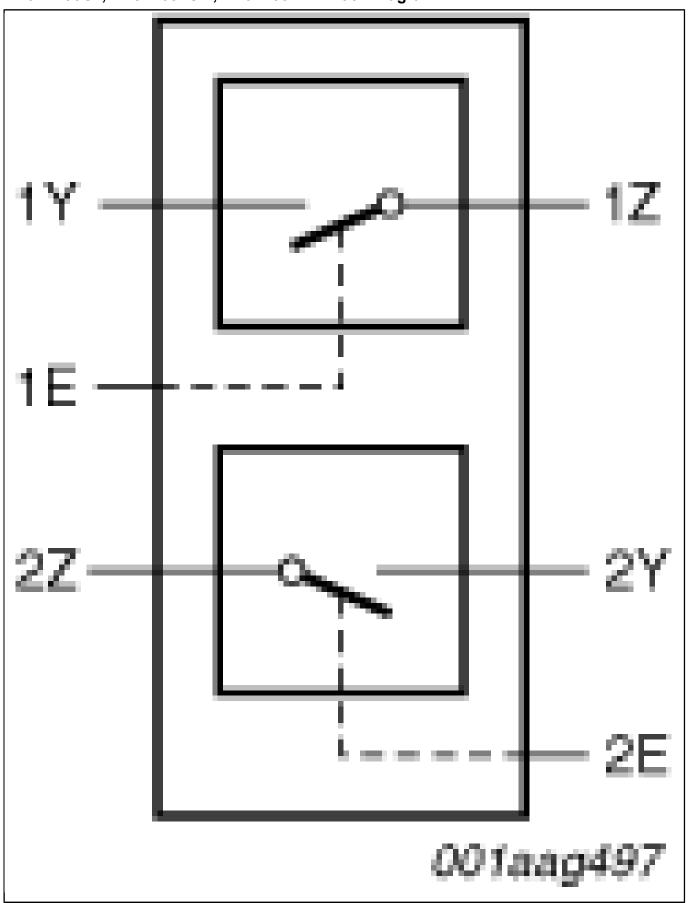
Last Updated: Dec 15, 2024

The NX3L2T66 is a dual low-ohmic single-pole single-throw analog switch. Each switch has two input/output terminals (nY and nZ) and an active HIGH enable input (nE). When pin nE is LOW, the analog switch is turned off.

Schmitt trigger action at the enable input (nE) makes the circuit tolerant to slower input rise and fall times. A low input voltage threshold allows pin nE to be driven by lower level logic signals without a significant increase in supply current ICC. This makes it possible for the NX3L2T66 to switch 4.3 V signals with a 1.8 V digital controller, eliminating the need for logic level translation.

The NX3L2T66 allows signals with amplitude up to VCC to be transmitted from nY to nZ; or from nZ to nY. Its low ON resistance (0.5  $\Omega$ ) and flatness (0.13  $\Omega$ ) ensures minimal attenuation and distortion of transmitted signals.

Block diagram: NX3L2G66GD, NX3L2G66GM, NX3L2G66GT, NX3L2T66GD, NX3L2T66GM, NX3L2T66GT, NX3L4684GM, NX3L4684TK Block Diagram



Note: The information on this document is subject to change without notice.
WAWAY DYD COM
<b>www.nxp.com</b> NXP and the NXP logo are trademarks of NXP B.V. All other product or service names are the property of their respective owners. The related technology may be protected by any or all of patents, copyrights, designs and trade secrets. All rights reserved. © 2025 NXP B.V.

View additional information for Dual Low-Ohmic Single-Pole Single-Throw Analog Switch.