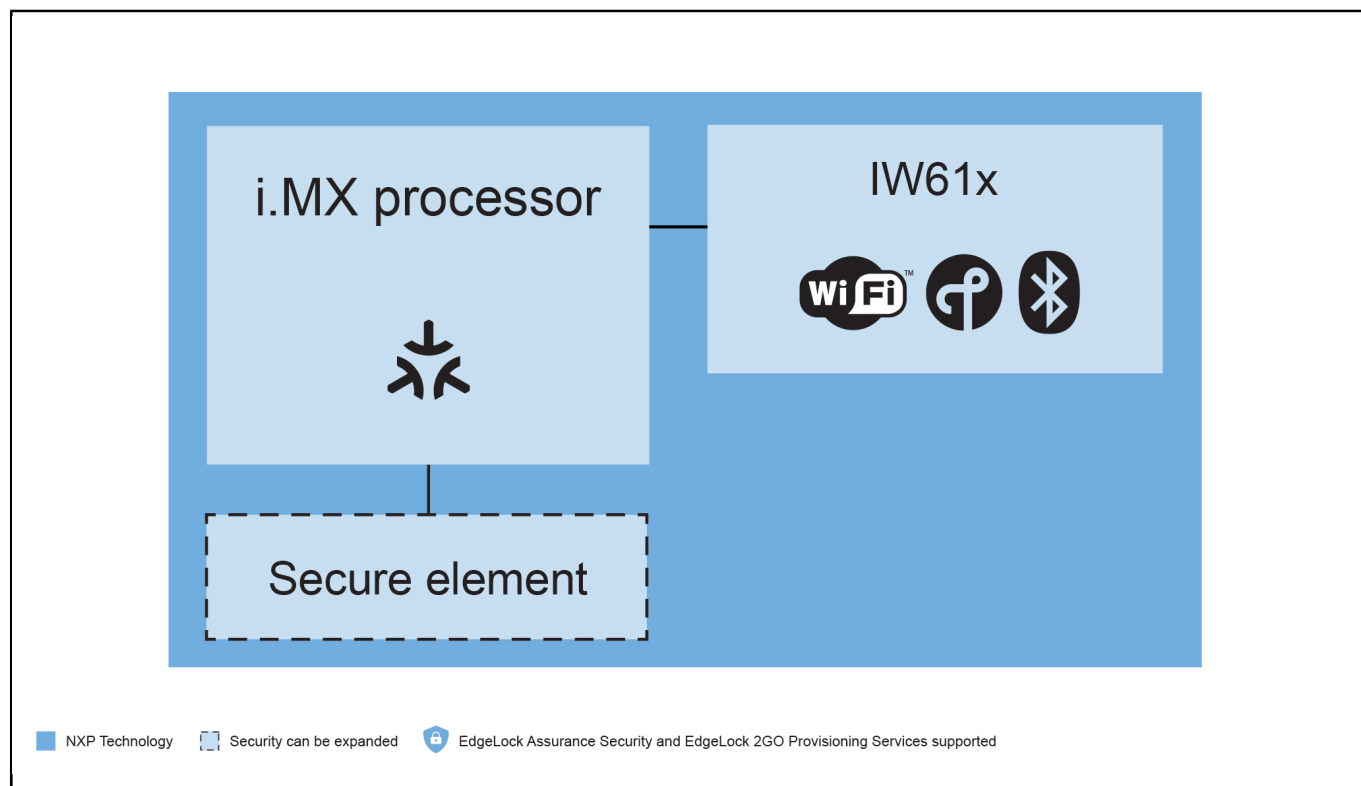# MPU / Linux® Hosted Matter Development Platform
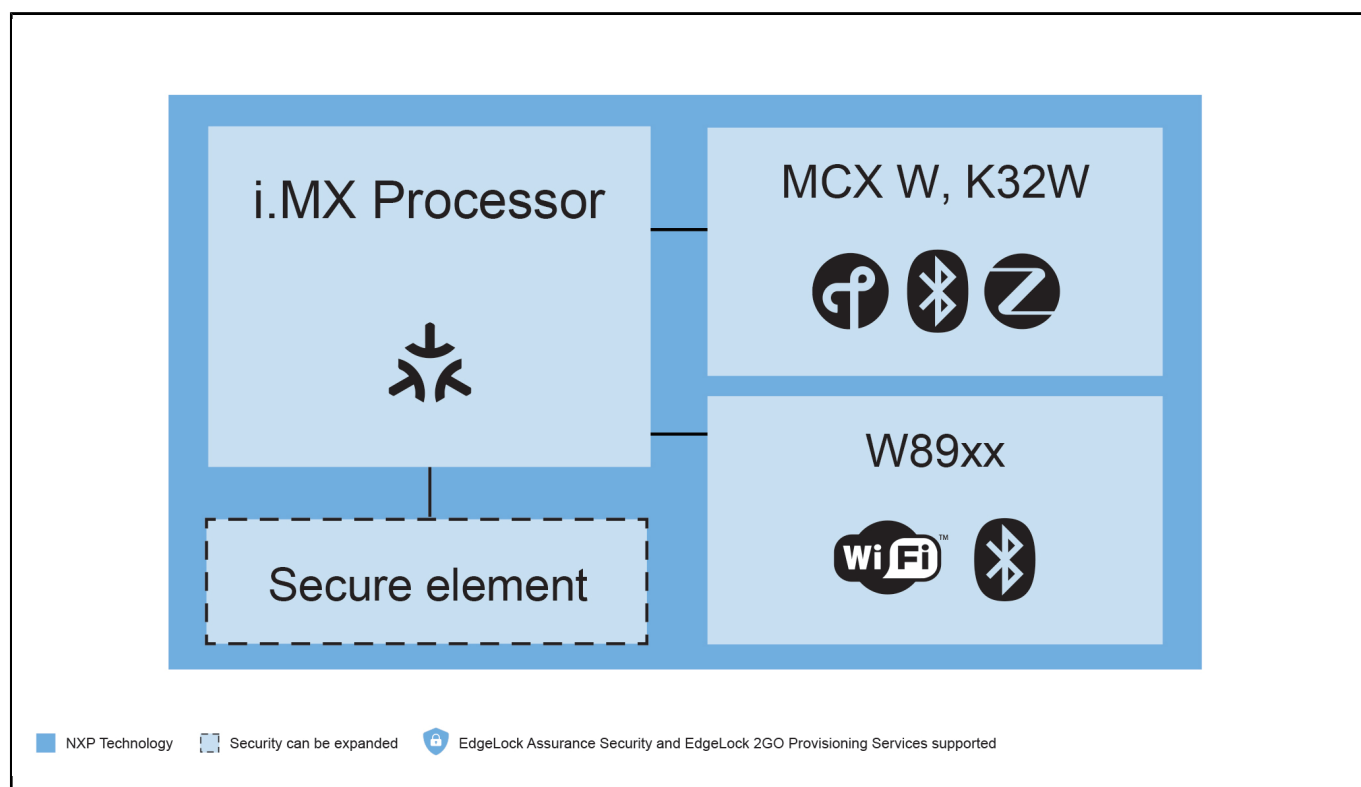
MPU-LINUX-MATTER-DEV-PLATFORM

Last Updated: Apr 5, 2025

NXP's MPU/Linux Hosted Matter Development platforms includes the hardware, software and enablement needed to create advanced edge devices using Matter over Wi-Fi and/or Thread that act as mesh extenders, Matter controllers and/or Thread Border Routers. These Development Platforms implement a hosted architecture using a range of the i.MX Linux portfolio along with the radio chip needed to support the targeted use case.

Integrated Matter Development Platform Block Diagram

i.MX processor	**IW61x** (Wi-Fi, Thread, Bluetooth)
Secure element	

NXP Technology   Security can be expanded   EdgeLock Assurance Security and EdgeLock 2GO Provisioning Services supported

Modular Matter Development Platform Block Diagram

i.MX Processor	**MCX W, K32W** (Thread, Bluetooth, Zigbee)
Secure element	**W89xx** (Wi-Fi, Bluetooth)

NXP Technology   Security can be expanded   EdgeLock Assurance Security and EdgeLock 2GO Provisioning Services supported

View additional information for MPU / Linux® Hosted Matter Development Platform.

Note: The information on this document is subject to change without notice.

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