PowerQUICC® II Processor with 128-ch. HDLC, UTOPIA II, 10/100 Ethernet

MPC8255

Not Recommended for New Designs

This page contains information on a product that is not recommended for new designs.

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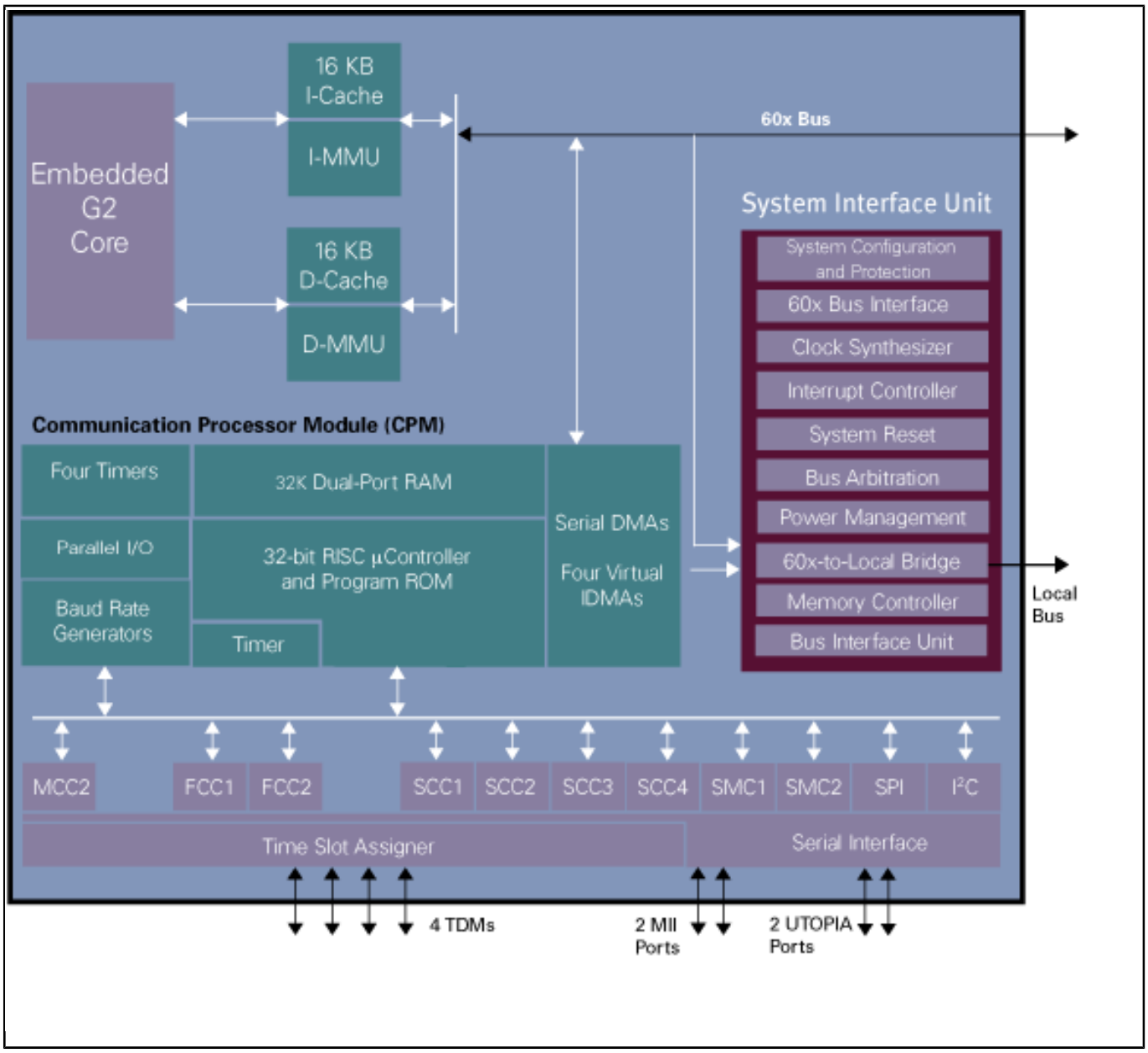
The PowerQUICC® II™ integrated communications processor family delivers excellent integration of processing power for networking and communications peripherals, providing customers with an innovative, total system solution for building high-end communications systems. NXP® Semiconductors's PowerQUICC II processor family is the next generation of the leading PowerQUICC™ line of integrated communications processors, providing higher performance in all areas of device operation, including greater flexibility, extended capabilities, and higher integration.

Our leading PowerQUICC architecture integrates two processing blocks. One block is a high-performance embedded G2 core and the second block is the Communications Processor Module (CPM). The CPM of the MPC8255 processor can support up to two fast serial communications controllers (FCCs), one multichannel controller (MCC), four serial communications controllers (SCCs), two serial management controllers (SMCs), one serial peripheral interface (SPI) and one I2C interface. The combination of the G2 core and the CPM, along with the versatility and performance of the PowerQUICC II processor family, provides customers with enormous potential in developing networking and communications products while significantly reducing time-to-market development stages.

MPC8255_BLKDIAG Block Diagram



View additional information for PowerQUICC® II Processor with 128-ch. HDLC, UTOPIA II, 10/100 Ethernet.

Note: The information on this document is subject to change without notice.