

# **MPC7448**

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The MPC7448 processor represents the most significant product update in the MPC74xx line of processors to date. The MPC7448 is our first high-performance processor manufactured on 90 nanometer silicon-on-insulator (SOI) process technology and continues our strong legacy of providing Power Architecture products with extensive processing performance at very low power. The MPC7448 is designed to perform between 600 MHz and in excess of 1.5 GHz, contains a full megabyte of L2 cache and offers enhanced power management capabilities. MPC7448 processors are ideal for leading-edge pervasive computing, embedded network control and signal processing applications.

### AltiVec® Engine Acceleration

The MPC7448 includes the same powerful 128-bit AltiVec vector execution unit as found in previous MPC74xx devices, but with the enhanced support for out-of-order instructions. AltiVec technology may dramatically enhance the performance of applications such as voice over IP, speech recognition, multi-channel modems, virtual private network servers, high-resolution 3D graphics, motion video (MPEG®2, MPEG4), high fidelity audio (3D audio, AC-3), and so on. AltiVec computational instructions are executed in the four independent, pipelined AltiVec execution units. A maximum of two AltiVec instructions can be issued in order to any combination of AltiVec execution units per clock cycle. In the MPC7448, a maximum of two AltiVec instructions can be issued out-of-order to any combination of AltiVec execution units per clock cycle from the bottom two AltiVec instruction queue entries. For example, an instruction in queue 1 destined for AltiVec integer unit 1 does not have to wait for an instruction in queue 0 that is stalled behind an instruction waiting for operand availability.

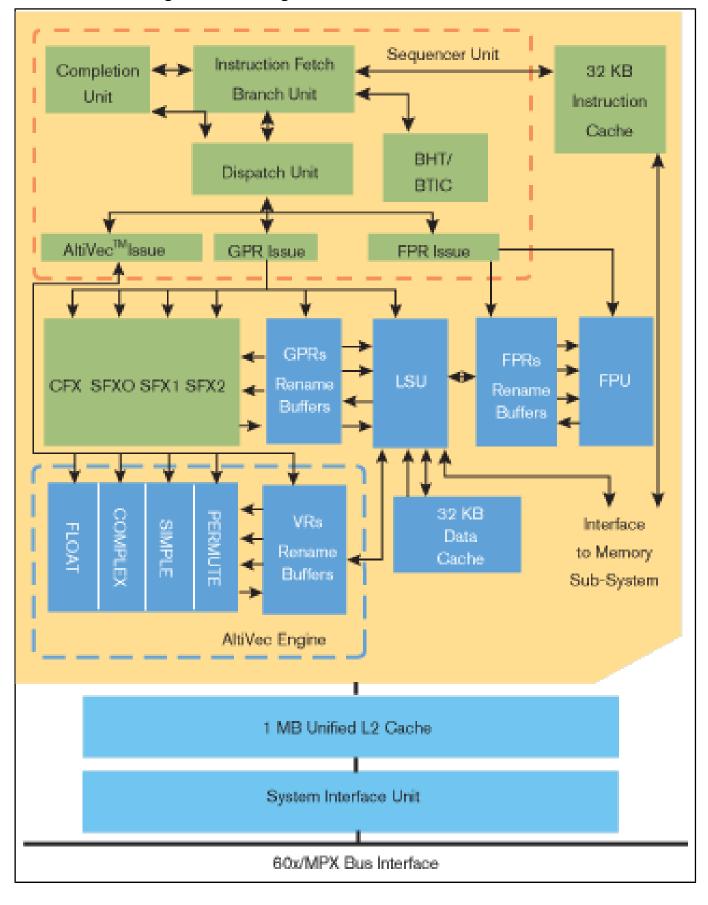
## **Power Management**

Continuing to pursue lower and lower power consumption is a keen focus with the NXP® family of Power Archtitecture processors, and the MPC7448 is no exception. Power management features include:

- Expanded Dynamic Frequency Switching (DFS) enabling software to change power consumption
- Nap and Sleep modes
- Voltage scaling down to at least 1.0 volt
- Added benefits of 90-nm technology:
  - Multi-Vt and Triple Gate Oxide integrated transistors for low standby power
  - Low-K Dielectric for high performance with reduced power and noise

• Temperature sensing diodes included to monitor die temperature

## MPC7448 Block Diagram Block Diagram



Note: The information on this document is subject to change without notice.

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