



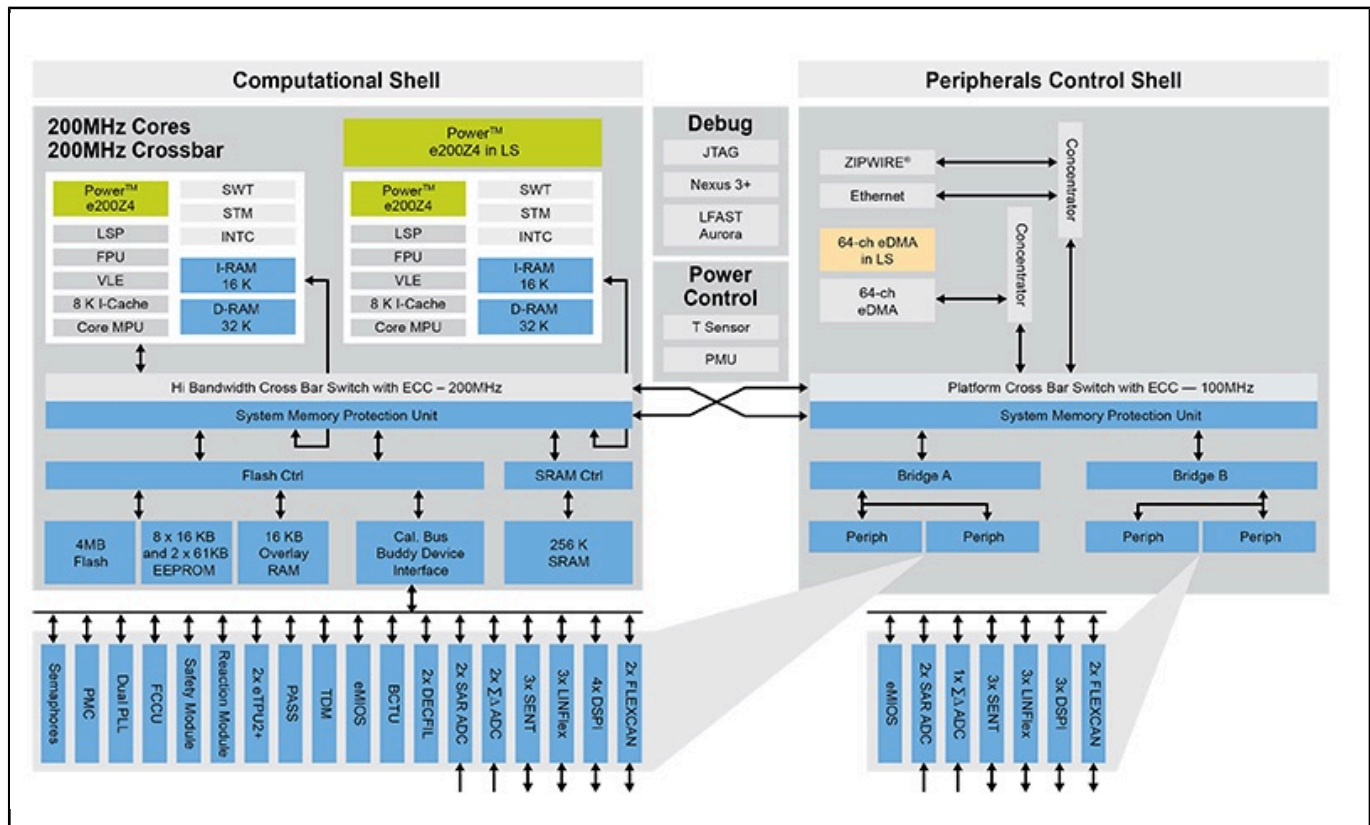
Automotive and Industrial Engine Management MCU

MPC5746R

Last Updated: Mar 7, 2025

The MPC5746R Power Architecture® MCU targets industrial and automotive engine/transmission control applications that require advanced performance, timing systems and functional safety capabilities. The family offers a high performance multi-core architecture with industry standard eTPU based timer system. Scalable Flash, core configurations and packaging options are available along with the highest level of functional safety (ASIL D) support.

MPC5746R 4M Block Diagram Block Diagram



View additional information for [Automotive and Industrial Engine Management MCU](#).

Note: The information on this document is subject to change without notice.

www.nxp.com

NXP and the NXP logo are trademarks of NXP B.V. All other product or service names are the property of their respective owners. The related technology may be protected by any or all of patents, copyrights, designs and trade secrets. All rights reserved. © 2025 NXP B.V.