

32 Bit Microcontroller

MPC566

Not Recommended for New Designs

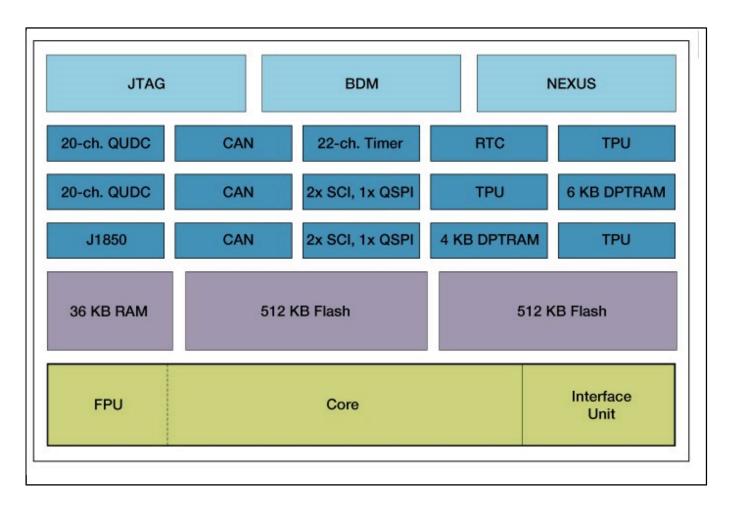
This page contains information on a product that is not recommended for new designs.

Last Updated: Apr 9, 2022

The MPC566 integrates a Power Architecture® core with a dual precision floating point unit and BCC, 36 KB of RAM and 1 MB of flash memory.

The MPC566, an upgraded version of the MPC565, offers code compression to enable more efficient use of internal or external flash memory. Code compression is optimized for automotive (non-cached) applications and the new scheme increases performance by 40 to 50 percent.

MPC566 Block Diagram Block Diagram



View additional information for 32 Bit Microcontroller.

Note: The information on this document is subject to change without notice.

www.nxp.com

NXP and the NXP logo are trademarks of NXP B.V. All other product or service names are the property of their respective owners. The related technology may be protected by any or all of patents, copyrights, designs and trade secrets. All rights reserved. © 2025 NXP B.V.