



Ultra-Reliable Dual-Core 32-bit MCU for Automotive and Industrial Applications

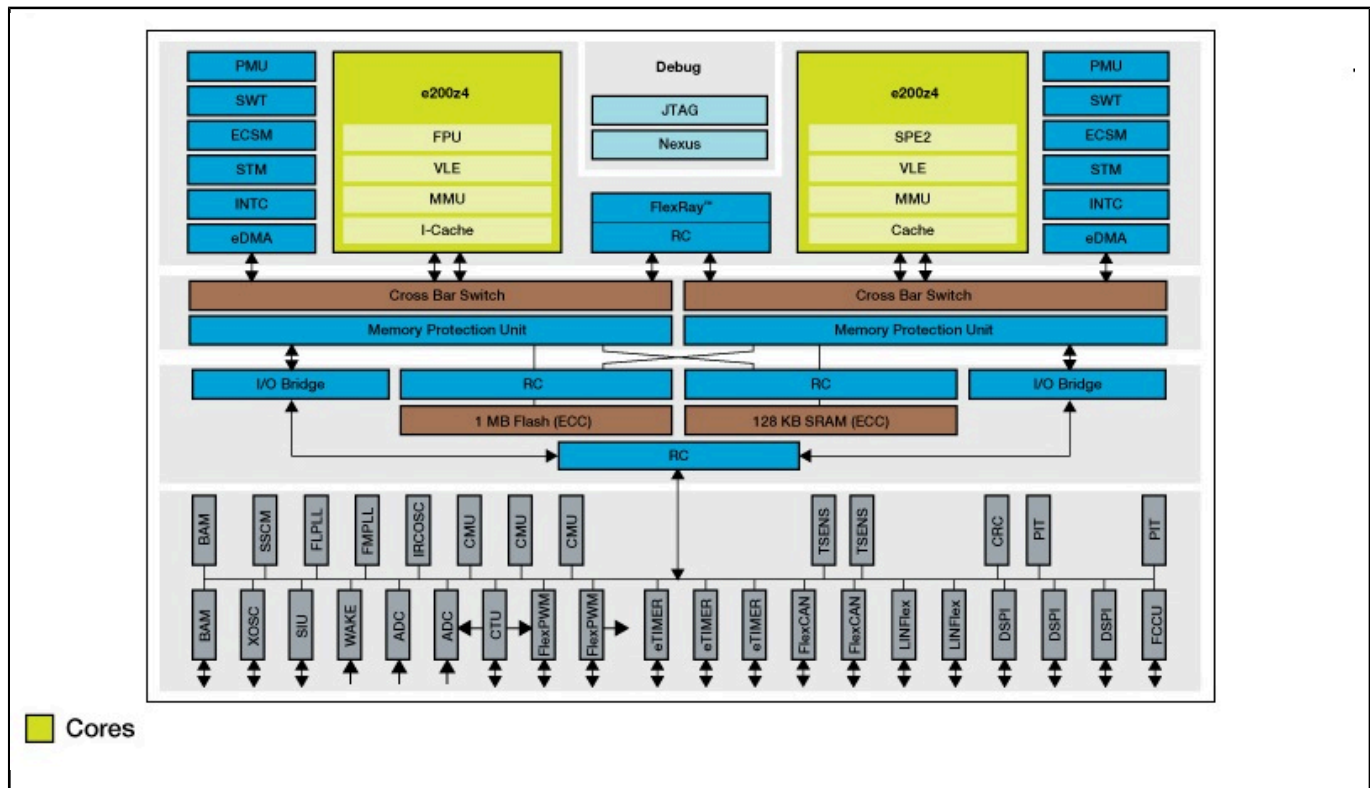
MPC564xL

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The MPC5643L 32-bit MCU built on Power Architecture® technology reduces design complexity and component count by putting key functional safety features on a single chip. It specifically addresses the ISO 26262 safety standard up to the most stringent ASIL D requirements.

- Dual-core, dual-issue architecture enables more performance while using less power
- Lockstep mode provides a software environment for redundant processing and calculations
- Decoupled parallel mode supports applications needing maximum performance or software diversity
- Precise and deterministic control timing for real-time applications, such as motor control
- A [SafeAssure® functional safety solution](#)

MPC564XL Microcontroller Block Diagram Block Diagram



View additional information for [Ultra-Reliable Dual-Core 32-bit MCU for Automotive and Industrial Applications](#).

Note: The information on this document is subject to change without notice.

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