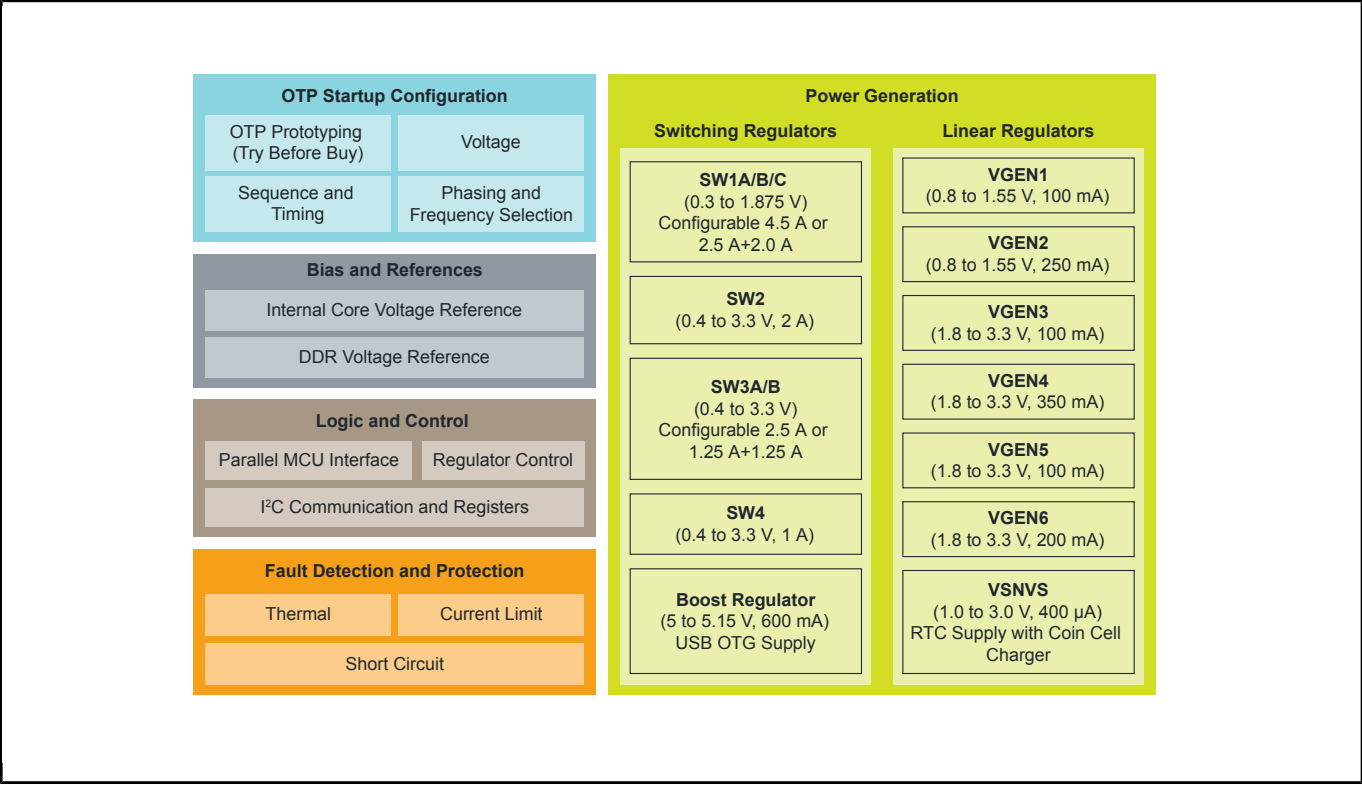# 14-Channel Configurable PMIC

MMPF0100

Last Updated: Mar 24, 2025

The MMPF0100 is the first of the PF series of our PMICs, featuring:

- Configurable and programmable architecture supporting outputs with various current ratings to power the core processor, memory and a wide range of peripherals
- Integration of full system-level power management functions to simplify system design
- Optimized performance architecture for efficient, advanced functionality for consumer, industrial and automotive applications

The MMPF0100 is suited to all i.MX 6 processors: i.MX 6SoloX, i.MX 6SoloLite, i.MX 6Solo, i.MX 6DualLite, i.MX 6Dual, i.MX 6Quad, i.MX 6DualPlus and i.MX 6QuadPlus. It is built into multiple reference designs in the i.MX 6 family to provide a platform-level solution from that enables faster time to market.

MMPF0100 Block Diagram

OTP Startup Configuration

OTP Prototyping (Try Before Buy)	Voltage
Sequence and Timing	Phasing and Frequency Selection

Bias and References

Internal Core Voltage Reference

DDR Voltage Reference

Logic and Control

Parallel MCU Interface	Regulator Control

I²C Communication and Registers

Fault Detection and Protection

Thermal	Current Limit

Short Circuit

Power Generation

Switching Regulators

SW1A/B/C
(0.3 to 1.875 V)
Configurable 4.5 A or
2.5 A+2.0 A

SW2
(0.4 to 3.3 V, 2 A)

SW3A/B
(0.4 to 3.3 V)
Configurable 2.5 A or
1.25 A+1.25 A

SW4
(0.4 to 3.3 V, 1 A)

Boost Regulator
(5 to 5.15 V, 600 mA)
USB OTG Supply

Linear Regulators

VGEN1
(0.8 to 1.55 V, 100 mA)

VGEN2
(0.8 to 1.55 V, 250 mA)

VGEN3
(1.8 to 3.3 V, 100 mA)

VGEN4
(1.8 to 3.3 V, 350 mA)

VGEN5
(1.8 to 3.3 V, 100 mA)

VGEN6
(1.8 to 3.3 V, 200 mA)

VSNVS
(1.0 to 3.0 V, 400 µA)
RTC Supply with Coin Cell
Charger

View additional information for 14-Channel Configurable PMIC.

Note: The information on this document is subject to change without notice.