



SBC Gen2 with High-Speed CAN and LIN

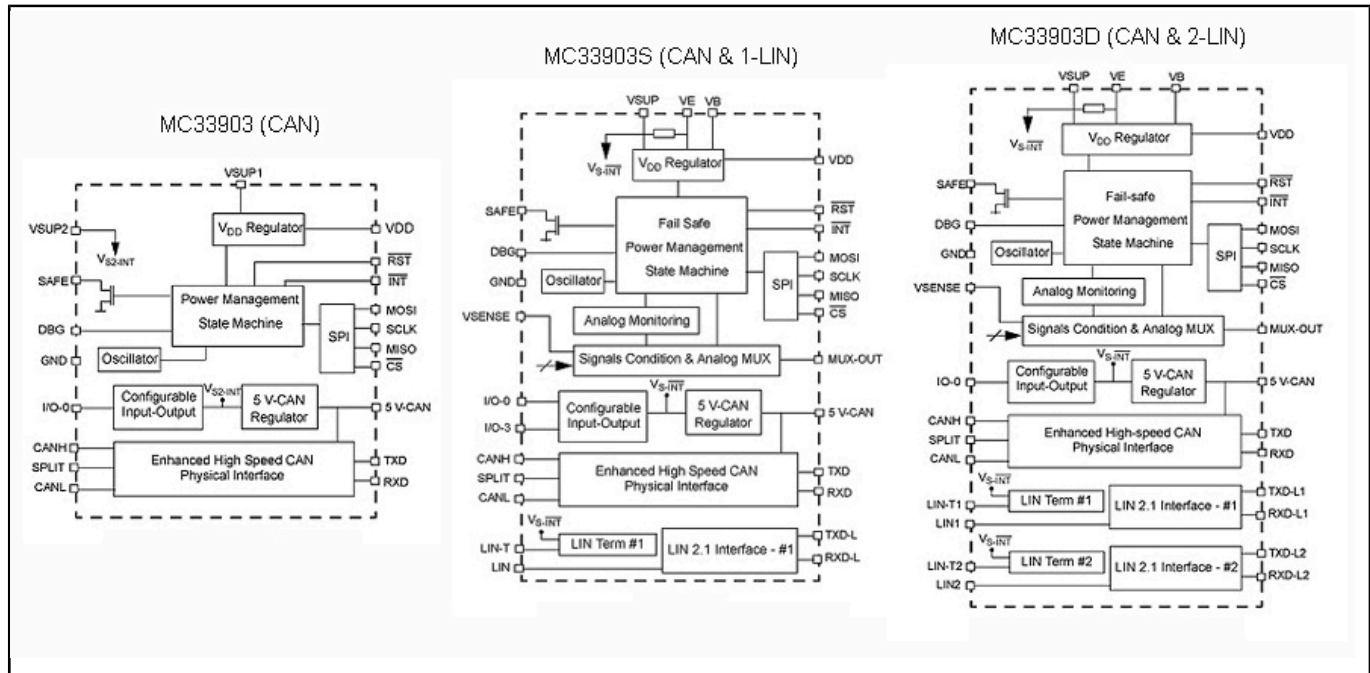
MC33903

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The NXP MC33903 is part of a second generation family of system basis chips, combining multiple features and enhanced module design.

- The MC33903 works as an advanced power management unit for the MCU and additional integrated circuits such as sensors and CAN transceivers
- Built-in enhanced high-speed CAN interface (ISO11898-2 and -5), with local and bus failure diagnostics, protection and fail-safe operation mode.
- The SBC may include zero, one or two LIN 2.1 interfaces with LIN output pin switches.

MC33903 Battery Management Block Diagram



View additional information for [SBC Gen2 with High-Speed CAN and LIN](#).

Note: The information on this document is subject to change without notice.

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