



Layerscape® 1043A and 1023A Processors

LS1043A

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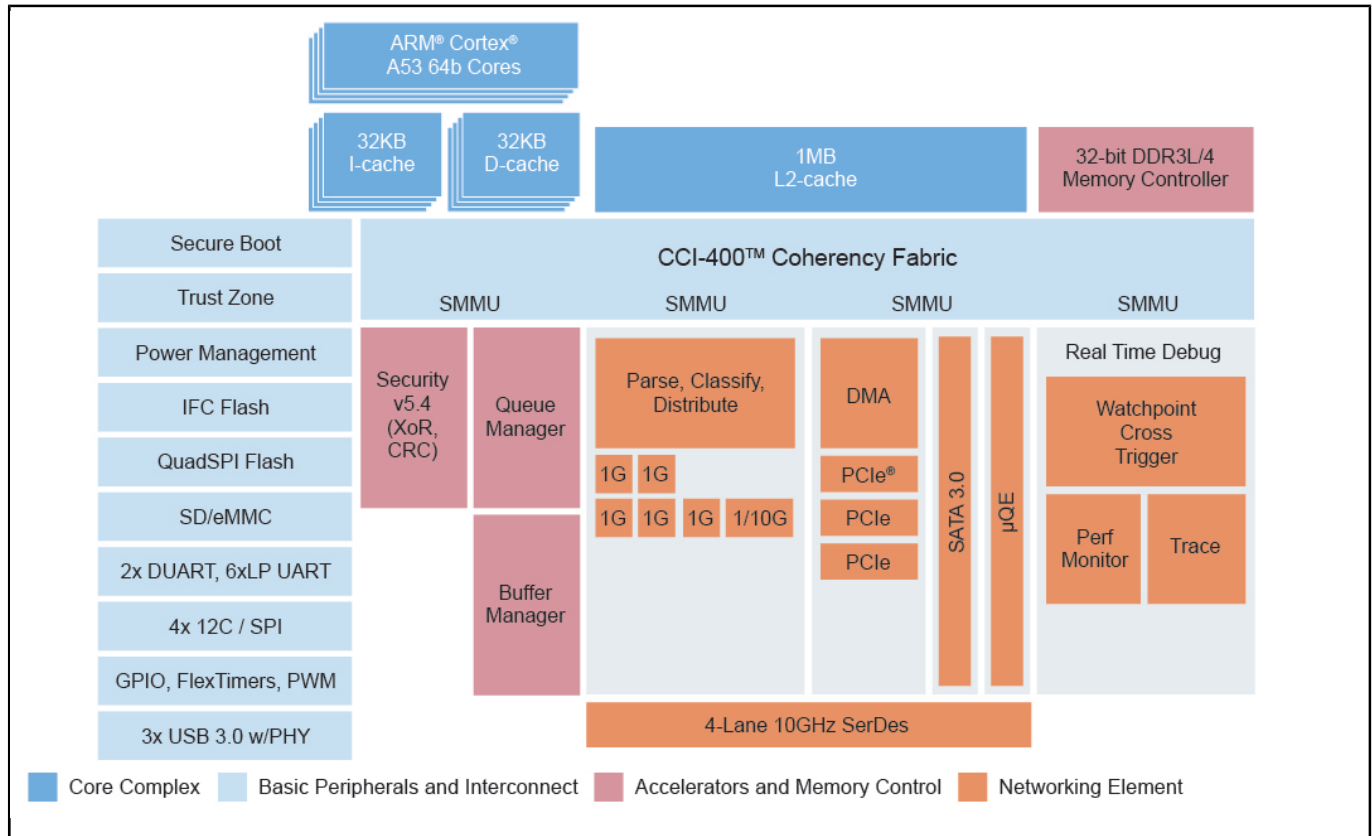
The LS1043A processor was NXP's first quad-core, 64-bit Arm®-based processor for embedded networking. LS1023A (two-core version) and LS1043A (four-core version) deliver greater than 10 Gbps of performance in a flexible I/O package supporting fanless designs. This SoC is a purpose-built solution for small-form-factor networking, industrial and automotive applications with BOM optimizations for economical low layer PCB, lower-cost power supply and single clock design.

The new 0.9V versions of the LS1043A and LS1023A deliver additional power savings for applications such as Wireless LAN and to Power over Ethernet systems. The 23x23 package options allow for pin-compatible design, enabling scaling to the LS1046A (quad A72 processor) or LS1088A (octal A53 core processor).

The LS1043A delivers a performance boost over dual-core Arm 32-bit products and continues the QorIQ legacy of I/O flexibility and integrated QUICC Engine® for legacy glue-less HDLC, TDM or Profibus support.

Layerscape processors are part of NXP's EdgeVerse™ [edge computing](#) platform.

LS1043A BD Block Diagram



View additional information for [Layerscape® 1043A and 1023A Processors](#).

Note: The information on this document is subject to change without notice.

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