



LS1021A-IoT Gateway Reference Design

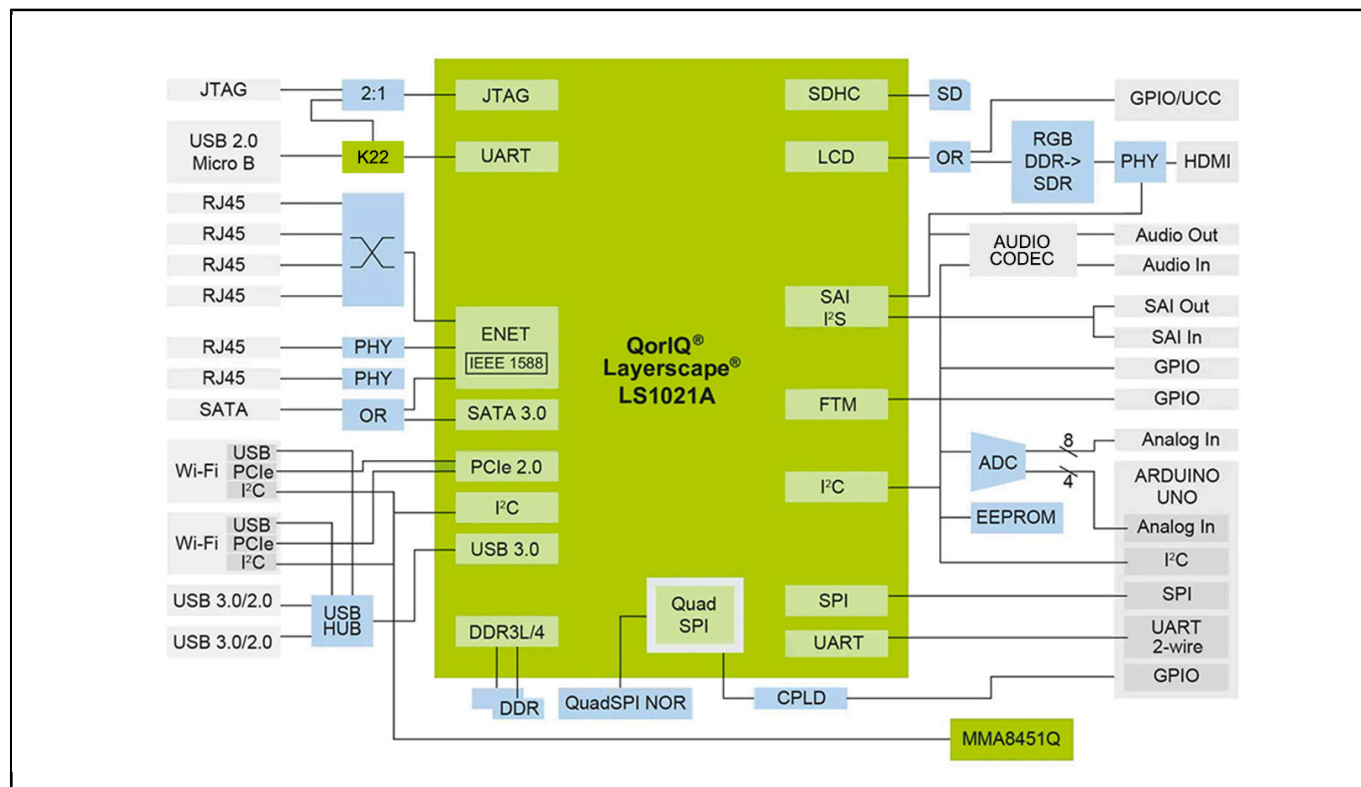
LS1021A-IoT

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The LS1021A-IoT gateway reference design based on the LS1021A processor is a purpose-built, small footprint hardware platform with a wide array of high-speed connectivity and low-speed serial interfaces to support secure delivery of IoT services for home or business.

[IoT Gateway Reference Design - Fact Sheet](#)

LS1021A-IoT Gateway Block Diagram



View additional information for [LS1021A-IoT Gateway Reference Design](#).

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