



# LPC840: 32-Bit Arm® Cortex®-M0+-Based Low-Cost MCU

## LPC84X

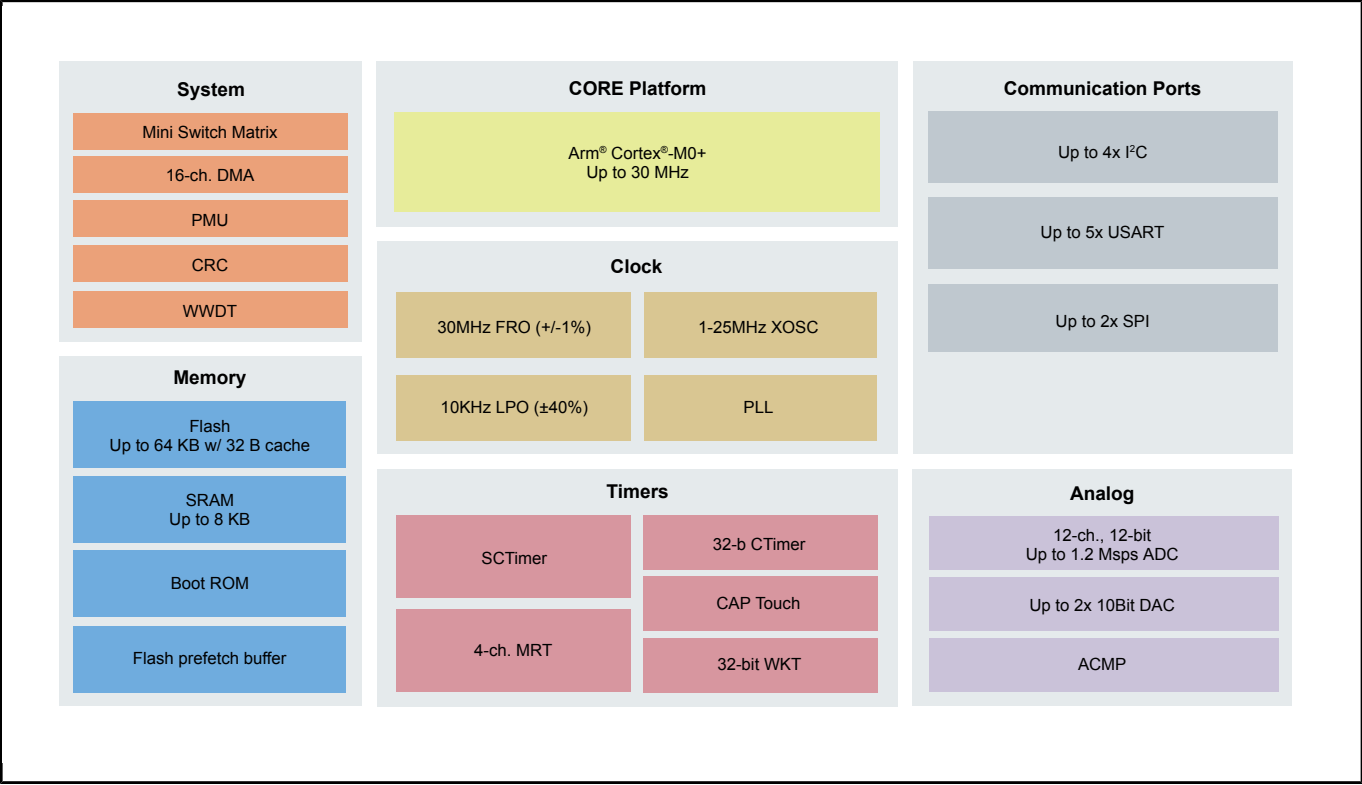
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Based on the Arm® Cortex®-M0+ core, LPC84x is a low-cost, 32-bit MCU family operating at frequencies of up to 30 MHz. The LPC84x MCU family supports up to 64 KB of flash memory and 16 KB of SRAM.

This family features exceptional power efficiency in the low-current mode using the FRO as the clock source. The peripheral complement of the LPC84x MCU family includes a CRC engine, four I2C-bus interfaces, up to five UARTs, up to two SPI interfaces, capacitive touch interface (enablement coming in Q3), one multi-rate timer, self-wake-up timer, SCTimer/PWM, one general purpose 32-bit counter/timer, a DMA, one 12-bit ADC, two 10-bit DACs, one analog comparator, function-configurable I/O ports through a switch matrix, an input pattern match engine, and up to 54 general-purpose I/O pins.

This device is fully supported by NXP's [MCUXpresso Software and Tools](#), a comprehensive and cohesive set of free software development tools for Kinetis, LPC and i.MX RT microcontrollers. MCUXpresso SDK also includes project files for Keil MDK and IAR EWARM.

# LPC84x MCU Block Diagram



View additional information for [LPC840: 32-Bit Arm® Cortex®-M0+-Based Low-Cost MCU](#).

**Note:** The information on this document is subject to change without notice.