



Arm9™ microcontroller with CAN, LIN, and USB

LPC2929FBD144

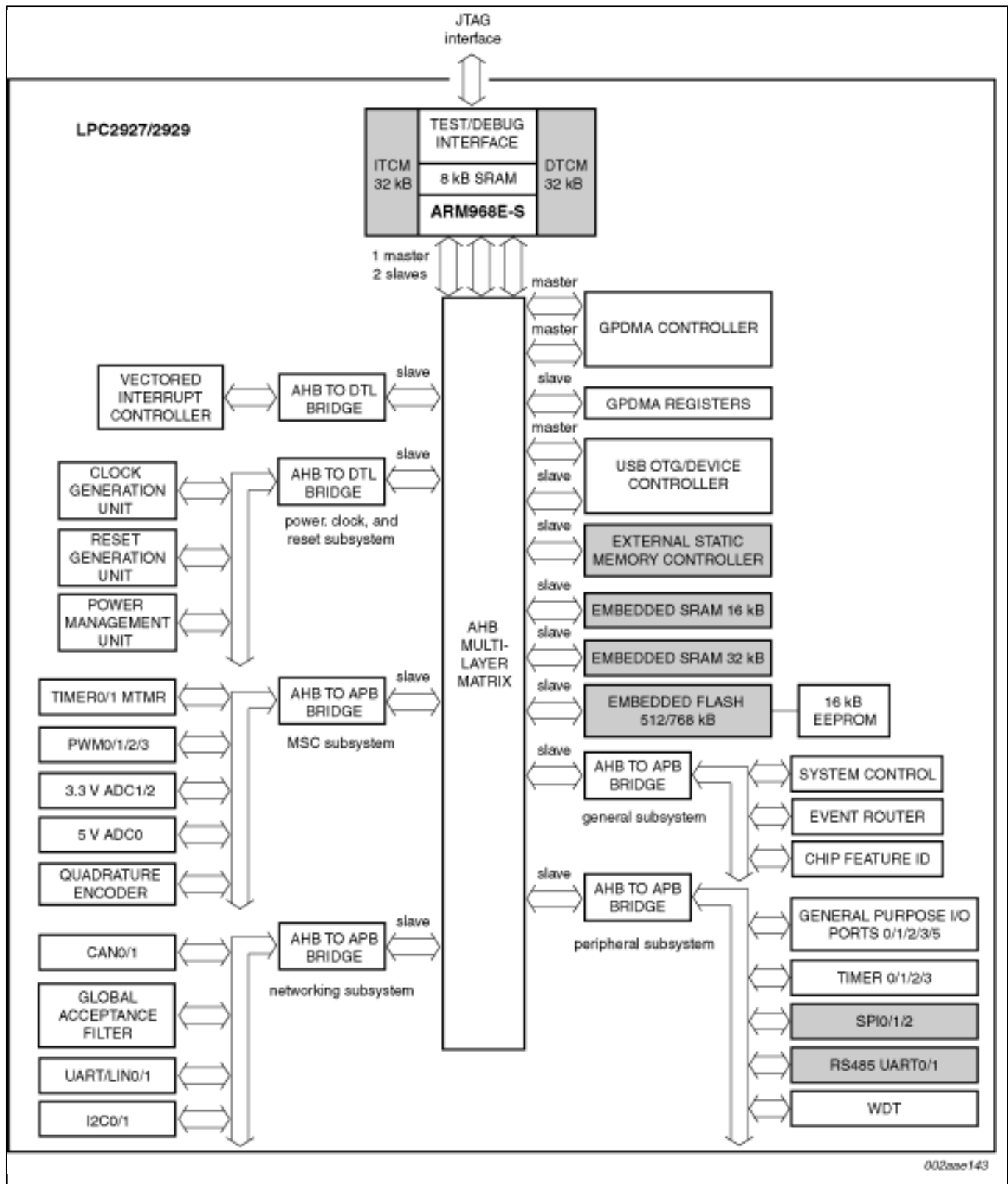
Not Recommended for New Designs

This page contains information on a product that is not recommended for new designs.

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The LPC2926/2927/2929 combine an ARM968E-S CPU core with two integrated TCM blocks operating at frequencies of up to 125 MHz, Full-speed USB 2.0 OTG and device controller, CAN and LIN, 56 kB SRAM, up to 768 kB flash memory, external memory interface, three 10-bit ADCs, and multiple serial and parallel interfaces in a single chip targeted at consumer, industrial and communication markets. To optimize system power consumption, the LPC2926/2927/2929 has a very flexible Clock Generation Unit (CGU) that provides dynamic clock gating and scaling.

Block diagram: LPC2927FBD144, LPC2929FBD144 Block Diagram



Note: The information on this document is subject to change without notice.

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