Layerscape® Access LA12xx Programmable Baseband Processor

LA12xx

Active

Contact NXP support or your sales representative for pricing and buying options.
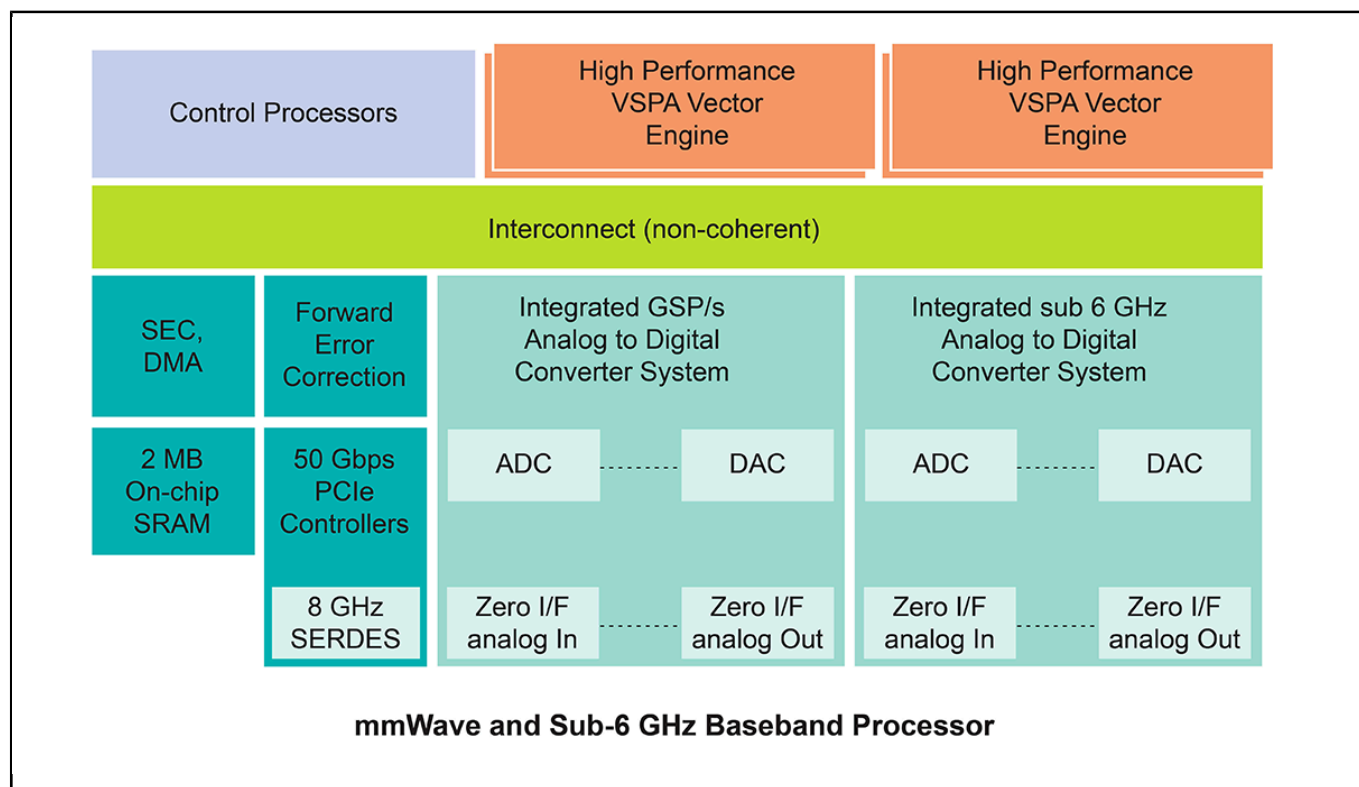
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The Layerscape Access LA12xx family of programmable baseband processors delivers high-performance digital signal processing capabilities, performance scalability and application flexibility for 5G sub-6 GHz and mmWave Fixed Wireless Access equipment such as Integrated Small Cells, O-RAN Distributed Unit and Radio Unit and Customer Premises Equipment (CPE). It can be also used in proprietary wireless applications such as defense and civilian radio systems and non-terrestrial networks (NTN).

The family is equipped with a unique combination of:

- High performance Vector Signal Processing Accelerators (VSPA)
- Forward Error Correction Accelerator (FECA)
- Integrated data converting subsystem (ADC/DAC)

For additional information and sample availability, contact NXP Support.

Layerscape Access LA12xx Block Diagram Block Diagram

Control Processors	High Performance VSPA Vector Engine	High Performance VSPA Vector Engine

Interconnect (non-coherent)

SEC, DMA	Forward Error Correction	Integrated GSP/s Analog to Digital Converter System		Integrated sub 6 GHz Analog to Digital Converter System	
2 MB On-chip SRAM	50 Gbps PCIe Controllers	ADC	DAC	ADC	DAC
	8 GHz SERDES	Zero I/F analog In	Zero I/F analog Out	Zero I/F analog In	Zero I/F analog Out

mmWave and Sub-6 GHz Baseband Processor

View additional information for Layerscape® Access LA12xx Programmable Baseband Processor.

Note: The information on this document is subject to change without notice.