



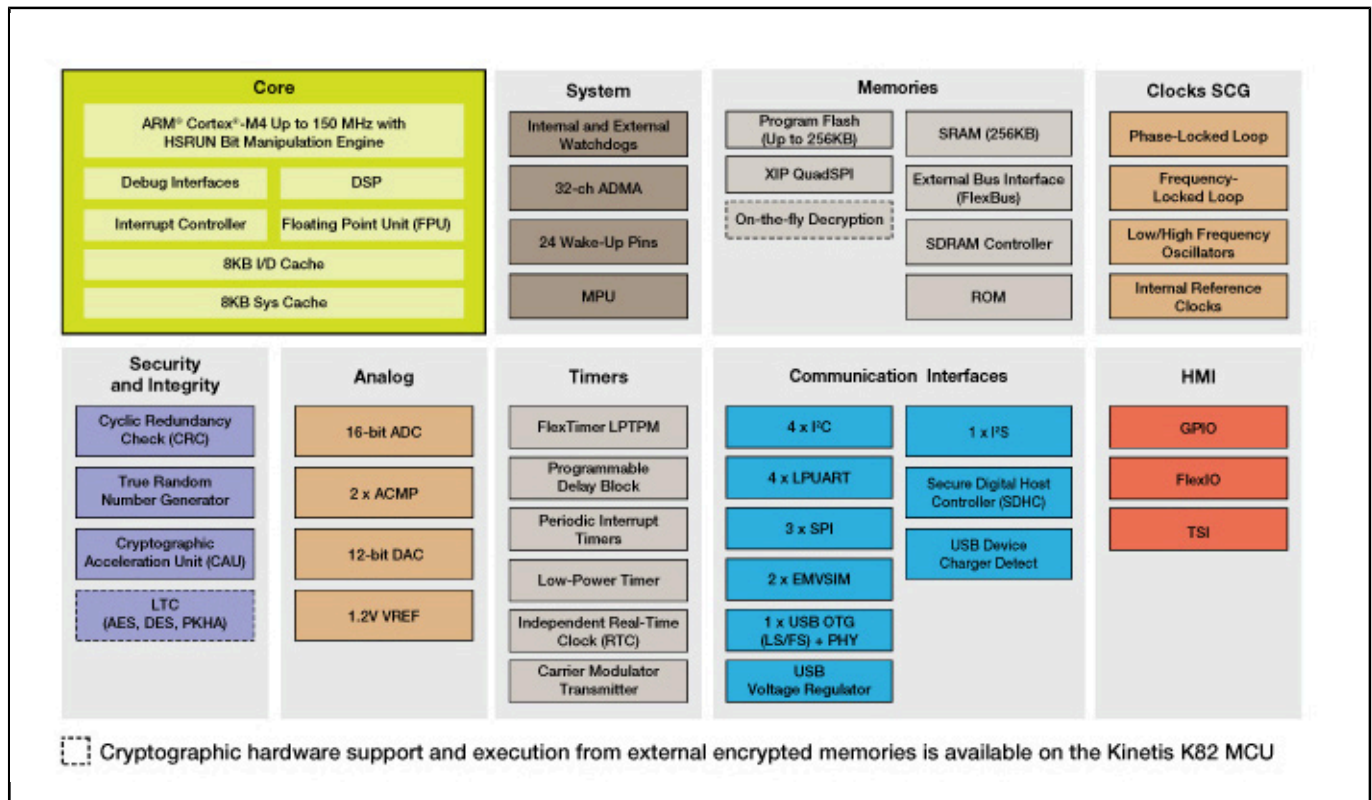
# **Kinetis® K82–150 MHz HW Cryptographic Co-Processor and QuadSPI Microcontrollers (MCUs) Based on Arm® Cortex®–M4 Core**

## **K82\_150**

Last Updated: Mar 5, 2025

The Kinetis® K82 MCU sub-family builds upon the Kinetis MCU portfolio with advanced security capabilities including boot ROM to support encrypted firmware updates. The Kinetis K82 MCU contains automatic decryption from external serial NOR flash memory, hardware AES acceleration with sideband attack protection, and hardware support for public key cryptography. These advancements are done while maintaining a high level of compatibility with the rest of the Kinetis MCU portfolio.

## Kinetis K80/K82 MCU Block Diagram Block Diagram



View additional information for [Kinetis® K82-150 MHz HW Cryptographic Co-Processor and QuadSPI Microcontrollers \(MCUs\) Based on Arm® Cortex®-M4 Core](#).

**Note:** The information on this document is subject to change without notice.

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