Kinetis® K60/61-120–150 MHz, Mixed-Signal Integration and Anti-Tamper Microcontrollers based on Arm® Cortex®-M4 Core
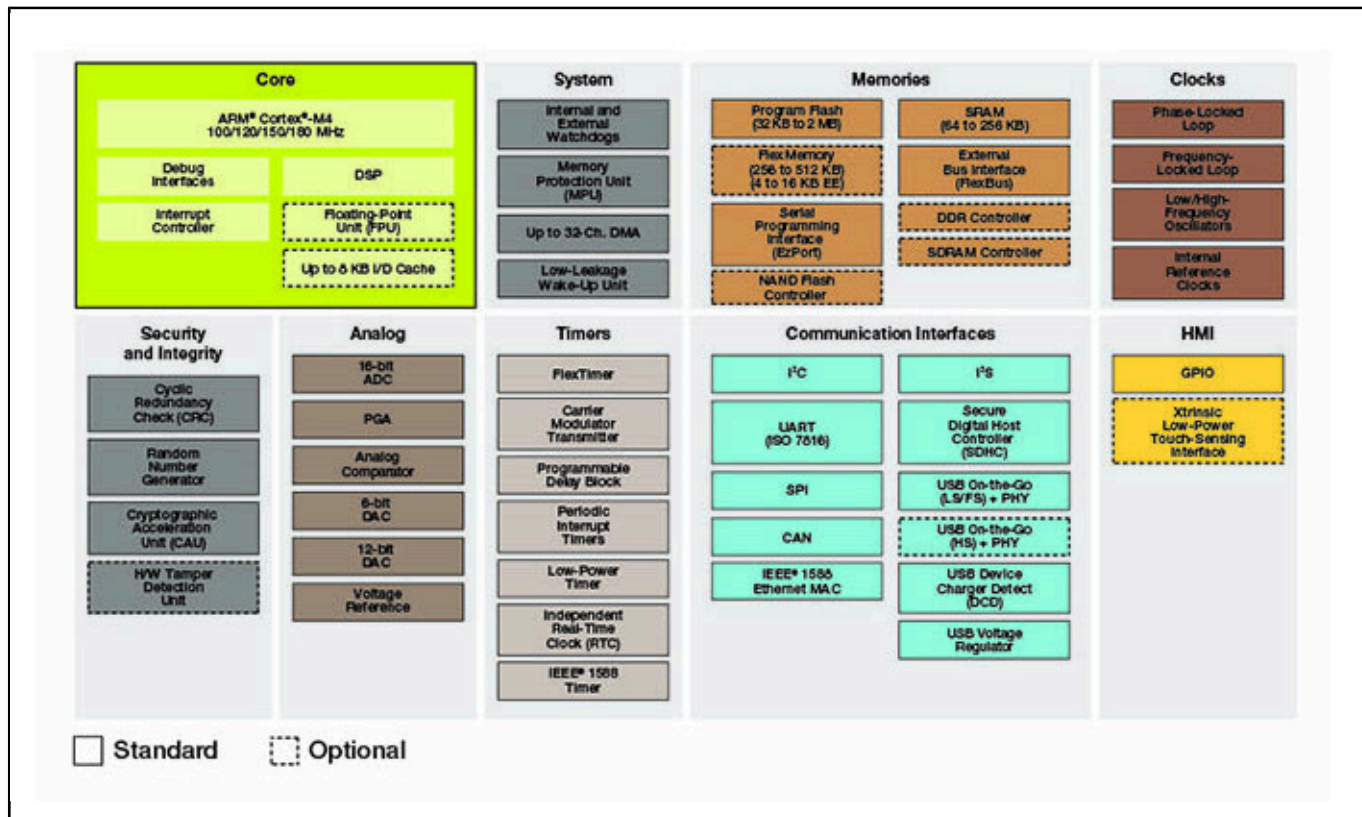
K60_120

Last Updated: Mar 5, 2025

Performance efficient Kinetis® K60/61 MCUs offer outstanding computational power for control algorithms, sensor data processing, and audio processing.

- Delivers optimized low power while providing significant bill-of-materials savings through smart on-chip integration
- Includes options for security encryption and tamper detection
- Provides scalability with various levels of integration, offering a rich suite of analog, communication, timing and control peripherals to accommodate a wide range of requirements
- Shares the comprehensive enablement and scalability of the Kinetis portfolio
- Promotes faster time-to-market with comprehensive development tools, software and enablement

Please contact your local NXP representative to download the K61 Security Data sheet and Reference Manual documents (under NDA).

Kinetis K6x MCU Family Block Diagram Block Diagram

View additional information for Kinetis® K60/61-120–150 MHz, Mixed-Signal Integration and Anti-Tamper Microcontrollers based on Arm® Cortex®-M4 Core.

Note: The information on this document is subject to change without notice.

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