



Kinetis K21-120 MHz, Full-Speed USB, Anti-Tamper Microcontrollers based on Arm[®] Cortex[®]-M4

K21_120

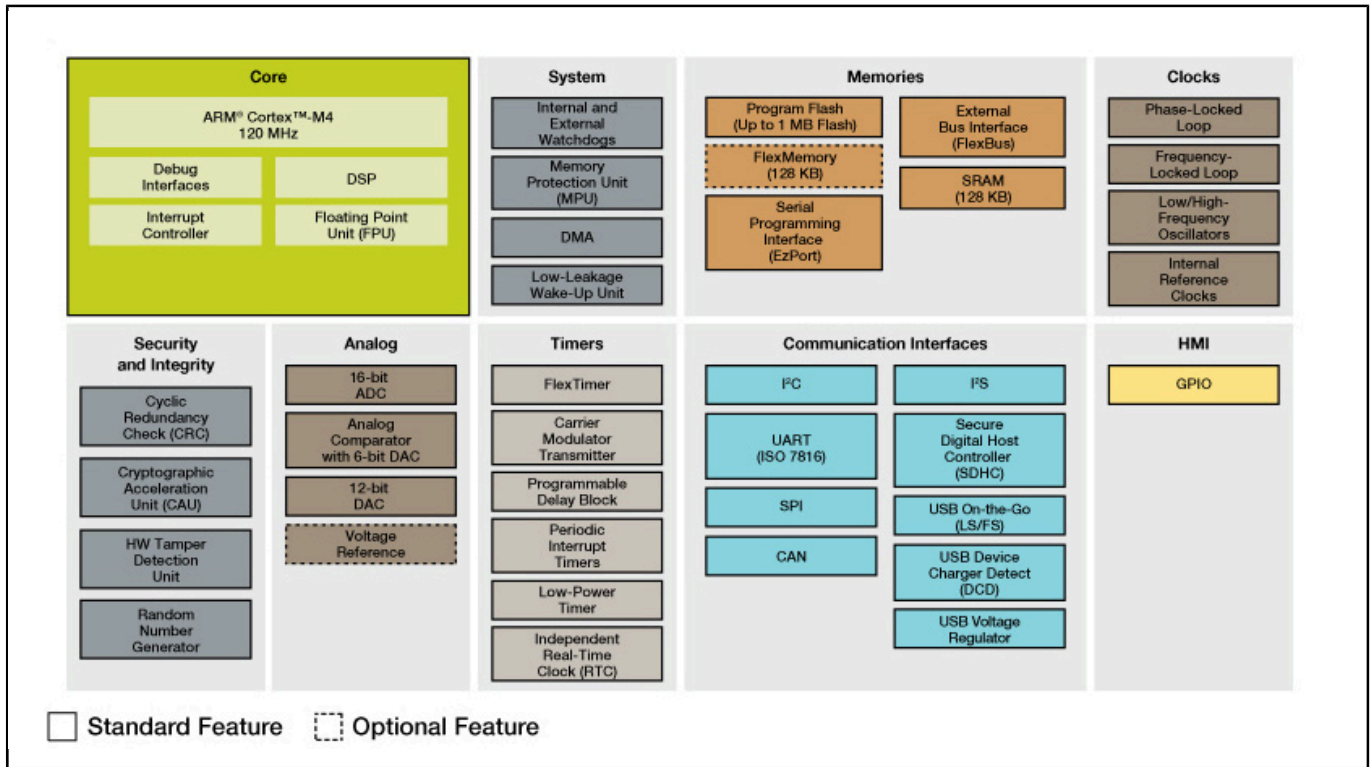
Last Updated: Jan 16, 2023

The Kinetis K21 120 MHz MCU family targets cost-sensitive applications requiring low-power functionality.

- Features hardware encryption and tamper detection
- Includes a rich suite of analog, communication, timing and control peripherals to accommodate a wide range of requirements
- Maximizes board space and enhances performance with minimum-length interconnections
- Provides performance efficiency with low power
- Delivers significant BOM savings through smart on-chip integration
- Shares the comprehensive enablement and scalability of the Kinetis portfolio

Please contact your local NXP representative to download the K21 Security Data sheet and Reference Manual documents (under NDA).

Kinetis K21 MCU Family Block Diagram Block Diagram



View additional information for [Kinetis K21-120 MHz, Full-Speed USB, Anti-Tamper Microcontrollers based on Arm® Cortex®-M4](#).

Note: The information on this document is subject to change without notice.

www.nxp.com

NXP and the NXP logo are trademarks of NXP B.V. All other product or service names are the property of their respective owners. The related technology may be protected by any or all of patents, copyrights, designs and trade secrets. All rights reserved. © 2024 NXP B.V.