Kinetis® K02-100 MHz, Microcontrollers (MCUs) with Optimized Features Based on Arm® Cortex®-M4 Core
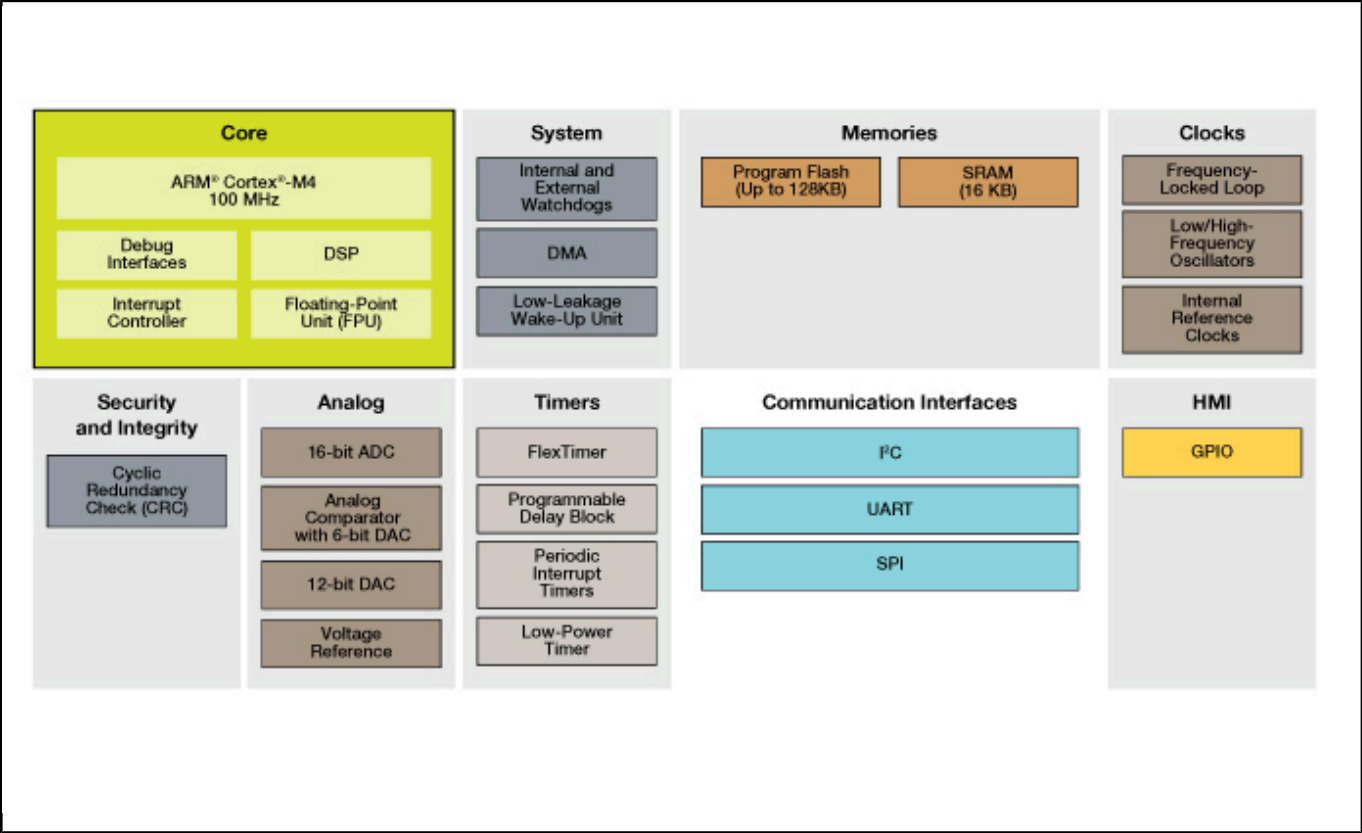
K02_100

Last Updated: Mar 4, 2025

The entry-level Kinetis® K02 100 MHz MCU offers a streamlined solution with high performance processing efficiency for very low-power applications.

- Offers low dynamic power consumption and advanced static current consumption with more than 10 flexible low-power modes
- Helps extend battery life and reduce overall board power supply requirements
- Delivers outstanding computational power for control algorithms, sensor data processing, audio processing while increasing math accuracy and reducing code size
- Shares the comprehensive scalability of the Kinetis portfolio
- Enables faster time-to-market with comprehensive enablement solutions

Kinetis K02_100 MCUs Block Diagram Block Diagram

Core	System	Memories	Clocks
ARM® Cortex®-M4 100 MHz	Internal and External Watchdogs	Program Flash (Up to 128KB) SRAM (16 KB)	Frequency-Locked Loop
Debug Interfaces DSP	DMA		Low/High-Frequency Oscillators
Interrupt Controller Floating-Point Unit (FPU)	Low-Leakage Wake-Up Unit		Internal Reference Clocks

Security and Integrity	Analog	Timers	Communication Interfaces	HMI
Cyclic Redundancy Check (CRC)	16-bit ADC	FlexTimer	I²C	GPIO
	Analog Comparator with 6-bit DAC	Programmable Delay Block	UART	
	12-bit DAC	Periodic Interrupt Timers	SPI	
	Voltage Reference	Low-Power Timer		

View additional information for Kinetis® K02-100 MHz, Microcontrollers (MCUs) with Optimized Features Based on Arm® Cortex®-M4 Core.

Note: The information on this document is subject to change without notice.