



Generic Timer Module Configuration Tool for MPC5xxx MCUs

GTM_CFG_TOOL

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The NXP Generic Timer Module (GTM) configuration tool assists with the development of software for NXP MCUs that contain the GTM IP by allowing the user to configure the GTM through an Eclipse-based GUI and then using the settings to automatically generate initialization code for the GTM which is run once by the CPU upon initialization.

The GTM configuration tool provides a GUI to configure the individual register sets within the GTM with the target goal of supporting specialized timer input and output signals in support of specific application goals such as 4, 5, 6 and 8 cylinder applications for powertrain, transmission and motor control, including angle clock generation.

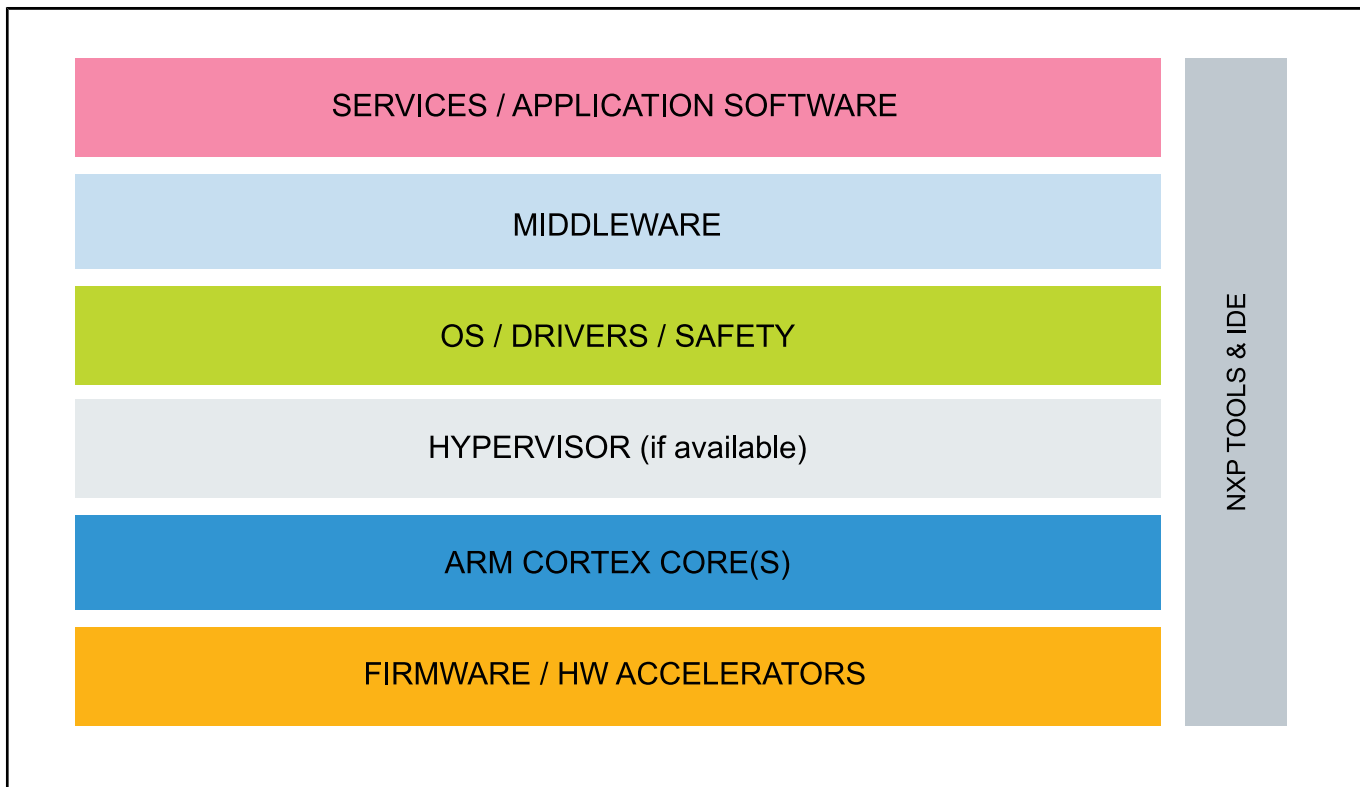
Use the GTM configuration tool to save time and become an expert on the GTM IP.

The screenshot displays the GTM Configuration Tool Browser, which is used for configuring the GTM (Global Time Measurement) system. The interface is divided into two main sections: a block diagram on the left and a configuration window on the right.

Block Diagram (Left): The diagram illustrates the GTM architecture. At the top, the **AEIMux** and **ICM** blocks are connected to the **MON** and **TBU & CMU** blocks. The **TBU & CMU** block is connected to the **DPLL & MAP** block, which in turn is connected to the **TIM** block. The **TIM** block is connected to the **SPE** block, which is connected to the **TOM** block. The **TOM** block is connected to the **CMP** block, which is connected to the **ATOM** block. The **ATOM** block is connected to the **PSM** block, which is connected to the **BRC** block. The **BRC** block is connected to the **MCS Core(s)** block. The **ARU** block is connected to the **MON** and **TBU & CMU** blocks.

Configuration Window (Right): The window shows the configuration for the **WISSE2** signal processor. It includes sections for **General Configuration**, **WISSE2 signal configuration**, **Address pointer**, **Test and input WISSE2**, **WISSE2 values**, **Test data**, and **Incremental location**. The **General Configuration** section includes fields for **WISSE2 mode select**, **WISSE2 reference mode**, **WISSE2 offset size of both ranges**, and **WISSE2 input data position**. The **WISSE2 signal configuration** section includes fields for **WISSE2 input select**, **WISSE2 input 1**, **WISSE2 input 2**, **WISSE2 input 3**, **WISSE2 input 4**, **WISSE2 input 5**, **WISSE2 input 6**, **WISSE2 input 7**, **WISSE2 input 8**, **WISSE2 input 9**, **WISSE2 input 10**, **WISSE2 input 11**, **WISSE2 input 12**, **WISSE2 input 13**, **WISSE2 input 14**, **WISSE2 input 15**, **WISSE2 input 16**, **WISSE2 input 17**, **WISSE2 input 18**, **WISSE2 input 19**, **WISSE2 input 20**, **WISSE2 input 21**, **WISSE2 input 22**, **WISSE2 input 23**, **WISSE2 input 24**, **WISSE2 input 25**, **WISSE2 input 26**, **WISSE2 input 27**, **WISSE2 input 28**, **WISSE2 input 29**, **WISSE2 input 30**, **WISSE2 input 31**, **WISSE2 input 32**, **WISSE2 input 33**, **WISSE2 input 34**, **WISSE2 input 35**, **WISSE2 input 36**, **WISSE2 input 37**, **WISSE2 input 38**, **WISSE2 input 39**, **WISSE2 input 40**, **WISSE2 input 41**, **WISSE2 input 42**, **WISSE2 input 43**, **WISSE2 input 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Automotive General Block Diagram Block Diagram



View additional information for [Generic Timer Module Configuration Tool for MPC5xxx MCUs](#).

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