



# elQ<sup>®</sup> Inference with TensorFlow<sup>™</sup> Lite Micro

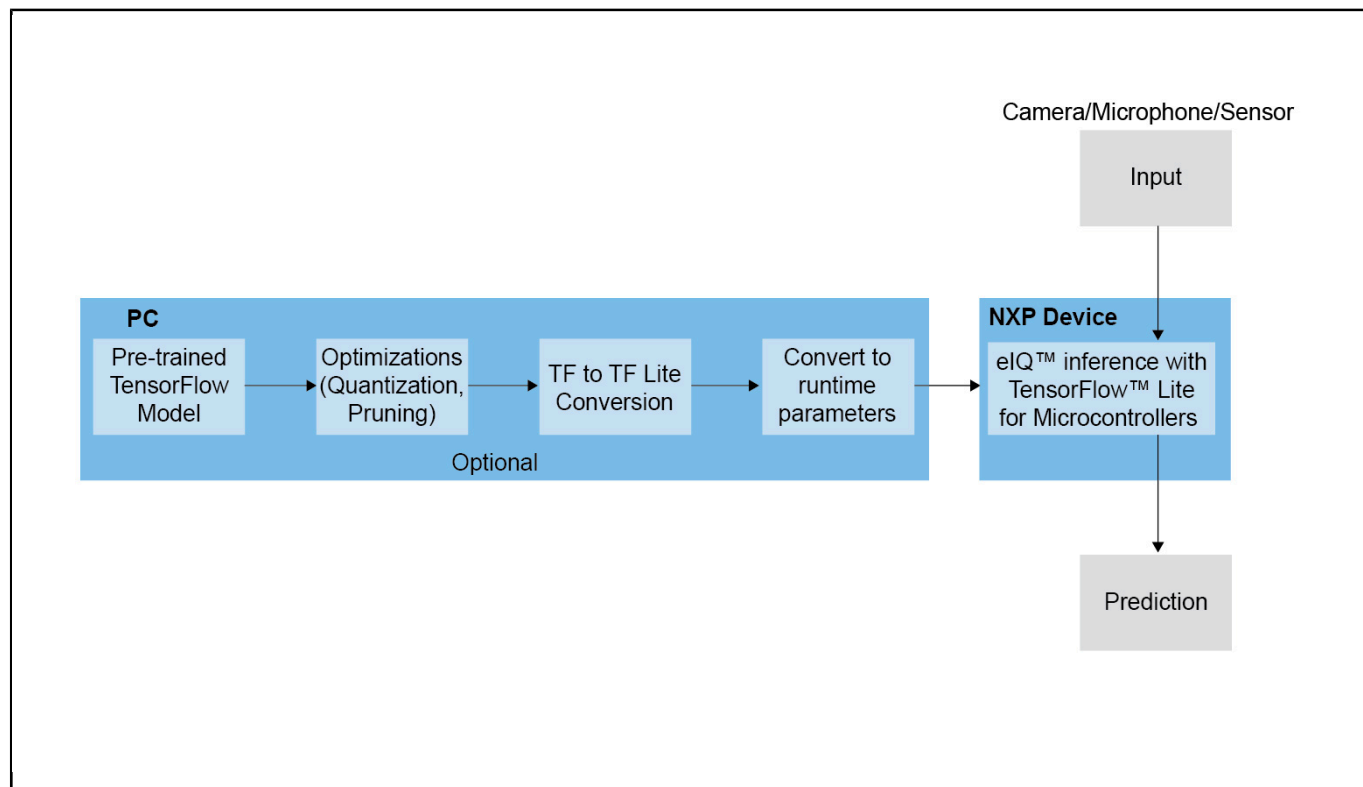
## EIQ-TFLITE-MICRO

Last Updated: Apr 16, 2024

The elQ inference with TensorFlow<sup>™</sup> Lite for Microcontrollers (TF Micro) is optimized for running machine learning models on resource constrained devices, including NXP's i.MX RT crossover MCUs.

Faster and smaller than the traditional TensorFlow Lite open source platform for machine learning, this TF Micro implementation enables inferencing at the edge with lower latency and smaller binary size.

## eIQ® TensorFlow Lite for MCUs Block Diagram



View additional information for [eIQ® Inference with TensorFlow™ Lite Micro](#).

**Note:** The information on this document is subject to change without notice.

**www.nxp.com**

NXP and the NXP logo are trademarks of NXP B.V. All other product or service names are the property of their respective owners. The related technology may be protected by any or all of patents, copyrights, designs and trade secrets. All rights reserved. © 2025 NXP B.V.