



# **QorIQ® B4860 Baseband Processor**

## **B4860**

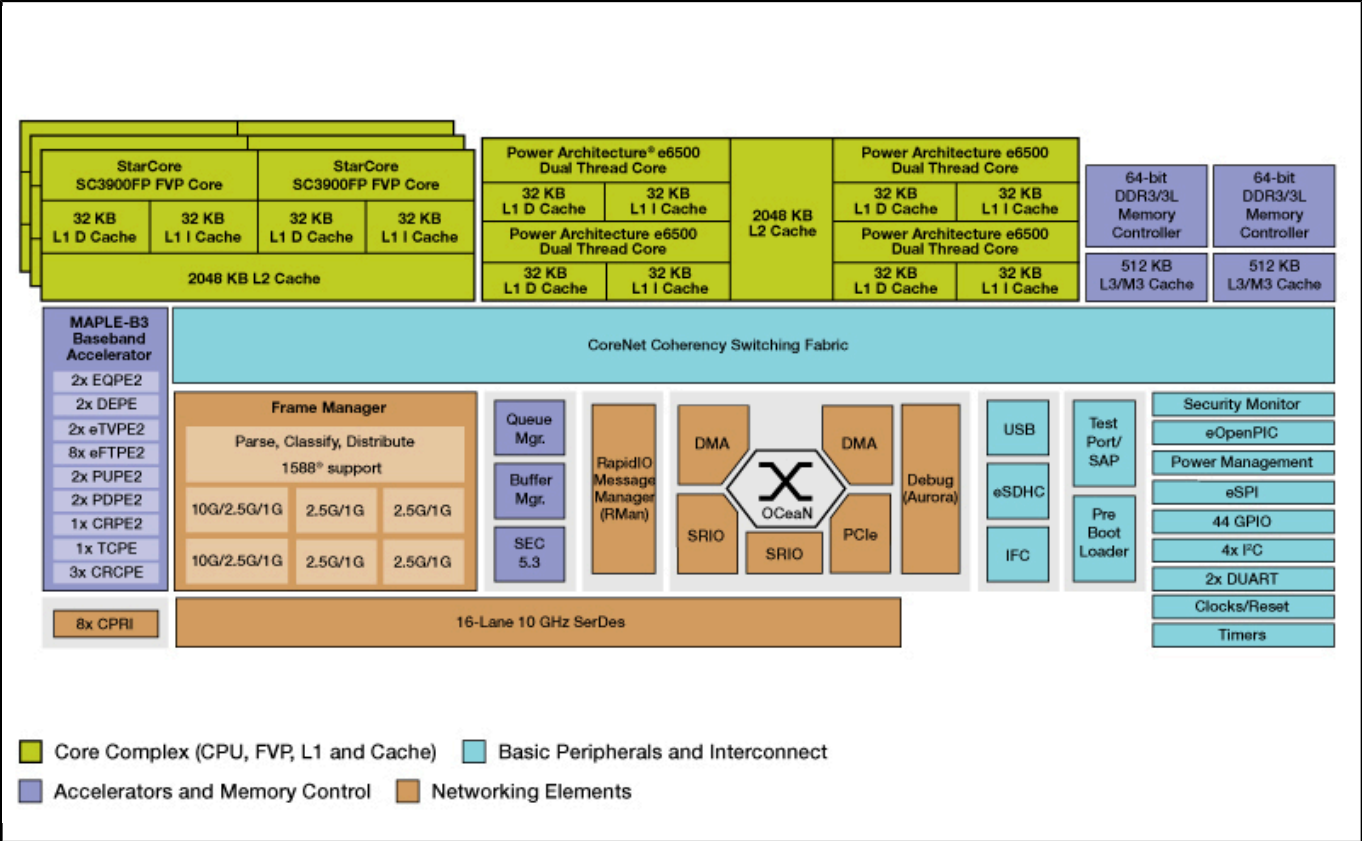
Last Updated: Dec 16, 2024

The QorIQ® Qonverge B4860 system-on-chip is designed for next-generation, multi-standard wireless base stations. Based on 28 nm process technology, the B4860 offers unequaled throughput and capacity and integrates a compelling blend of efficient and high-performance programmable cores, as well as application-specific accelerators to deliver optimal power and cost. It targets macrocell base station designs for broadband wireless infrastructure and builds upon the proven success of our multicore CPUs and DSPs in wireless infrastructure markets.

The B4860 combines four 64-bit, dual-threaded e6500 cores built on Power Architecture® technology, six StarCore® SC3900FP Fixed/Floating-Point DSP cores and MAPLE-B baseband acceleration processing engines. It is designed to adapt to the rapidly changing and expanding standards of LTE (FDD and TDD), LTE-Advanced including 3GPP LTE Rel.10/11 and WCDMA, and supports different standards simultaneously.



B4860 BD IMG Block Diagram



View additional information for [QorIQ® B4860 Baseband Processor](#).

**Note:** The information on this document is subject to change without notice.

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