



i.MX 8X Family – Arm® Cortex®-A35, 3D Graphics, 4K Video, DSP, Error Correcting Code on DDR

i.MX8X

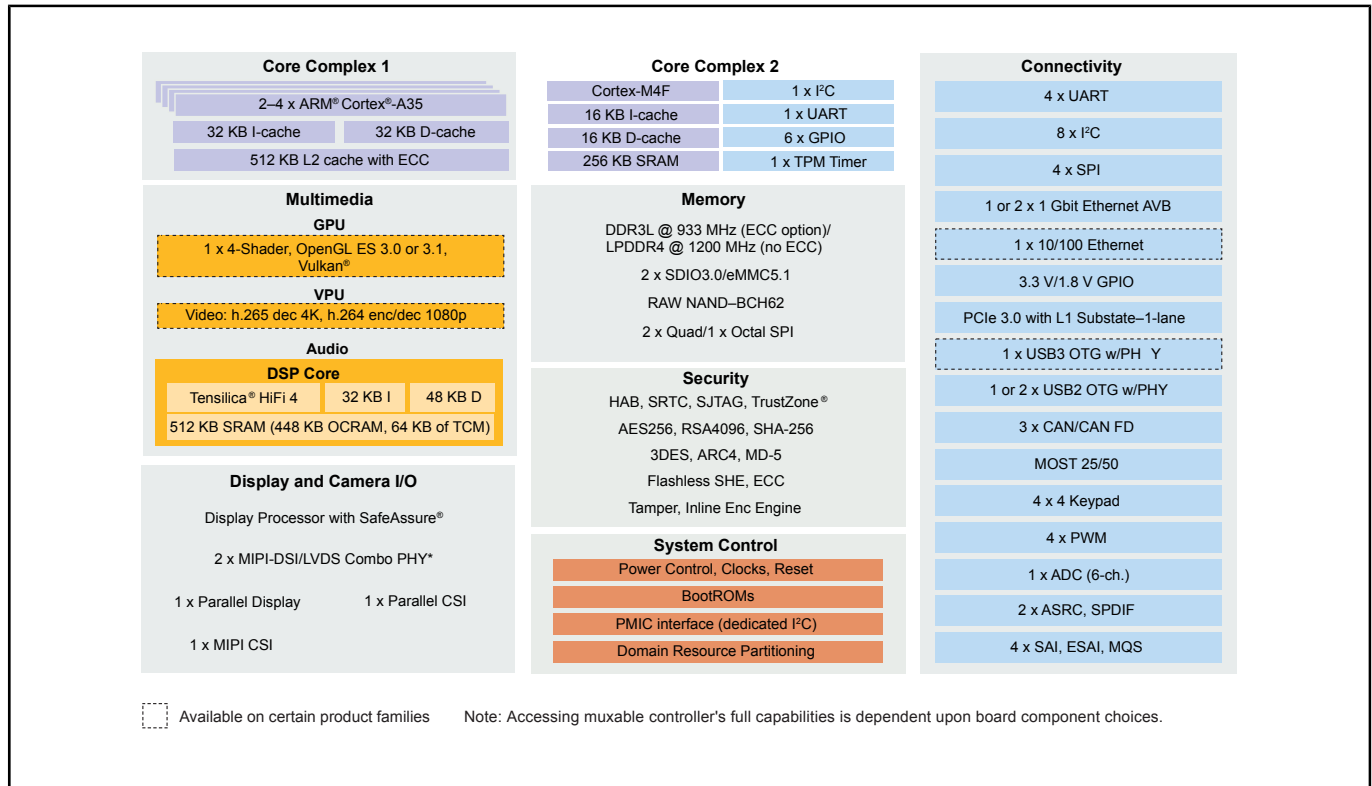
Last Updated: Apr 11, 2024

Extending the scalable range of the i.MX 8 series, the i.MX 8X family is comprised of common subsystems and architecture from the higher-end i.MX 8 family, establishing a range of cost-performance scaling with pin-compatible options and a high level of software reuse. Built with high-level integration to support graphics, video, image processing, audio and voice functions, the i.MX 8X processor family is ideal for efficient performance requirements. Example applications include industrial automation and control, HMI, robotics, building control, automotive cluster, display audio, infotainment and telematics applications.

i.MX 8 applications processors are part of NXP's EdgeVerse™ [edge computing](#) platform.

For additional information and sample availability, contact [NXP support](#).

i.MX 8X Processors Block Diagram



View additional information for [i.MX 8X Family – Arm® Cortex®-A35, 3D Graphics, 4K Video, DSP, Error Correcting Code on DDR](#).

Note: The information on this document is subject to change without notice.

www.nxp.com

NXP and the NXP logo are trademarks of NXP B.V. All other product or service names are the property of their respective owners. The related technology may be protected by any or all of patents, copyrights, designs and trade secrets. All rights reserved. © 2024 NXP B.V.